



# SPEC® CFP2006 Result

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## Lenovo Group Limited

Lenovo ThinkServer RD550 (Intel Xeon E5-2680 v3, 2.50 GHz)

SPECfp®2006 = 110

SPECfp\_base2006 = 106

CPU2006 license: 9017

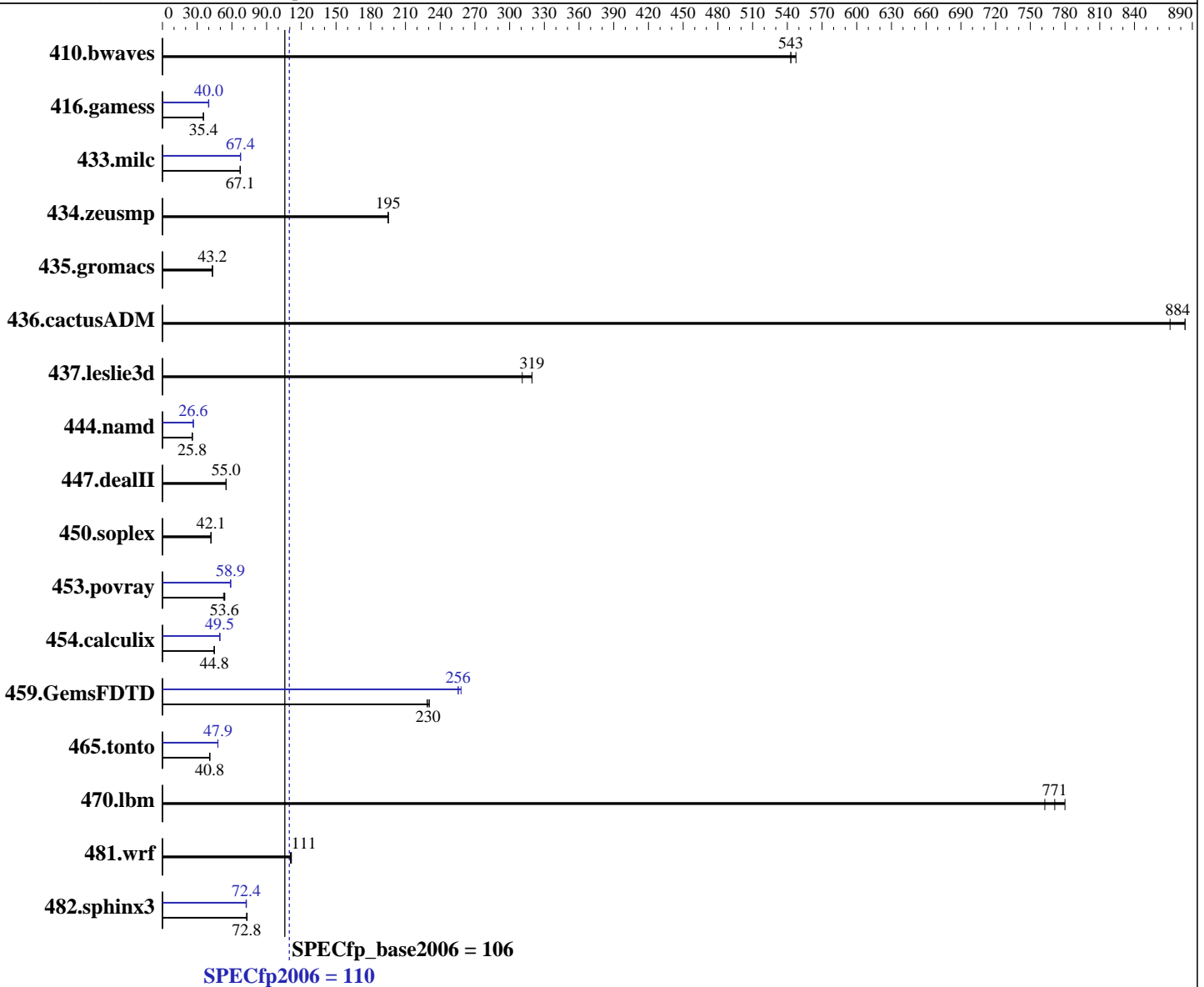
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014



### Hardware

CPU Name: Intel Xeon E5-2680 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.0	543	<u>25.0</u>	<u>543</u>	24.8	547	25.0	543	<u>25.0</u>	<u>543</u>	24.8	547
416.gamess	555	35.3	<u>553</u>	<u>35.4</u>	552	35.4	489	40.0	493	39.7	<u>489</u>	<u>40.0</u>
433.milc	137	67.1	136	67.3	<u>137</u>	<u>67.1</u>	<u>136</u>	<u>67.4</u>	136	67.5	136	67.4
434.zeusmp	<u>46.6</u>	<u>195</u>	46.6	195	46.6	195	<u>46.6</u>	<u>195</u>	46.6	195	46.6	195
435.gromacs	<u>165</u>	<u>43.2</u>	165	43.2	165	43.2	<u>165</u>	<u>43.2</u>	165	43.2	165	43.2
436.cactusADM	<u>13.5</u>	<u>884</u>	13.5	884	13.7	871	<u>13.5</u>	<u>884</u>	13.5	884	13.7	871
437.leslie3d	<u>29.4</u>	<u>319</u>	30.2	311	29.4	319	<u>29.4</u>	<u>319</u>	30.2	311	29.4	319
444.namd	<u>311</u>	<u>25.8</u>	311	25.8	311	25.8	301	26.6	<u>301</u>	<u>26.6</u>	301	26.7
447.dealII	208	55.0	<u>208</u>	<u>55.0</u>	208	54.9	208	55.0	<u>208</u>	<u>55.0</u>	208	54.9
450.soplex	198	42.1	200	41.7	<u>198</u>	<u>42.1</u>	198	42.1	200	41.7	<u>198</u>	<u>42.1</u>
453.povray	<u>99.2</u>	<u>53.6</u>	98.7	53.9	101	52.8	90.7	58.7	90.2	59.0	<u>90.3</u>	<u>58.9</u>
454.calculix	185	44.7	184	44.8	<u>184</u>	<u>44.8</u>	166	49.6	<u>167</u>	<u>49.5</u>	167	49.5
459.GemsFDTD	46.4	229	<u>46.2</u>	<u>230</u>	46.0	231	<u>41.5</u>	<u>256</u>	41.5	256	41.1	258
465.tonto	<u>241</u>	<u>40.8</u>	240	41.0	242	40.7	205	47.9	206	47.8	<u>205</u>	<u>47.9</u>
470.lbm	18.0	763	<u>17.8</u>	<u>771</u>	17.6	780	18.0	763	<u>17.8</u>	<u>771</u>	17.6	780
481.wrf	<u>100</u>	<u>111</u>	101	111	100	111	<u>100</u>	<u>111</u>	101	111	100	111
482.sphinx3	<u>268</u>	<u>72.8</u>	266	73.2	268	72.8	<u>269</u>	<u>72.4</u>	270	72.2	268	72.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Cluster On Die set to Disabled  
Early Snoop set to Disabled  
Performance Profile set to Custom  
ClE Support set to Disabled  
Core C3 set to Disabled  
Core C6 set to Disabled  
Thermal Profile set to High Fan Speed  
Memory Power Savings set to Disabled  
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818

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### Platform Notes (Continued)

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on RD550 Fri Nov 7 17:01:14 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      264411780 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RD550 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 7 17:00
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  730G  42G   651G   7% /
```

Additional information from dmidecode:

BIOS LENOVO PB1TS110 10/06/2014

Memory:

16x 16 GB

8x NO DIMM NO DIMM

16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)

RD550 support 4 channels and 12 DIMMs per processor, total 8 channels and  
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## Platform Notes (Continued)

24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD550-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-RD550-revA.xml>

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