



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>\_rate2006 = 568

Huawei RH1288A V2 (Intel Xeon E5-2658 v2)

SPECfp\_rate\_base2006 = 554

CPU2006 license: 3175

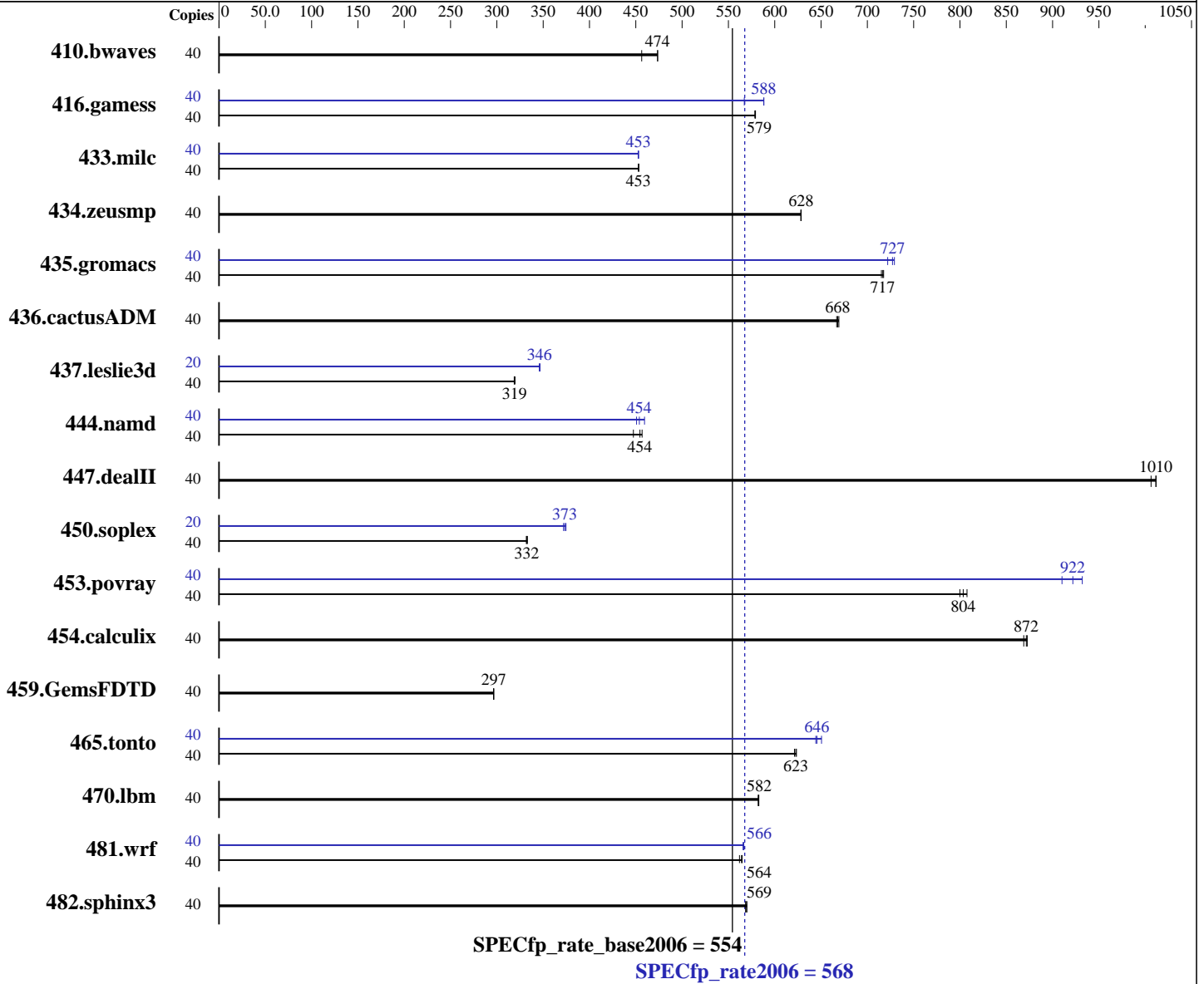
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2658 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-11, ECC)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	1148	474	<b><u>1148</u></b>	<b><u>474</u></b>	1191	456	40	1148	474	<b><u>1148</u></b>	<b><u>474</u></b>	1191	456
416.gamess	40	1352	579	1354	579	<b><u>1352</u></b>	<b><u>579</u></b>	40	1381	567	<b><u>1331</u></b>	<b><u>588</u></b>	1331	588
433.milc	40	811	453	810	453	<b><u>811</u></b>	<b><u>453</u></b>	40	811	453	<b><u>811</u></b>	<b><u>453</u></b>	811	453
434.zeusmp	40	579	628	<b><u>579</u></b>	<b><u>628</u></b>	579	629	40	579	628	<b><u>579</u></b>	<b><u>628</u></b>	579	629
435.gromacs	40	399	715	398	718	<b><u>399</u></b>	<b><u>717</u></b>	40	392	729	<b><u>393</u></b>	<b><u>727</u></b>	396	722
436.cactusADM	40	714	669	<b><u>716</u></b>	<b><u>668</u></b>	717	667	40	714	669	<b><u>716</u></b>	<b><u>668</u></b>	717	667
437.leslie3d	40	<b><u>1179</u></b>	<b><u>319</u></b>	1180	319	1176	320	20	<b><u>543</u></b>	<b><u>346</u></b>	542	347	543	346
444.namd	40	717	447	<b><u>706</u></b>	<b><u>454</u></b>	702	457	40	<b><u>707</u></b>	<b><u>454</u></b>	698	459	712	451
447.dealII	40	<b><u>452</u></b>	<b><u>1010</u></b>	452	1010	455	1010	40	<b><u>452</u></b>	<b><u>1010</u></b>	452	1010	455	1010
450.soplex	40	1002	333	<b><u>1004</u></b>	<b><u>332</u></b>	1005	332	20	448	372	445	375	<b><u>447</u></b>	<b><u>373</u></b>
453.povray	40	<b><u>265</u></b>	<b><u>804</u></b>	263	808	266	800	40	<b><u>231</u></b>	<b><u>922</u></b>	234	910	228	932
454.calculix	40	378	873	<b><u>379</u></b>	<b><u>872</u></b>	380	869	40	378	873	<b><u>379</u></b>	<b><u>872</u></b>	380	869
459.GemsFDTD	40	1431	297	1431	297	<b><u>1431</u></b>	<b><u>297</u></b>	40	1431	297	1431	297	<b><u>1431</u></b>	<b><u>297</u></b>
465.tonto	40	634	621	631	623	<b><u>632</u></b>	<b><u>623</u></b>	40	611	644	<b><u>610</u></b>	<b><u>646</u></b>	605	651
470.lbm	40	943	583	<b><u>944</u></b>	<b><u>582</u></b>	944	582	40	943	583	<b><u>944</u></b>	<b><u>582</u></b>	944	582
481.wrf	40	<b><u>792</u></b>	<b><u>564</u></b>	791	565	795	562	40	789	567	789	566	<b><u>789</u></b>	<b><u>566</u></b>
482.sphinx3	40	1368	570	1371	569	<b><u>1370</u></b>	<b><u>569</u></b>	40	1368	570	1371	569	<b><u>1370</u></b>	<b><u>569</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%

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Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013

## Platform Notes (Continued)

Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on localhost Mon Sep 15 16:36:53 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2658 v2 @ 2.40GHz  
2 "physical id"s (chips)  
40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 10  
siblings : 20  
physical 0: cores 0 1 2 3 4 8 9 10 11 12  
physical 1: cores 0 1 2 3 4 8 9 10 11 12  
cache size : 25600 KB

From /proc/meminfo  
MemTotal: 132103760 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux localhost 2.6.32-431.el6.x86\_64 #1 SMP Sun Nov 10 22:19:54 EST 2013  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Sep 15 03:31

SPEC is set to: /spec  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sdal ext4 439G 81G 336G 20% /

Additional information from dmidecode:  
BIOS Insyde Corp. RMIBV388 08/09/2014  
Memory:  
8x Samsung M393B2G70QH0-CMA 16 GB 1866 MHz 2 rank

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
The Huawei RH2288A V2 and Huawei RH1288A V2 are electronically equivalent.  
The results have been measured on a Huawei RH2288A V2 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

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## Peak Portability Flags (Continued)

437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -inline-level=0 -scalar-rep-

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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