



SPEC® CINT2006 Result

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Lenovo Group Limited

Lenovo System x iDataPlex dx360 M4
(Intel Xeon E5-2637 v2, 3.50 GHz)

SPECint®_rate2006 = 429

SPECint_rate_base2006 = 412

CPU2006 license: 9017

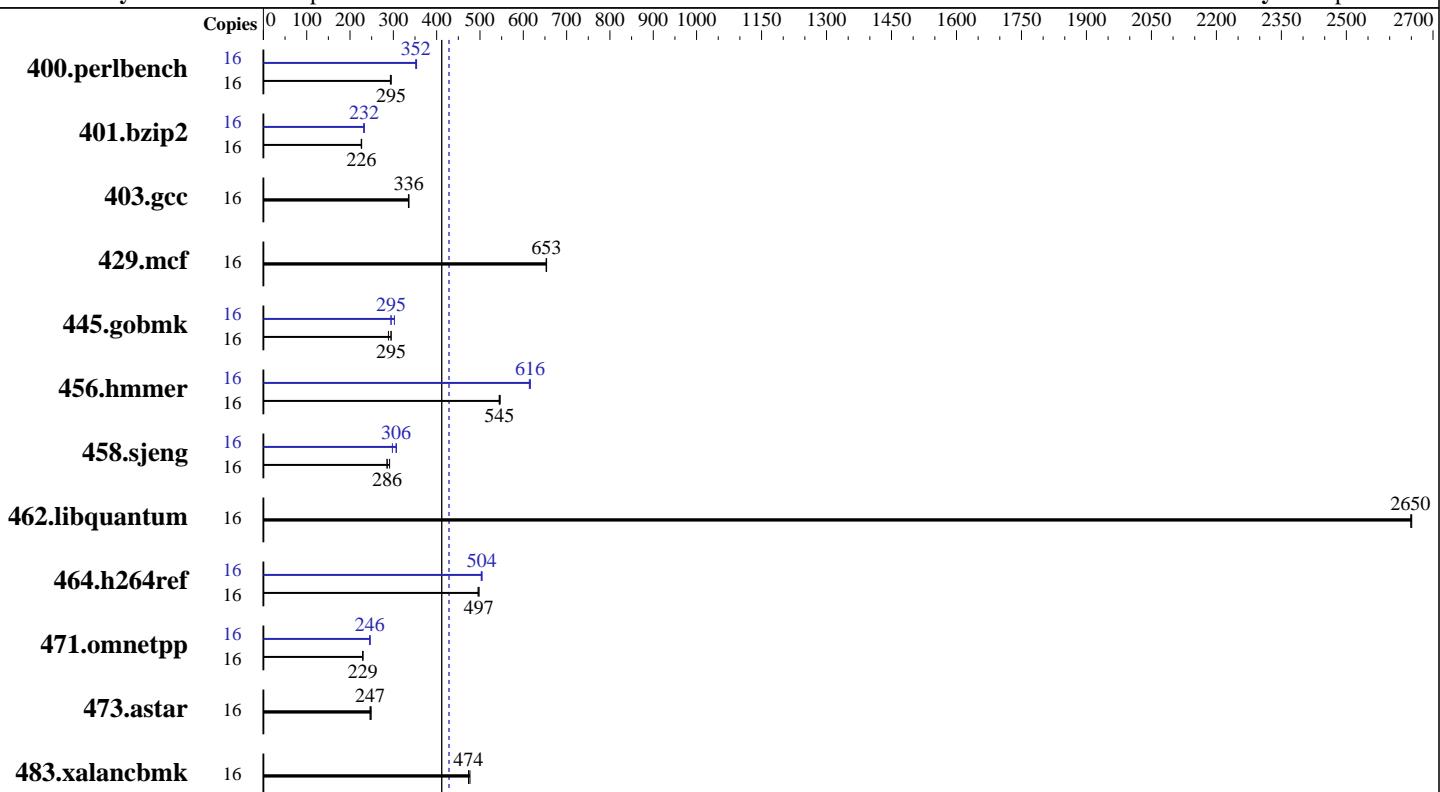
Test date: Dec-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Dec-2013

Tested by: IBM Corporation

Software Availability: Sep-2013



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Hardware

CPU Name: Intel Xeon E5-2637 v2
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz: 3500
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
Compiler: 2.6.32-358.el6.x86_64
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	533	293	530	295	530	295	16	442	354	444	352	444	352
401.bzip2	16	684	226	680	227	682	226	16	666	232	665	232	663	233
403.gcc	16	384	336	384	336	385	335	16	384	336	384	336	385	335
429.mcf	16	224	653	223	653	223	654	16	224	653	223	653	223	654
445.gobmk	16	569	295	581	289	569	295	16	555	302	569	295	570	295
456.hammer	16	274	545	273	547	274	545	16	242	616	242	616	243	614
458.sjeng	16	664	291	678	286	678	286	16	650	298	632	306	630	307
462.libquantum	16	125	2650	125	2650	125	2650	16	125	2650	125	2650	125	2650
464.h264ref	16	712	498	715	495	712	497	16	702	504	704	503	702	505
471.omnetpp	16	436	229	435	230	436	229	16	406	246	406	246	407	246
473.astar	16	456	246	455	247	451	249	16	456	246	455	247	451	249
483.xalancbmk	16	233	474	231	477	233	474	16	233	474	231	477	233	474

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:
intel_idle.max_cstate=0

Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6874

\$Rev: 6874 \$ \$Date::: 2013-11-20 #\\$ 654bd3fcf53b06faef0efe54ed011998

running on dx360M4 Tue Dec 2 19:10:12 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2637 v2 @ 3.50GHz
2 "physical id"s (chips)

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Platform Notes (Continued)

```
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 8
    physical 0: cores 1 2 3 4
    physical 1: cores 1 2 3 4
    cache size : 15360 KB

From /proc/meminfo
MemTotal:      264643476 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux dx360M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 2 19:05

SPEC is set to: /home/SPECCpu-20140116-ic14.0
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/mapper/vg_td2-lv_home
                ext4   380G  174G  187G  49%  /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TDE139OUS-1.50]- 02/21/2014
Memory:
16x Samsung M393B2G70QH0-CMA 16 GB 2 rank 1866 MHz, configured at 1867 MHz

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPECCpu-20140116-ic14.0/libs/32:/home/SPECCpu-20140116-ic14.0/libs/64:/home/SPECCpu-20140116-ic14.0/sh"

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32
```

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4_2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4_2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32`

458.sjeng: `-xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll14 -auto-ilp32`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

```
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll2 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/sh -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.xml>

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For other inquiries, please contact webmaster@spec.org.

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