



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®2006 = 55.7

Express5800/R120f-2M (Intel Xeon E5-2620 v3)

SPECint_base2006 = 53.5

CPU2006 license: 9006

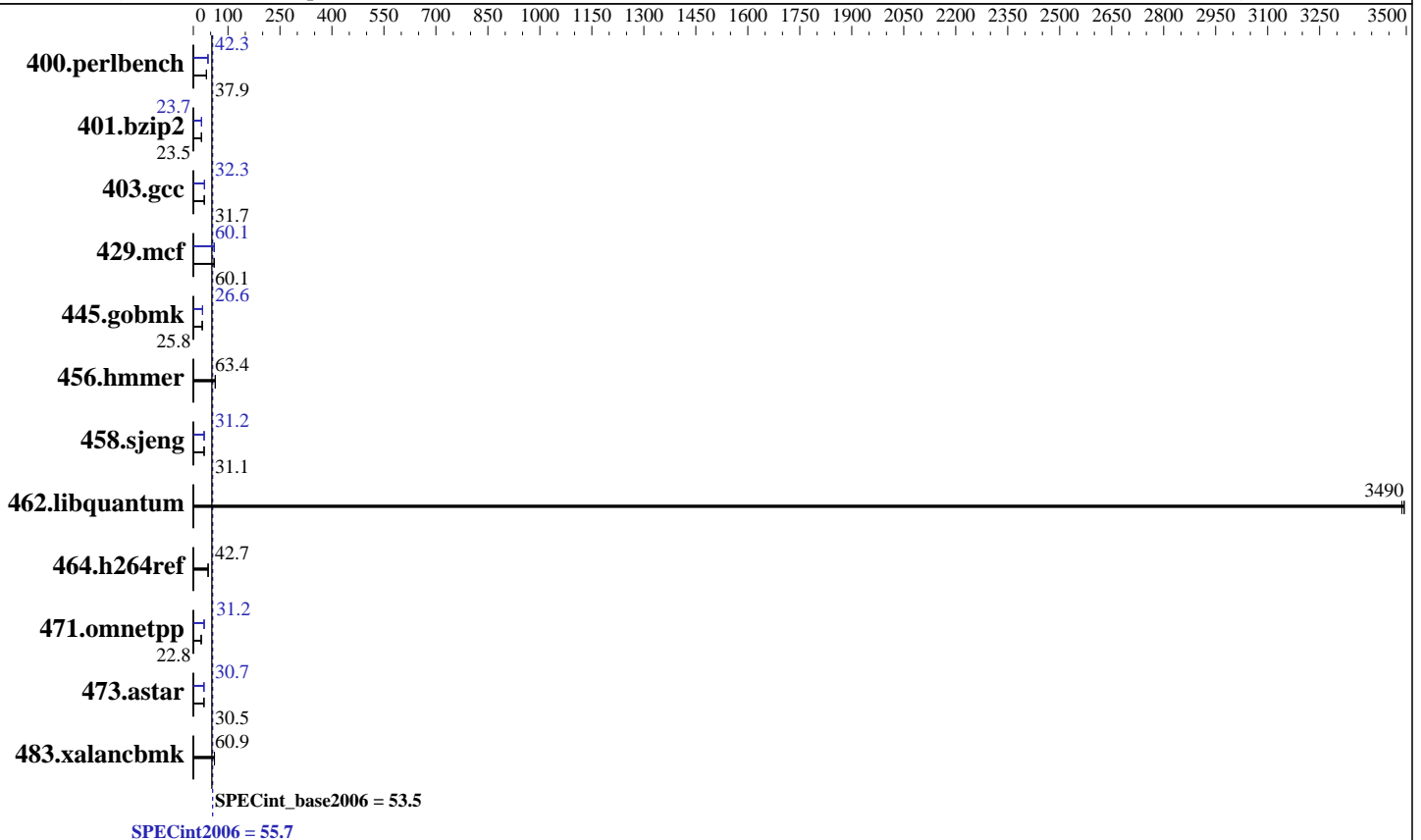
Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



Hardware

CPU Name: Intel Xeon E5-2620 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.17.1.el6.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = **55.7**

Express5800/R120f-2M (Intel Xeon E5-2620 v3)

SPECint_base2006 = **53.5**

CPU2006 license: 9006

Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	259	37.8	258	37.9	<u>258</u>	<u>37.9</u>	231	42.3	<u>231</u>	<u>42.3</u>	232	42.1
401.bzip2	410	23.5	<u>411</u>	<u>23.5</u>	412	23.4	408	23.7	409	23.6	<u>408</u>	<u>23.7</u>
403.gcc	<u>254</u>	<u>31.7</u>	254	31.7	254	31.8	<u>249</u>	<u>32.3</u>	249	32.3	254	31.7
429.mcf	151	60.4	152	60.0	<u>152</u>	<u>60.1</u>	152	60.1	<u>152</u>	<u>60.1</u>	150	60.8
445.gobmk	407	25.8	<u>407</u>	<u>25.8</u>	408	25.7	<u>395</u>	<u>26.6</u>	395	26.6	395	26.6
456.hammer	147	63.4	147	63.3	<u>147</u>	<u>63.4</u>	147	63.4	147	63.3	<u>147</u>	<u>63.4</u>
458.sjeng	389	31.1	<u>389</u>	<u>31.1</u>	389	31.1	388	31.2	388	31.2	<u>388</u>	<u>31.2</u>
462.libquantum	5.93	3490	<u>5.93</u>	<u>3490</u>	5.94	3490	5.93	3490	<u>5.93</u>	<u>3490</u>	5.94	3490
464.h264ref	520	42.6	<u>518</u>	<u>42.7</u>	518	42.8	520	42.6	<u>518</u>	<u>42.7</u>	518	42.8
471.omnetpp	<u>275</u>	<u>22.8</u>	274	22.8	276	22.7	200	31.3	201	31.0	<u>200</u>	<u>31.2</u>
473.astar	<u>230</u>	<u>30.5</u>	227	30.9	230	30.5	229	30.7	229	30.6	<u>229</u>	<u>30.7</u>
483.xalancbmk	<u>113</u>	<u>60.9</u>	113	60.8	113	61.1	<u>113</u>	<u>60.9</u>	113	60.8	113	61.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Demand Scrub: Disabled
 Hyper-Threading: Disabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "12"

The Express5800/R120f-1M (Intel Xeon E5-2620 v3) and the Express5800/R120f-2M (Intel Xeon E5-2620 v3) models are electronically equivalent. The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2620 v3) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 55.7

Express5800/R120f-2M (Intel Xeon E5-2620 v3)

SPECint_base2006 = 53.5

CPU2006 license: 9006

Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 55.7

Express5800/R120f-2M (Intel Xeon E5-2620 v3)

SPECint_base2006 = 53.5

CPU2006 license: 9006

Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
 401.bzip2: `-DSPEC_CPU_LP64`
 403.gcc: `-DSPEC_CPU_LP64`
 429.mcf: `-DSPEC_CPU_LP64`
 456.hmmmer: `-DSPEC_CPU_LP64`
 458.sjeng: `-DSPEC_CPU_LP64`
 462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
 464.h264ref: `-DSPEC_CPU_LP64`
 473.astar: `-DSPEC_CPU_LP64`
 483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmmer: `basepeak = yes`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 55.7

Express5800/R120f-2M (Intel Xeon E5-2620 v3)

SPECint_base2006 = 53.5

CPU2006 license: 9006

Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014

Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Feb 5 18:34:31 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 December 2014.