



SPEC® CFP2006 Result

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SPECfp®_rate2006 = 481

Express5800/R120f-2M (Intel Xeon E5-2620 v3)

SPECfp_rate_base2006 = 471

CPU2006 license: 9006

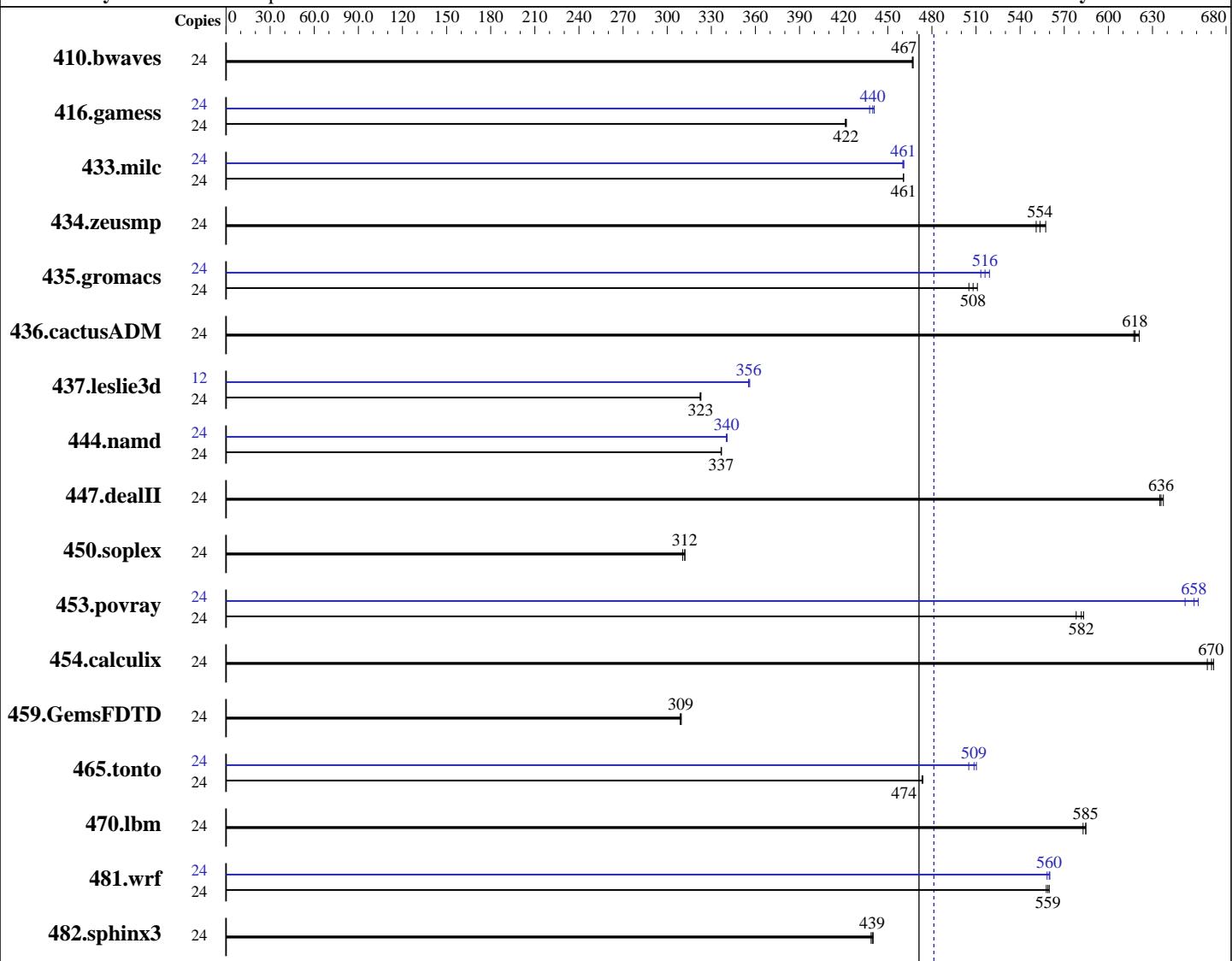
Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



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Hardware

CPU Name: Intel Xeon E5-2620 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: Kernel 2.6.32-431.17.1.el6.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	699	467	698	467	<u>698</u>	<u>467</u>	24	699	467	698	467	<u>698</u>	<u>467</u>
416.gamess	24	<u>1114</u>	<u>422</u>	1114	422	1116	421	24	1066	441	1074	438	<u>1069</u>	<u>440</u>
433.milc	24	478	461	478	461	<u>478</u>	<u>461</u>	24	478	461	<u>478</u>	<u>461</u>	479	460
434.zeusmp	24	396	551	<u>395</u>	<u>554</u>	392	557	24	396	551	<u>395</u>	<u>554</u>	392	557
435.gromacs	24	<u>337</u>	<u>508</u>	335	511	339	505	24	<u>332</u>	<u>516</u>	334	513	330	519
436.cactusADM	24	465	617	<u>464</u>	<u>618</u>	462	621	24	465	617	<u>464</u>	<u>618</u>	462	621
437.leslie3d	24	699	323	700	322	<u>699</u>	<u>323</u>	12	317	356	317	355	<u>317</u>	<u>356</u>
444.namd	24	<u>572</u>	<u>337</u>	572	337	571	337	24	566	340	<u>565</u>	<u>340</u>	565	341
447.dealII	24	432	635	<u>432</u>	<u>636</u>	431	637	24	432	635	<u>432</u>	<u>636</u>	431	637
450.soplex	24	645	310	<u>642</u>	<u>312</u>	641	312	24	645	310	<u>642</u>	<u>312</u>	641	312
453.povray	24	219	583	<u>220</u>	<u>582</u>	221	578	24	196	652	193	661	<u>194</u>	<u>658</u>
454.calculix	24	297	667	295	672	<u>295</u>	<u>670</u>	24	297	667	295	672	<u>295</u>	<u>670</u>
459.GemsFDTD	24	824	309	<u>824</u>	<u>309</u>	823	310	24	824	309	<u>824</u>	<u>309</u>	823	310
465.tonto	24	498	474	499	473	<u>499</u>	<u>474</u>	24	<u>464</u>	<u>509</u>	463	510	468	505
470.lbm	24	564	585	<u>564</u>	<u>585</u>	566	583	24	564	585	<u>564</u>	<u>585</u>	566	583
481.wrf	24	479	560	<u>480</u>	<u>559</u>	481	558	24	479	560	480	558	<u>479</u>	<u>560</u>
482.sphinx3	24	1063	440	1066	439	<u>1064</u>	<u>439</u>	24	1063	440	1066	439	<u>1064</u>	<u>439</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Power Management Policy: Custom

Energy Performance: Performance

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Platform Notes (Continued)

Patrol Scrub: Disabled
Demand Scrub: Disabled

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

The Express5800/R120f-1M (Intel Xeon E5-2620 v3) and
the Express5800/R120f-2M (Intel Xeon E5-2620 v3) models are electronically equivalent.
The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2620 v3) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable

Filesystem page cache cleared with:

echo 1 > /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
           -auto-ilp32
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2)
            -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.games: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

```
459.GemsFDTD: basepeak = yes
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
            -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32
```

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

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