



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 43.5

Huawei RH2288E V2 (Intel Xeon E5-2603 v2)

SPECfp\_base2006 = 41.4

CPU2006 license: 3175

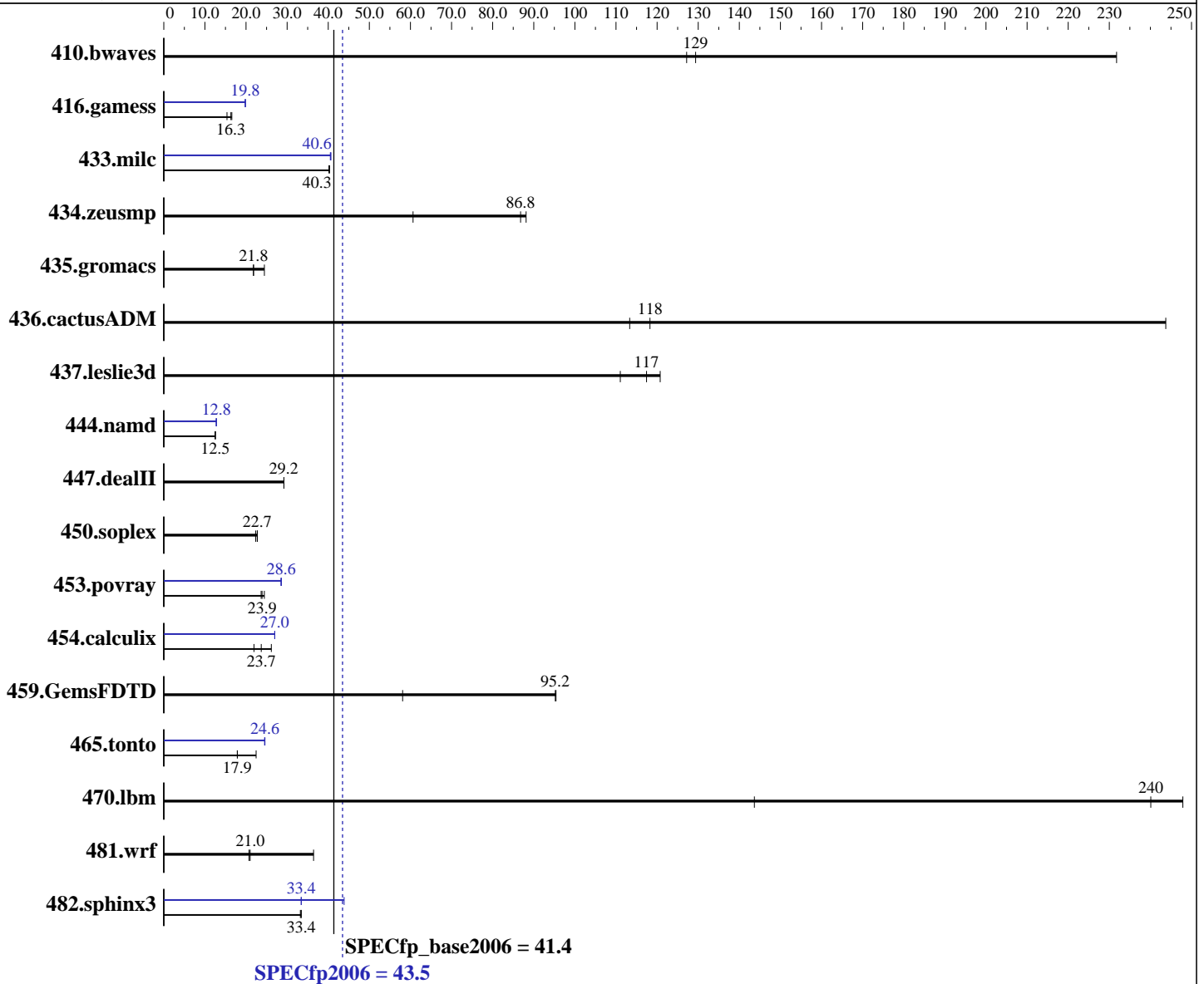
Test date: Oct-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



**Hardware**

CPU Name: Intel Xeon E5-2603 v2  
 CPU Characteristics:  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	107	127	<b>105</b>	<b>129</b>	58.6	232	107	127	<b>105</b>	<b>129</b>	58.6	232
416.gamess	1274	15.4	<b>1200</b>	<b>16.3</b>	1183	16.6	988	19.8	986	19.9	<b>988</b>	<b>19.8</b>
433.milc	228	40.2	<b>228</b>	<b>40.3</b>	228	40.3	226	40.6	<b>226</b>	<b>40.6</b>	227	40.5
434.zeusmp	150	60.6	103	88.1	<b>105</b>	<b>86.8</b>	150	60.6	103	88.1	<b>105</b>	<b>86.8</b>
435.gromacs	292	24.5	<b>327</b>	<b>21.8</b>	328	21.8	292	24.5	<b>327</b>	<b>21.8</b>	328	21.8
436.cactusADM	<b>101</b>	<b>118</b>	105	113	49.0	244	<b>101</b>	<b>118</b>	105	113	49.0	244
437.leslie3d	84.6	111	77.8	121	<b>80.0</b>	<b>117</b>	84.6	111	77.8	121	<b>80.0</b>	<b>117</b>
444.namd	640	12.5	<b>640</b>	<b>12.5</b>	640	12.5	<b>628</b>	<b>12.8</b>	628	12.8	628	12.8
447.dealII	<b>392</b>	<b>29.2</b>	391	29.2	392	29.2	<b>392</b>	<b>29.2</b>	391	29.2	392	29.2
450.soplex	373	22.3	<b>367</b>	<b>22.7</b>	367	22.7	373	22.3	<b>367</b>	<b>22.7</b>	367	22.7
453.povray	225	23.7	<b>223</b>	<b>23.9</b>	218	24.5	187	28.5	<b>186</b>	<b>28.6</b>	186	28.6
454.calculix	<b>348</b>	<b>23.7</b>	315	26.2	376	21.9	306	27.0	<b>306</b>	<b>27.0</b>	306	27.0
459.GemsFDTD	<b>111</b>	<b>95.2</b>	111	95.4	183	58.1	<b>111</b>	<b>95.2</b>	111	95.4	183	58.1
465.tonto	438	22.5	550	17.9	<b>550</b>	<b>17.9</b>	<b>400</b>	<b>24.6</b>	400	24.6	401	24.5
470.lbm	<b>57.2</b>	<b>240</b>	95.6	144	55.4	248	<b>57.2</b>	<b>240</b>	95.6	144	55.4	248
481.wrf	307	36.4	539	20.7	<b>532</b>	<b>21.0</b>	307	36.4	539	20.7	<b>532</b>	<b>21.0</b>
482.sphinx3	<b>583</b>	<b>33.4</b>	587	33.2	583	33.4	<b>583</b>	<b>33.4</b>	583	33.4	444	43.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec14/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on speccputest Sun Oct 26 10:29:44 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Hardware Availability: Sep-2013

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2603 v2 @ 1.80GHz
 2 "physical id"s (chips)
 8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings   : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB
```

From /proc/meminfo

```
MemTotal:      264478184 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux speccputest 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 18 17:28

SPEC is set to: /spec14

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  451G   61G  368G  15% /
```

Additional information from dmidecode:

BIOS Insyde Corp. RMIBV379 03/19/2014

Memory:

```
16x Hynix HMT42GR7MFR4C-PB 16 GB 1333 MHz 2 rank
8x NO DIMM NO DIMM
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/spec14/libs/32:/spec14/libs/64:/spec14/sh"
OMP_NUM_THREADS = "8"
```

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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