



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL380 Gen9
(3.00 GHz, Intel Xeon E5-2623 v3)

SPECfp[®]_rate2006 = 388

SPECfp_rate_base2006 = 379

CPU2006 license: 3

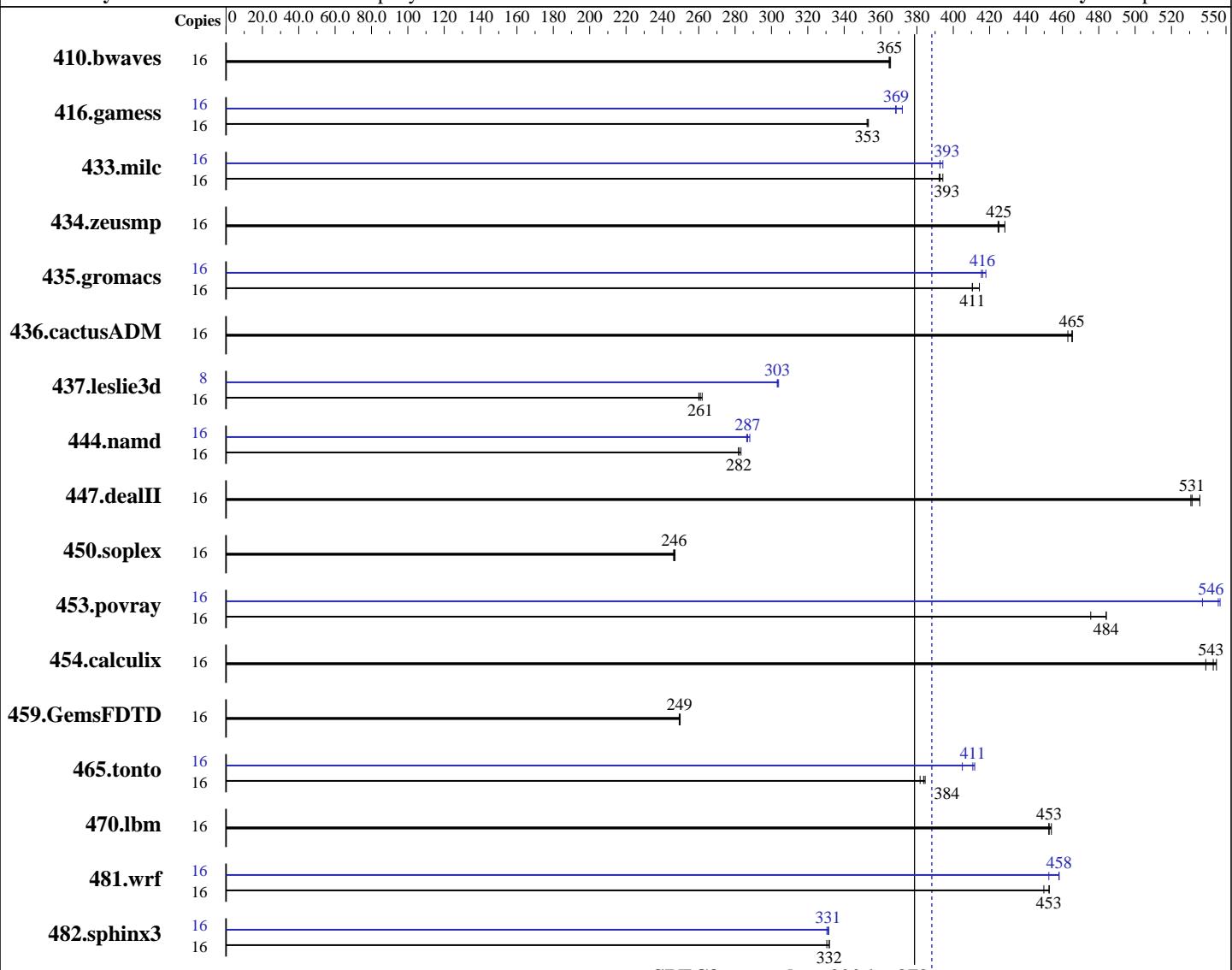
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



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Hardware

CPU Name: Intel Xeon E5-2623 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: Kernel 3.10.0-121.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R,
running at 1866 MHz)
Disk Subsystem: 1 x 400 GB SSD SAS, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	596	365	595	365	596	365	16	596	365	595	365	596	365
416.gamess	16	889	353	887	353	888	353	16	842	372	850	369	851	368
433.milc	16	375	392	374	393	373	394	16	374	393	373	394	374	393
434.zeusmp	16	340	428	343	425	343	425	16	340	428	343	425	343	425
435.gromacs	16	278	410	276	414	278	411	16	275	415	273	418	275	416
436.cactusADM	16	411	465	413	463	411	466	16	411	465	413	463	411	466
437.leslie3d	16	574	262	577	261	578	260	8	248	303	248	303	247	304
444.namd	16	453	283	455	282	455	282	16	447	287	445	288	448	286
447.dealII	16	342	536	345	531	345	531	16	342	536	345	531	345	531
450.soplex	16	542	246	540	247	542	246	16	542	246	540	247	542	246
453.povray	16	176	484	179	476	176	484	16	156	547	158	537	156	546
454.calculix	16	243	543	245	539	242	545	16	243	543	245	539	242	545
459.GemsFDTD	16	681	249	680	250	681	249	16	681	249	680	250	681	249
465.tonto	16	412	382	409	385	410	384	16	389	405	383	411	382	412
470.lbm	16	486	453	486	453	484	454	16	486	453	486	453	484	454
481.wrf	16	397	450	395	453	395	453	16	390	458	395	453	390	458
482.sphinx3	16	944	330	940	332	940	332	16	941	332	942	331	944	330

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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Platform Notes

BIOS Configuration:

HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Minimum Processor Idle Power Package State set to No Package State
QPI Snoop Configuration set to Early Snoop
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh
Sysinfo program /cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date::: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on R112-BAO-DL380-Gen9VP2 Sat Nov 8 13:38:07 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2623 v3 @ 3.00GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 4
 siblings : 8
 physical 0: cores 0 1 2 3
 physical 1: cores 0 1 2 3
cache size : 10240 KB

From /proc/meminfo
MemTotal: 263846772 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
 NAME="Red Hat Enterprise Linux Server"
 VERSION="7.0 (Maipo)"
 ID="rhel"
 ID_LIKE="fedora"
 VERSION_ID="7.0"
 PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
 ANSI_COLOR="0;31"
 CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux R112-BAO-DL380-Gen9VP2 3.10.0-121.el7.x86_64 #1 SMP Tue Apr 8 10:48:19
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Platform Notes (Continued)

EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 8 04:04

SPEC is set to: /cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	ext4	362G	203G	141G	60%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 07/11/2014

Memory:

1x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1866 MHz
15x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1866 MHz
8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have two lines reading as:

1x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1866 MHz
15x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1866 MHz

General Notes

Environment variables set by runspec before the start of the run:
`LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/lib64:/cpu2006/sh"`

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`



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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
           -auto-ilp32
```

470.lbm: basepeak = yes

```
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
              -unroll12
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2)
            -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep-
```

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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