



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 284

PowerEdge T430 (Intel Xeon E5-2603 v3, 1.60 GHz)

SPECfp_rate_base2006 = 278

CPU2006 license: 55

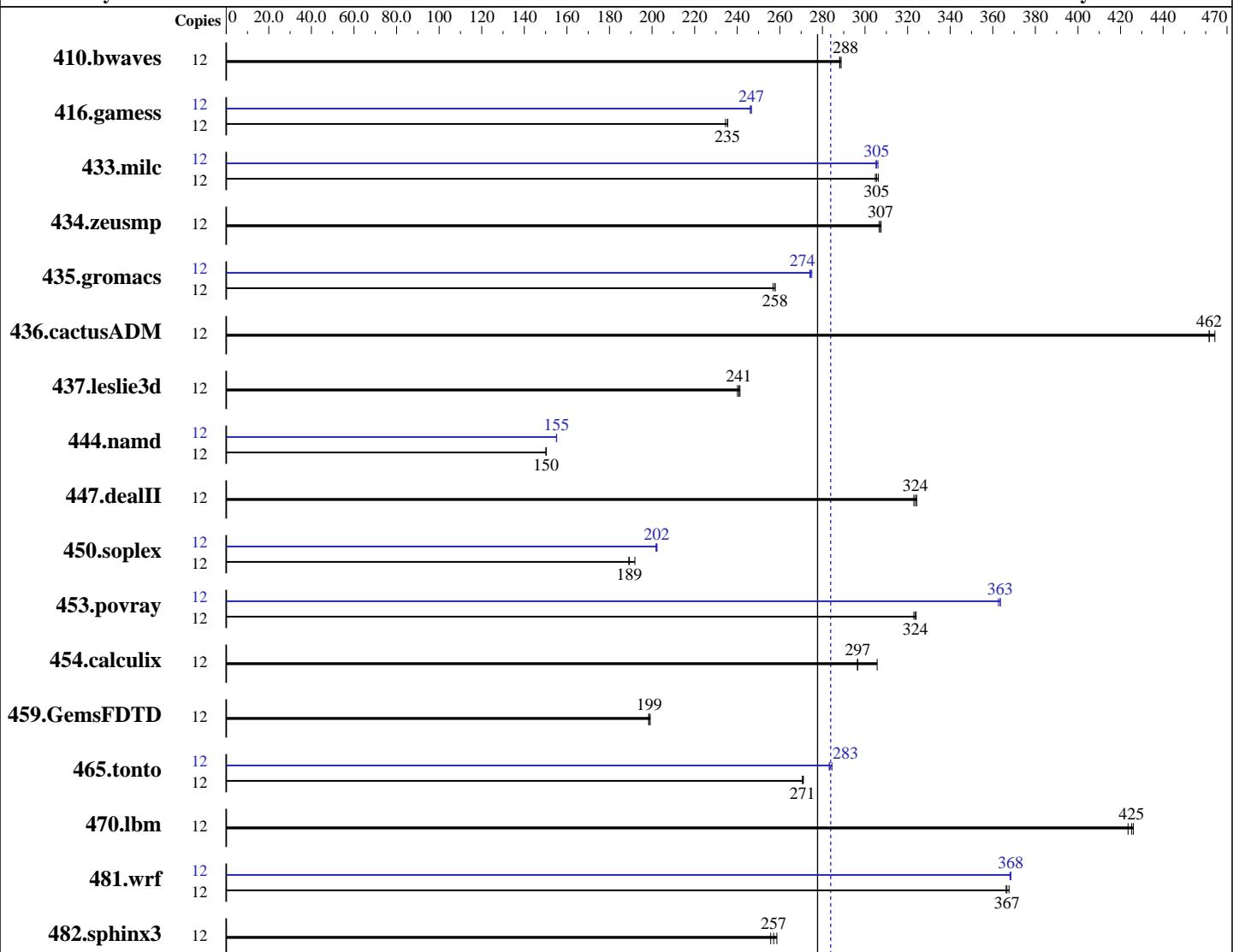
Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Dec-2014

Tested by: Dell Inc.

Software Availability: Jan-2014



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Hardware

CPU Name: Intel Xeon E5-2603 v3
 CPU Characteristics:
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Compiler: 2.6.32-431.el6.x86_64
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 1 x 600 GB 10000 RPM SAS
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	<u>566</u>	<u>288</u>	566	288	565	289	12	<u>566</u>	<u>288</u>	566	288	565	289
416.gamess	12	<u>998</u>	<u>235</u>	1002	234	997	236	12	<u>955</u>	<u>246</u>	953	247	<u>953</u>	<u>247</u>
433.milc	12	360	306	<u>361</u>	<u>305</u>	361	305	12	360	306	361	305	<u>361</u>	<u>305</u>
434.zeusmp	12	<u>355</u>	<u>307</u>	356	307	355	308	12	<u>355</u>	<u>307</u>	356	307	355	308
435.gromacs	12	<u>332</u>	<u>258</u>	332	258	334	257	12	312	275	<u>312</u>	<u>274</u>	313	274
436.cactusADM	12	309	464	<u>311</u>	<u>462</u>	311	462	12	309	464	<u>311</u>	<u>462</u>	311	462
437.leslie3d	12	467	241	<u>469</u>	<u>241</u>	470	240	12	467	241	<u>469</u>	<u>241</u>	470	240
444.namd	12	640	150	641	150	<u>641</u>	<u>150</u>	12	621	155	620	155	<u>620</u>	<u>155</u>
447.dealII	12	425	323	423	324	<u>424</u>	<u>324</u>	12	425	323	423	324	<u>424</u>	<u>324</u>
450.soplex	12	<u>529</u>	<u>189</u>	529	189	521	192	12	496	202	<u>495</u>	<u>202</u>	495	202
453.povray	12	<u>197</u>	<u>324</u>	197	324	198	323	12	176	363	<u>176</u>	<u>363</u>	176	364
454.calculix	12	<u>334</u>	<u>297</u>	324	306	334	296	12	<u>334</u>	<u>297</u>	324	306	334	296
459.GemsFDTD	12	642	198	639	199	<u>641</u>	<u>199</u>	12	642	198	639	199	<u>641</u>	<u>199</u>
465.tonto	12	436	271	<u>436</u>	<u>271</u>	436	271	12	415	284	<u>417</u>	<u>283</u>	417	283
470.lbm	12	<u>388</u>	<u>425</u>	387	426	389	424	12	<u>388</u>	<u>425</u>	387	426	389	424
481.wrf	12	366	366	<u>366</u>	<u>367</u>	365	368	12	364	368	364	369	<u>364</u>	<u>368</u>
482.sphinx3	12	<u>910</u>	<u>257</u>	904	259	915	256	12	<u>910</u>	<u>257</u>	904	259	915	256

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:

Snoop Mode set to Cluster on Die

Virtualization Technology disabled

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Platform Notes (Continued)

```
Execute Disable disabled
System Profile set to Custom
Memory Patrol Scrub set to Disabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date::: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Sun Oct 19 00:59:43 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 6
        siblings : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      132054904 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 18 13:51
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  546G  9.7G  509G   2%  /
```

Additional information from dmidecode:

BIOS Dell Inc. 1.0.0 10/15/2014

Memory:

4x 000000000000 Not Specified 1600 MHz 1 rank
4x 002C00B3002C 36ASF2G72PZ-2G1A2 16 GB 1600 MHz 2 rank

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Platform Notes (Continued)

4x 00CE00B300CE M393A2G40DB0-CPB 16 GB 1600 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64

465.tonto: -DSPEC_CPU_LP64

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
           -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
           -ansi-alias

```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

```
410.bwaves: basepeak = yes  
  
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
             -inline-level=0 -scalar-rep-  
  
434.zeusmp: basepeak = yes  
  
437.leslie3d: basepeak = yes  
  
459.GemsFDTD: basepeak = yes  
  
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
             -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
              -O3(pass 2) -no-prec-div(pass 2)  
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
              -opt-prefetch -auto-ilp32  
  
436.cactusADM: basepeak = yes  
  
454.calculix: basepeak = yes  
  
481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.xml>



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