



# SPEC® CINT2006 Result

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## Lenovo Group Limited

IBM System x iDataPlex dx360 M4  
(Intel Xeon E5-2643 v2, 3.50 GHz)

**SPECint®\_rate2006 = 637**

**SPECint\_rate\_base2006 = 611**

CPU2006 license: 9017

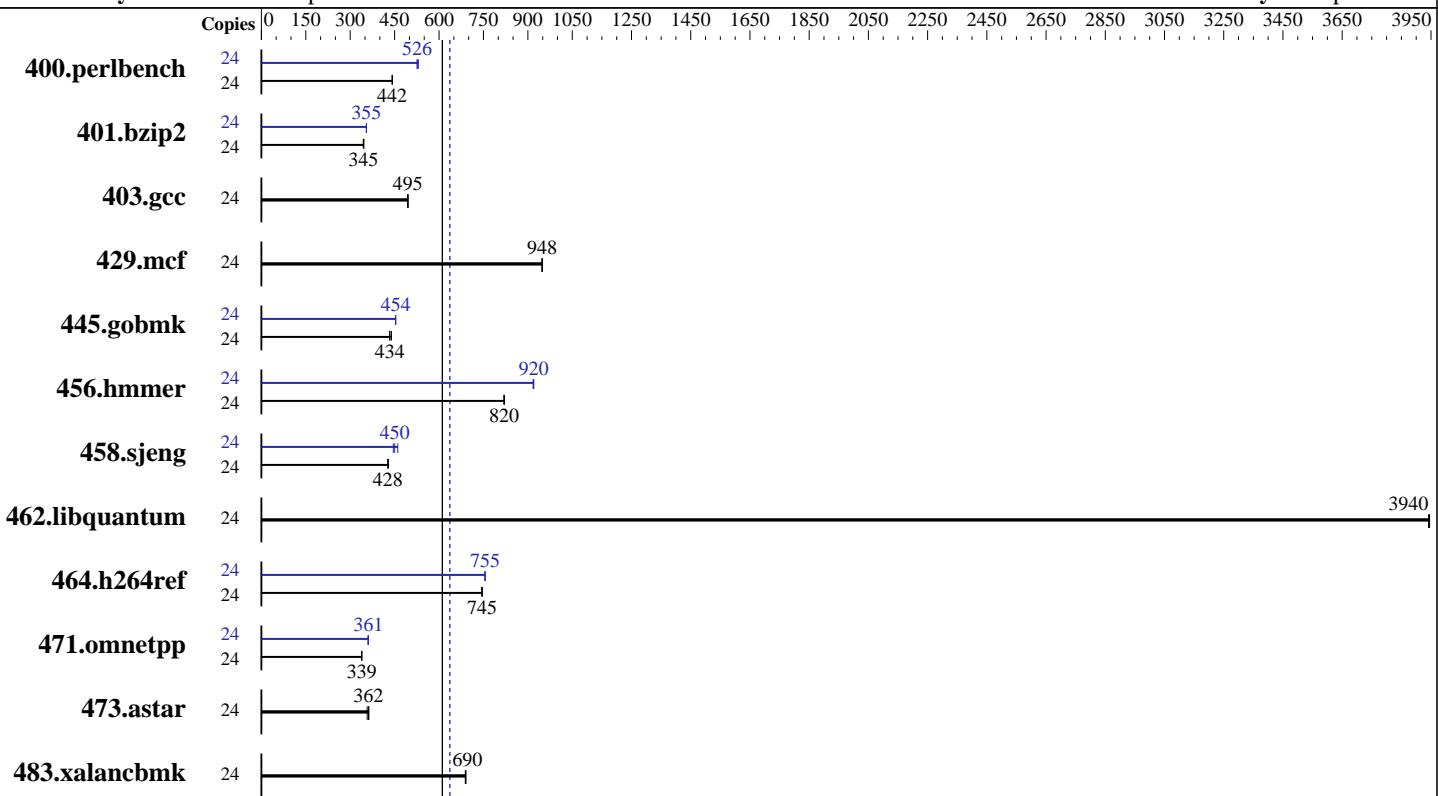
Test sponsor: Lenovo Group Limited

Tested by: IBM Corporation

Test date: Nov-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



**SPECint\_rate\_base2006 = 611**

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### Hardware

CPU Name: Intel Xeon E5-2643 v2  
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
CPU MHz: 3500  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
Compiler: 2.6.32-358.el6.x86\_64  
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
Auto Parallel: No  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

| Benchmark      | Base   |            |             |            |            |            |            | Peak   |            |             |            |            |            |            |
|----------------|--------|------------|-------------|------------|------------|------------|------------|--------|------------|-------------|------------|------------|------------|------------|
|                | Copies | Seconds    | Ratio       | Seconds    | Ratio      | Seconds    | Ratio      | Copies | Seconds    | Ratio       | Seconds    | Ratio      | Seconds    | Ratio      |
| 400.perlbench  | 24     | <b>530</b> | <b>442</b>  | 530        | 442        | 531        | 442        | 24     | 442        | 530         | 446        | 526        | <b>445</b> | <b>526</b> |
| 401.bzip2      | 24     | 669        | 346         | <b>671</b> | <b>345</b> | 673        | 344        | 24     | 653        | 355         | <b>653</b> | <b>355</b> | 653        | 354        |
| 403.gcc        | 24     | <b>390</b> | <b>495</b>  | 391        | 494        | 390        | 495        | 24     | <b>390</b> | <b>495</b>  | 391        | 494        | 390        | 495        |
| 429.mcf        | 24     | <b>231</b> | <b>948</b>  | 231        | 949        | 231        | 948        | 24     | <b>231</b> | <b>948</b>  | 231        | 949        | 231        | 948        |
| 445.gobmk      | 24     | 573        | 439         | <b>580</b> | <b>434</b> | 581        | 433        | 24     | 555        | 454         | <b>555</b> | <b>454</b> | 556        | 453        |
| 456.hammer     | 24     | 273        | 821         | <b>273</b> | <b>820</b> | 273        | 819        | 24     | 244        | 918         | <b>244</b> | <b>920</b> | 243        | 920        |
| 458.sjeng      | 24     | <b>679</b> | <b>428</b>  | 680        | 427        | 677        | 429        | 24     | 630        | 461         | 652        | 445        | <b>645</b> | <b>450</b> |
| 462.libquantum | 24     | <b>126</b> | <b>3940</b> | 126        | 3940       | 126        | 3950       | 24     | <b>126</b> | <b>3940</b> | 126        | 3940       | 126        | 3950       |
| 464.h264ref    | 24     | 713        | 745         | <b>713</b> | <b>745</b> | 712        | 746        | 24     | 703        | 755         | <b>703</b> | <b>755</b> | 703        | 755        |
| 471.omnetpp    | 24     | <b>443</b> | <b>339</b>  | 442        | 339        | 443        | 339        | 24     | 417        | 360         | 415        | 361        | <b>415</b> | <b>361</b> |
| 473.astar      | 24     | <b>466</b> | <b>362</b>  | 464        | 363        | 472        | 357        | 24     | <b>466</b> | <b>362</b>  | 464        | 363        | 472        | 357        |
| 483.xalancbmk  | 24     | 240        | 690         | 240        | 689        | <b>240</b> | <b>690</b> | 24     | 240        | 690         | 240        | 689        | <b>240</b> | <b>690</b> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

echo 1 > /proc/sys/vm/zone\_reclaim\_mode

Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:  
intel\_idle.max\_cstate=0

## Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6874

\$Rev: 6874 \$ \$Date::: 2013-11-20 #\\$ 654bd3fcf53b06faef0efe54ed011998

running on dx360M4 Fri Nov 7 16:12:06 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2643 v2 @ 3.50GHz  
2 "physical id"s (chips)

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## Platform Notes (Continued)

```
24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings   : 12
physical 0: cores 2 3 4 8 9 10
physical 1: cores 2 3 4 8 9 10
cache size : 25600 KB

From /proc/meminfo
MemTotal:      264642460 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux dx360M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 7 15:28

SPEC is set to: /home/SPECCpu-20140116-ic14.0
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/mapper/vg_td2-lv_home
                  ext4   380G  174G  187G  49%  /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TDE139OUS-1.50]- 02/21/2014
Memory:
16x Samsung M393B2G70QH0-CMA 16 GB 2 rank 1866 MHz, configured at 1867 MHz

(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPECCpu-20140116-ic14.0/lib32:/home/SPECCpu-20140116-ic14.0/lib64:/home/SPECCpu-20140116-ic14.0/sh"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`  
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-unroll14 -auto-ilp32`

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

```
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll2 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/sh -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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