



SPEC® CINT2006 Result

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NEC Corporation

SPECint®_rate2006 = 1210

Express5800/R120f-1M (Intel Xeon E5-2697 v3)

SPECint_rate_base2006 = 1180

CPU2006 license: 9006

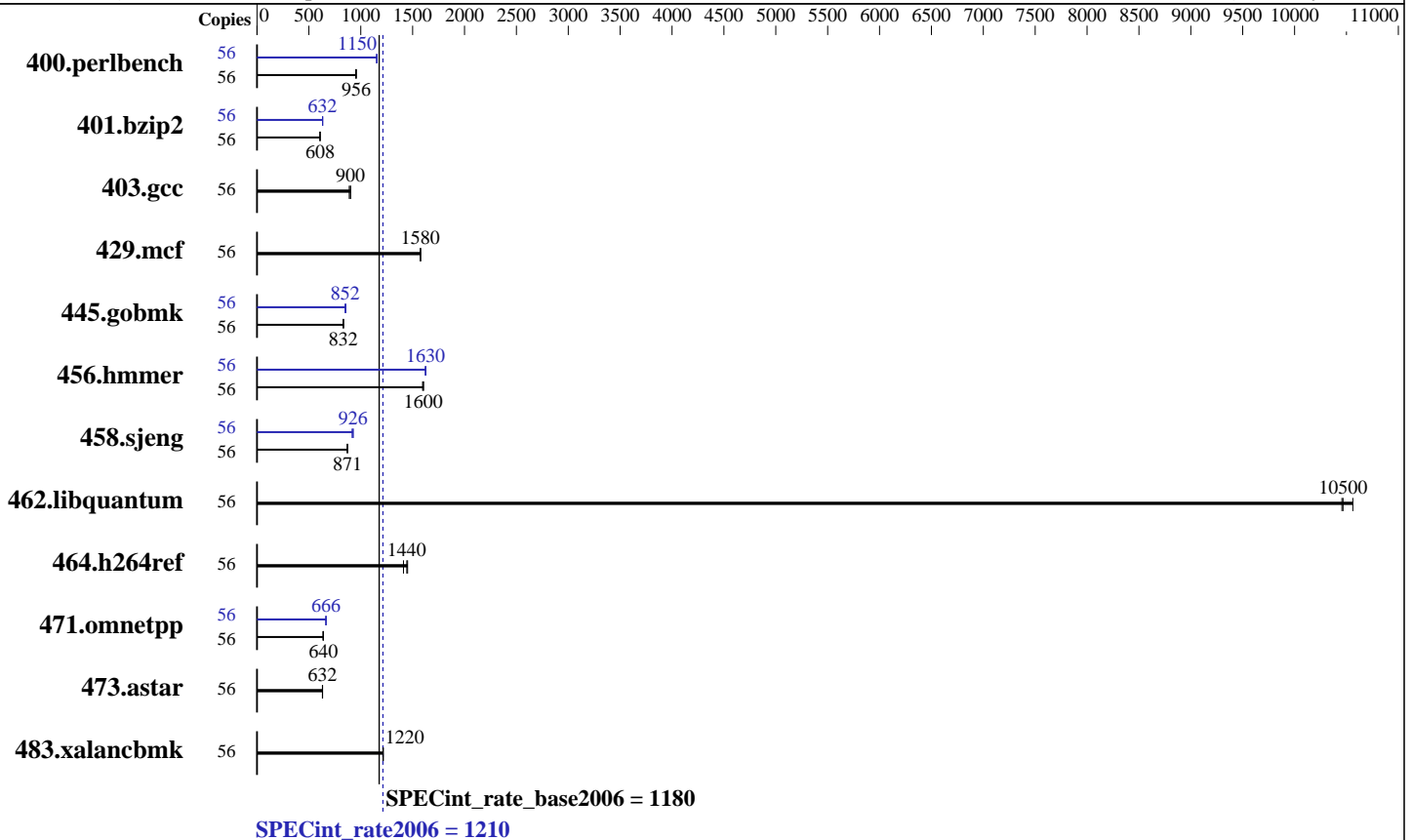
Test date: Nov-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: May-2014



Hardware

CPU Name: Intel Xeon E5-2697 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.17.1.el6.x86_64
 Compiler: C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	56	575	951	<u>572</u>	<u>956</u>	572	957	56	474	1150	<u>474</u>	<u>1150</u>	474	1150
401.bzip2	56	<u>889</u>	<u>608</u>	887	609	892	606	56	856	632	852	634	<u>855</u>	<u>632</u>
403.gcc	56	507	889	500	901	<u>501</u>	<u>900</u>	56	507	889	500	901	<u>501</u>	<u>900</u>
429.mcf	56	324	1580	<u>324</u>	<u>1580</u>	324	1580	56	324	1580	<u>324</u>	<u>1580</u>	324	1580
445.gobmk	56	707	831	704	835	<u>706</u>	<u>832</u>	56	692	849	686	856	<u>689</u>	<u>852</u>
456.hammer	56	326	1600	327	1600	<u>326</u>	<u>1600</u>	56	321	1630	322	1620	<u>321</u>	<u>1630</u>
458.sjeng	56	778	871	778	871	<u>778</u>	<u>871</u>	56	<u>732</u>	<u>926</u>	731	927	740	916
462.libquantum	56	111	10500	<u>111</u>	<u>10500</u>	110	10600	56	111	10500	<u>111</u>	<u>10500</u>	110	10600
464.h264ref	56	<u>858</u>	<u>1440</u>	854	1450	878	1410	56	<u>858</u>	<u>1440</u>	854	1450	878	1410
471.omnetpp	56	551	635	<u>547</u>	<u>640</u>	547	640	56	<u>526</u>	<u>666</u>	528	663	526	666
473.astar	56	624	630	<u>622</u>	<u>632</u>	622	632	56	624	630	<u>622</u>	<u>632</u>	622	632
483.xalancbmk	56	<u>318</u>	<u>1220</u>	318	1220	318	1220	56	<u>318</u>	<u>1220</u>	318	1220	318	1220

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Power Management Policy: Custom
Energy Performance: Performance
Patrol Scrub: Disabled
Cluster on Die: Enabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

The Express5800/R120f-1M (Intel Xeon E5-2697 v3) and the Express5800/R120f-2M (Intel Xeon E5-2697 v3) models are electronically equivalent. The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2697 v3) model.

Transparent Huge Pages enabled with:

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General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

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Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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