



# SPEC® CFP2006 Result

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Huawei

**SPECfp®2006 = 44.9**

Huawei CH242 v3 (E7-4809 v2)

**SPECfp\_base2006 = 41.9**

CPU2006 license: 3175

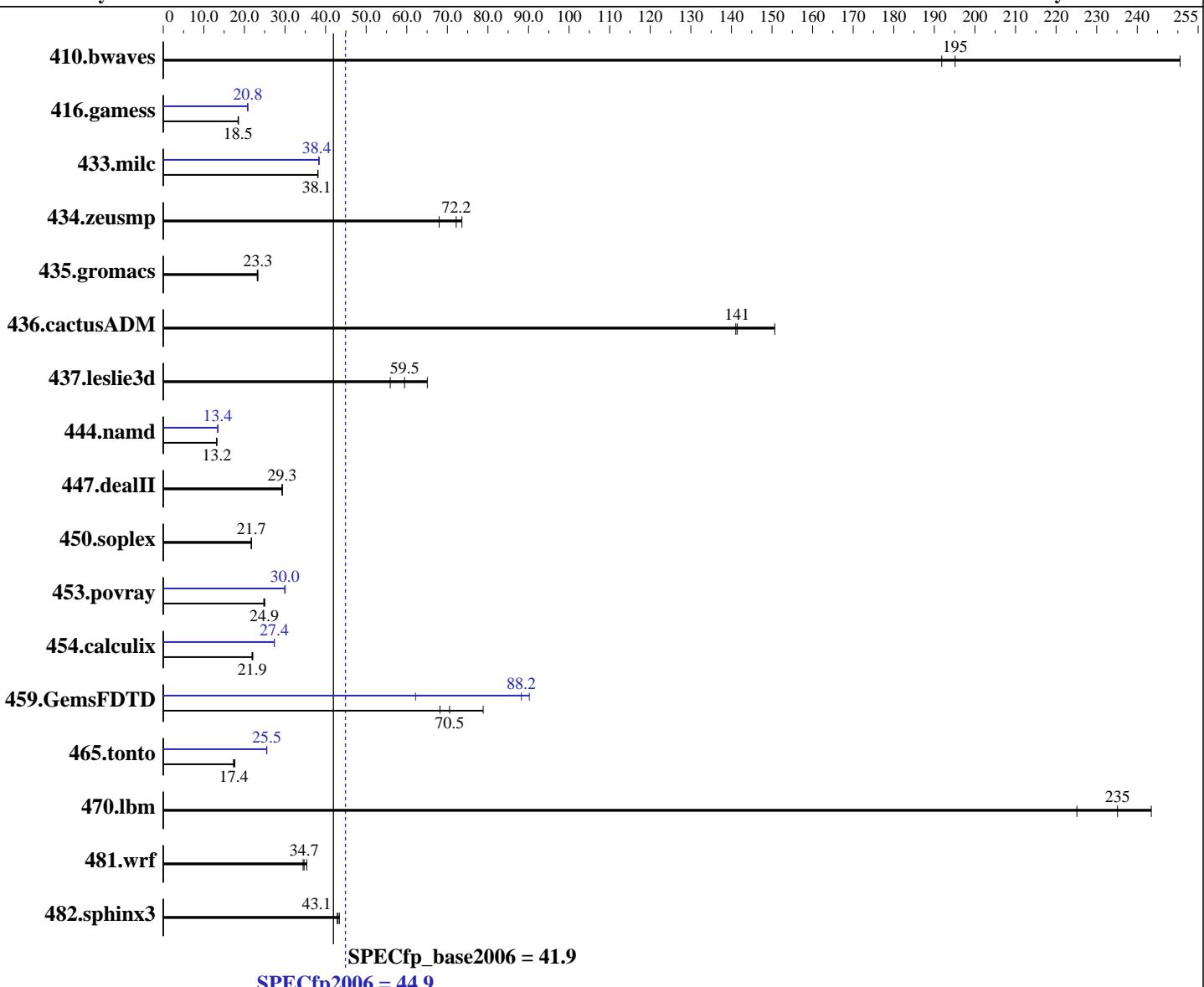
Test date: Oct-2014

Test sponsor: Huawei

Hardware Availability: Feb-2014

Tested by: Huawei

Software Availability: Nov-2013



## Hardware

CPU Name: Intel Xeon E7-4809 v2  
 CPU Characteristics:  
 CPU MHz: 1900  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Compiler: 2.6.32-431.el6.x86\_64  
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1066 MHz)  
 Disk Subsystem: 1 X 300 GB SATA 7200RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	70.8	192	54.2	251	<b><u>69.6</u></b>	<b><u>195</u></b>	70.8	192	54.2	251	<b><u>69.6</u></b>	<b><u>195</u></b>
416.gamess	1062	18.4	<b><u>1061</u></b>	<b><u>18.5</u></b>	1056	18.5	942	20.8	940	20.8	<b><u>940</u></b>	<b><u>20.8</u></b>
433.milc	241	38.1	<b><u>241</u></b>	<b><u>38.1</u></b>	241	38.1	239	38.4	239	38.4	<b><u>239</u></b>	<b><u>38.4</u></b>
434.zeusmp	<b><u>126</u></b>	<b><u>72.2</u></b>	134	68.0	124	73.6	<b><u>126</u></b>	<b><u>72.2</u></b>	134	68.0	124	73.6
435.gromacs	<b><u>307</u></b>	<b><u>23.3</u></b>	308	23.2	307	23.3	<b><u>307</u></b>	<b><u>23.3</u></b>	308	23.2	307	23.3
436.cactusADM	79.3	151	84.7	141	<b><u>84.5</u></b>	<b><u>141</u></b>	79.3	151	84.7	141	<b><u>84.5</u></b>	<b><u>141</u></b>
437.leslie3d	<b><u>158</u></b>	<b><u>59.5</u></b>	168	55.9	144	65.1	<b><u>158</u></b>	<b><u>59.5</u></b>	168	55.9	144	65.1
444.namd	609	13.2	<b><u>609</u></b>	<b><u>13.2</u></b>	609	13.2	597	13.4	597	13.4	<b><u>597</u></b>	<b><u>13.4</u></b>
447.dealII	<b><u>390</u></b>	<b><u>29.3</u></b>	391	29.3	390	29.3	<b><u>390</u></b>	<b><u>29.3</u></b>	391	29.3	390	29.3
450.soplex	<b><u>384</u></b>	<b><u>21.7</u></b>	385	21.7	383	21.8	<b><u>384</u></b>	<b><u>21.7</u></b>	385	21.7	383	21.8
453.povray	215	24.8	<b><u>214</u></b>	<b><u>24.9</u></b>	213	25.0	<b><u>177</u></b>	<b><u>30.0</u></b>	177	30.0	178	29.9
454.calculix	<b><u>376</u></b>	<b><u>21.9</u></b>	374	22.1	377	21.9	<b><u>302</u></b>	<b><u>27.4</u></b>	301	27.4	302	27.3
459.GemsFDTD	156	68.2	<b><u>150</u></b>	<b><u>70.5</u></b>	135	78.8	118	90.2	171	62.2	<b><u>120</u></b>	<b><u>88.2</u></b>
465.tonto	<b><u>567</u></b>	<b><u>17.4</u></b>	568	17.3	558	17.6	<b><u>386</u></b>	<b><u>25.5</u></b>	387	25.5	386	25.5
470.lbm	61.0	225	<b><u>58.4</u></b>	<b><u>235</u></b>	56.4	243	61.0	225	<b><u>58.4</u></b>	<b><u>235</u></b>	56.4	243
481.wrf	324	34.4	316	35.4	<b><u>322</u></b>	<b><u>34.7</u></b>	324	34.4	316	35.4	<b><u>322</u></b>	<b><u>34.7</u></b>
482.sphinx3	455	42.9	<b><u>452</u></b>	<b><u>43.1</u></b>	449	43.4	<b><u>455</u></b>	<b><u>42.9</u></b>	<b><u>452</u></b>	<b><u>43.1</u></b>	449	43.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set VMSE LockStep mode disable

Set Hyper Threading to disable

Sysinfo program /spec14/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Wed Oct 29 03:19:53 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E7-4809 v2 @ 1.90GHz
        4 "physical id"s (chips)
        24 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 6
        siblings : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
        physical 2: cores 0 1 2 3 4 5
        physical 3: cores 0 1 2 3 4 5
    cache size : 12288 KB
```

```
From /proc/meminfo
    MemTotal:       264360008 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 28 16:47
```

```
SPEC is set to: /spec14
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  255G  139G  104G  58% /
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. BLISV302 09/03/2014
Memory:
 32x 8 GB
 32x Micron 36KSF1G72PZ-1G6K1 8 GB 1066 MHz 2 rank
```

(End of data from sysinfo program)  
The system use 256 GB memory



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec14/libs/32:/spec14/libs/64:/spec14/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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