



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL360 Gen9
(2.30 GHz, Intel Xeon E5-2695 v3)

SPECfp®_rate2006 = 815

SPECfp_rate_base2006 = 790

CPU2006 license: 3

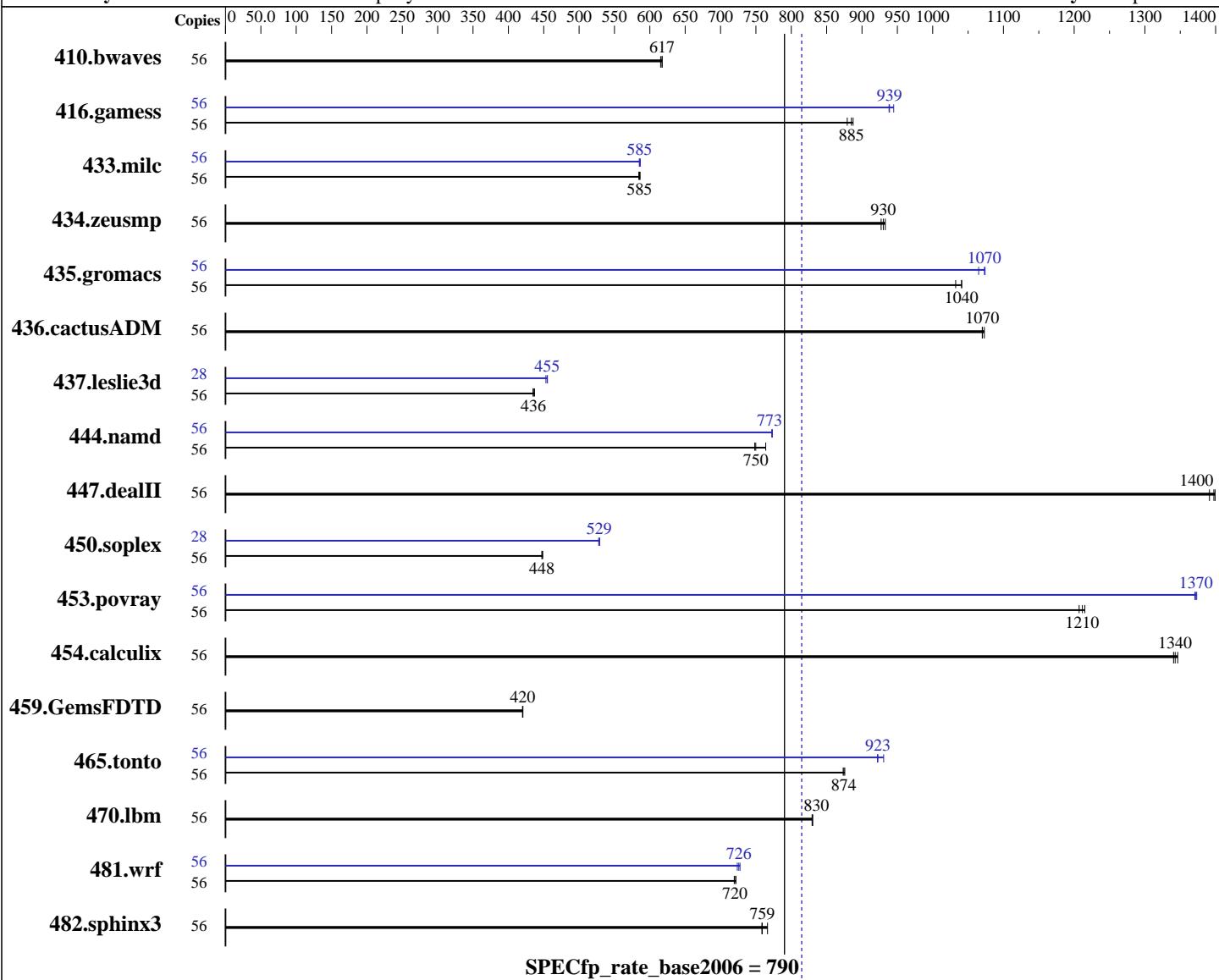
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2695 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: Kernel 3.10.0-123.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs

Continued on next page

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 400 GB SSA SAS, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|--------------------|--------------------|--------------------|--------------------|--------------------|-------------------|--------|--------------------|--------------------|--------------------|--------------------|-------------------|-------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 56 | <u>1233</u> | <u>617</u> | 1233 | 617 | 1237 | 615 | 56 | <u>1233</u> | <u>617</u> | 1233 | 617 | 1237 | 615 |
| 416.gamess | 56 | 1247 | 879 | <u>1239</u> | <u>885</u> | 1236 | 887 | 56 | 1160 | 945 | <u>1168</u> | <u>939</u> | 1169 | 938 |
| 433.milc | 56 | <u>878</u> | <u>585</u> | 877 | 586 | 880 | 584 | 56 | <u>876</u> | <u>587</u> | <u>878</u> | <u>585</u> | 878 | 585 |
| 434.zeusmp | 56 | 550 | 927 | 546 | 933 | <u>548</u> | <u>930</u> | 56 | <u>550</u> | 927 | 546 | 933 | <u>548</u> | <u>930</u> |
| 435.gromacs | 56 | 387 | 1030 | <u>384</u> | <u>1040</u> | 384 | 1040 | 56 | 372 | 1070 | <u>373</u> | <u>1070</u> | 375 | 1060 |
| 436.cactusADM | 56 | <u>625</u> | <u>1070</u> | 624 | 1070 | 625 | 1070 | 56 | <u>625</u> | <u>1070</u> | 624 | 1070 | 625 | 1070 |
| 437.leslie3d | 56 | <u>1209</u> | <u>436</u> | 1205 | 437 | 1210 | 435 | 28 | 581 | 453 | 578 | 455 | <u>579</u> | <u>455</u> |
| 444.namd | 56 | 600 | 748 | <u>599</u> | <u>750</u> | 588 | 764 | 56 | 581 | 772 | 581 | 773 | <u>581</u> | <u>773</u> |
| 447.dealII | 56 | 458 | 1400 | <u>458</u> | <u>1400</u> | 460 | 1390 | 56 | 458 | 1400 | <u>458</u> | <u>1400</u> | 460 | 1390 |
| 450.soplex | 56 | 1041 | 449 | 1044 | 447 | <u>1043</u> | <u>448</u> | 28 | 441 | 529 | 443 | 528 | <u>442</u> | <u>529</u> |
| 453.povray | 56 | <u>246</u> | <u>1210</u> | 247 | 1210 | 245 | 1220 | 56 | 217 | 1370 | <u>217</u> | <u>1370</u> | 217 | 1370 |
| 454.calculix | 56 | 343 | 1350 | <u>344</u> | <u>1340</u> | 345 | 1340 | 56 | 343 | 1350 | <u>344</u> | <u>1340</u> | 345 | 1340 |
| 459.GemsFDTD | 56 | <u>1414</u> | <u>420</u> | 1415 | 420 | 1413 | 421 | 56 | <u>1414</u> | <u>420</u> | 1415 | 420 | 1413 | 421 |
| 465.tonto | 56 | 631 | 873 | 629 | 876 | <u>630</u> | <u>874</u> | 56 | <u>597</u> | <u>923</u> | 592 | 931 | 598 | 922 |
| 470.lbm | 56 | 927 | 830 | 928 | 829 | <u>927</u> | <u>830</u> | 56 | 927 | 830 | 928 | 829 | <u>927</u> | <u>830</u> |
| 481.wrf | 56 | 867 | 722 | <u>869</u> | <u>720</u> | 870 | 719 | 56 | <u>862</u> | <u>726</u> | 864 | 724 | 860 | 728 |
| 482.sphinx3 | 56 | 1424 | 766 | <u>1438</u> | <u>759</u> | 1439 | 759 | 56 | <u>1424</u> | <u>766</u> | <u>1438</u> | <u>759</u> | 1439 | 759 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```



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Platform Notes

BIOS Configuration:

```
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Minimum Processor Idle Power Package State set to No Package State
QPI Snoop Configuration set to Cluster on Die
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh
```

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date::: 2014-06-25 #$
running on DL360-G9 Sat Oct 25 05:33:40 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2695 v3 @ 2.30GHz
        2 "physical id"s (chips)
        56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 7
        siblings : 14
        physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
        physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB
```

```
From /proc/meminfo
MemTotal:      263841672 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

uname -a:

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Platform Notes (Continued)

```
Linux DL360-G9 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 24 17:18
```

```
SPEC is set to: /home/cpu2006
```

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|---------------------------------|------|------|------|-------|------|------------|
| /dev/mapper/rhel_dl360--g9-home | xfs | 318G | 131G | 188G | 42% | /home |

```
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS HP P89 07/11/2014
```

```
Memory:
```

```
16x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz
```

```
8x UNKNOWN NOT AVAILABLE
```

```
(End of data from sysinfo program)
```

```
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 256 GB and the dmidecode description should have one line reading as:
16x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz
```

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
```

```
Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Continued on next page



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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

Continued on next page



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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



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For other inquiries, please contact webmaster@spec.org.

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