



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

SPECfp®2006 = 113

SPECfp\_base2006 = 109

CPU2006 license: 9017

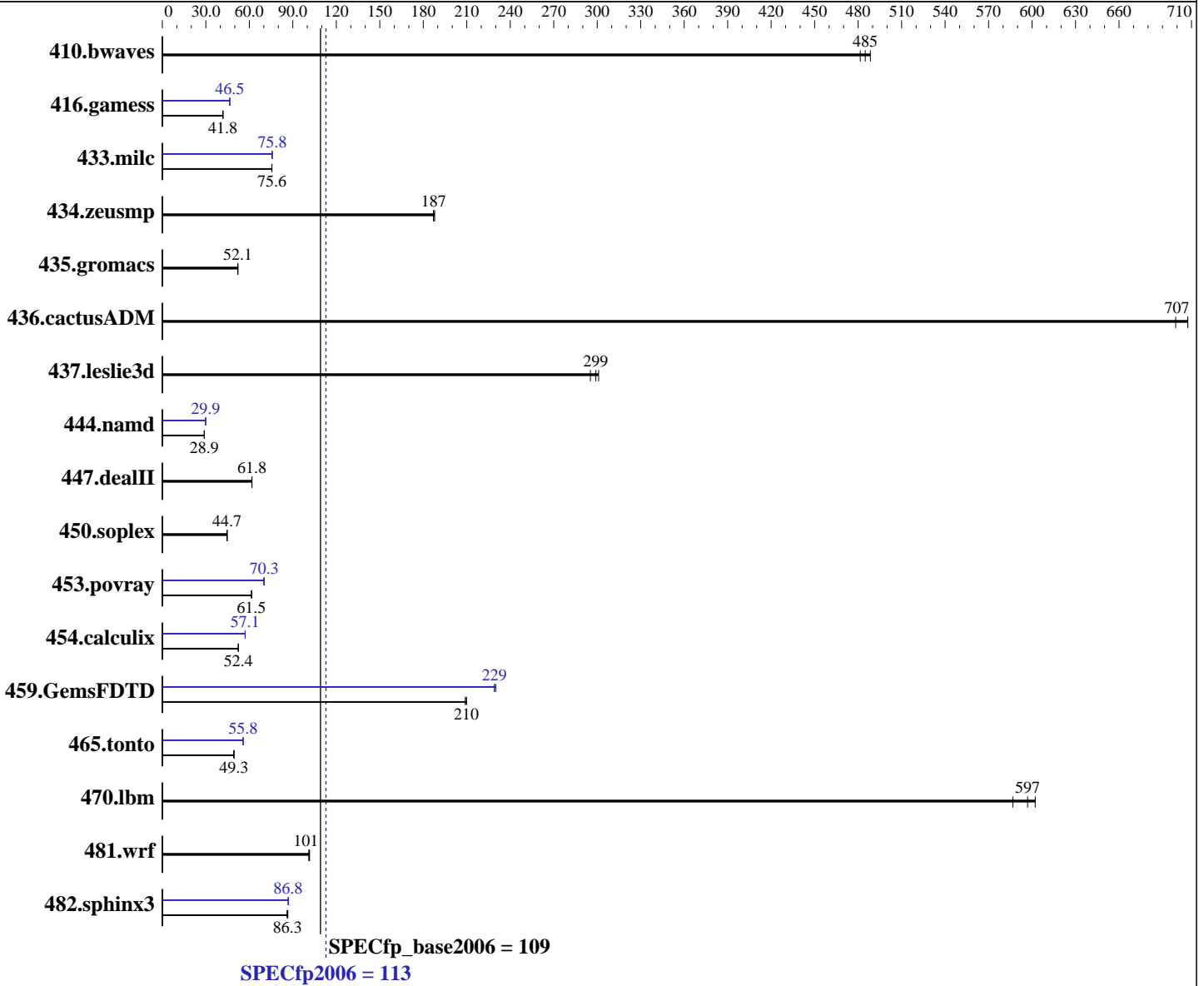
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Oct-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2014



### Hardware

CPU Name: Intel Xeon E5-2643 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp2006 = **113**

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

SPECfp\_base2006 = **109**

CPU2006 license: 9017

Test date: Oct-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Sep-2014

Tested by: Lenovo Group Limited

Software Availability: Nov-2014

L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	27.8	488	28.2	481	<b><u>28.0</u></b>	<b><u>485</u></b>	27.8	488	28.2	481	<b><u>28.0</u></b>	<b><u>485</u></b>
416.gamess	469	41.8	468	41.8	<b><u>468</u></b>	<b><u>41.8</u></b>	421	46.5	421	46.5	<b><u>421</u></b>	<b><u>46.5</u></b>
433.milc	122	75.5	<b><u>122</u></b>	<b><u>75.6</u></b>	121	75.6	121	75.8	<b><u>121</u></b>	<b><u>75.8</u></b>	121	75.7
434.zeusmp	48.4	188	<b><u>48.6</u></b>	<b><u>187</u></b>	48.6	187	48.4	188	<b><u>48.6</u></b>	<b><u>187</u></b>	48.6	187
435.gromacs	137	52.1	137	52.1	<b><u>137</u></b>	<b><u>52.1</u></b>	137	52.1	137	52.1	<b><u>137</u></b>	<b><u>52.1</u></b>
436.cactusADM	17.1	699	<b><u>16.9</u></b>	<b><u>707</u></b>	16.9	707	17.1	699	<b><u>16.9</u></b>	<b><u>707</u></b>	16.9	707
437.leslie3d	<b><u>31.4</u></b>	<b><u>299</u></b>	31.8	295	31.2	301	<b><u>31.4</u></b>	<b><u>299</u></b>	31.8	295	31.2	301
444.namd	277	28.9	277	28.9	<b><u>277</u></b>	<b><u>28.9</u></b>	268	29.9	<b><u>268</u></b>	<b><u>29.9</u></b>	269	29.9
447.dealII	185	61.8	185	61.7	<b><u>185</u></b>	<b><u>61.8</u></b>	185	61.8	185	61.7	<b><u>185</u></b>	<b><u>61.8</u></b>
450.soplex	187	44.5	<b><u>187</u></b>	<b><u>44.7</u></b>	186	44.8	187	44.5	<b><u>187</u></b>	<b><u>44.7</u></b>	186	44.8
453.povray	86.1	61.8	<b><u>86.6</u></b>	<b><u>61.5</u></b>	86.6	61.5	75.6	70.3	<b><u>75.7</u></b>	<b><u>70.3</u></b>	76.1	69.9
454.calculix	157	52.5	<b><u>157</u></b>	<b><u>52.4</u></b>	158	52.3	144	57.1	144	57.1	<b><u>144</u></b>	<b><u>57.1</u></b>
459.GemsFDTD	50.6	210	<b><u>50.6</u></b>	<b><u>210</u></b>	50.8	209	<b><u>46.3</u></b>	<b><u>229</u></b>	46.3	229	46.1	230
465.tonto	199	49.5	200	49.3	<b><u>200</u></b>	<b><u>49.3</u></b>	177	55.7	<b><u>176</u></b>	<b><u>55.8</u></b>	176	55.8
470.lbm	<b><u>23.0</u></b>	<b><u>597</u></b>	23.4	587	22.8	602	<b><u>23.0</u></b>	<b><u>597</u></b>	23.4	587	22.8	602
481.wrf	111	101	<b><u>110</u></b>	<b><u>101</u></b>	110	102	111	101	<b><u>110</u></b>	<b><u>101</u></b>	110	102
482.sphinx3	225	86.6	227	86.0	<b><u>226</u></b>	<b><u>86.3</u></b>	224	87.1	225	86.8	<b><u>224</u></b>	<b><u>86.8</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Cluster On Die set to Disabled  
Early Snoop set to Disabled  
Performance Profile set to Custom  
ClE Support set to Disabled  
Core C3 set to Disabled  
Core C6 set to Disabled  
Thermal Profile set to High Fan Speed  
Memory Power Savings set to Disabled  
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECfp2006 = 113

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

SPECfp\_base2006 = 109

CPU2006 license: 9017

Test date: Oct-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Sep-2014

Tested by: Lenovo Group Limited

Software Availability: Nov-2014

### Platform Notes (Continued)

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on RD550 Thu Oct 16 18:46:43 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v3 @ 3.40GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      264414876 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RD550 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 16 18:45
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  730G  15G  679G   3% /
```

Additional information from dmidecode:

BIOS LENOVO PB1TS110 10/06/2014

Memory:

16x 16 GB

1x Hynix Semiconductor HMA42GR7MFR4N-TF 16 GB 2133 MHz 2 rank

15x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2133 MHz 2 rank

8x NO DIMM NO DIMM

(End of data from sysinfo program)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

**SPECfp2006 = 113**

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

**SPECfp\_base2006 = 109**

**CPU2006 license:** 9017

**Test date:** Oct-2014

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Sep-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Nov-2014

## Platform Notes (Continued)

RD550 support 4 channels and 12 DIMMs per processor, total 8 channels and 24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

459.GemsFDTD: -DSPEC\_CPU\_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECfp2006 = 113**

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

**SPECfp\_base2006 = 109**

**CPU2006 license:** 9017

**Test date:** Oct-2014

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Sep-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Nov-2014

## Base Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

**SPECfp2006 = 113**

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

**SPECfp\_base2006 = 109**

**CPU2006 license:** 9017

**Test date:** Oct-2014

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Sep-2014

**Tested by:** Lenovo Group Limited

**Software Availability:** Nov-2014

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Lenovo Group Limited

Lenovo ThinkServer RD550 (Intel Xeon E5-2643 v3, 3.40 GHz)

**SPECfp2006 = 113**

**SPECfp\_base2006 = 109**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Group Limited

**Tested by:** Lenovo Group Limited

**Test date:** Oct-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Nov-2014

## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Nov 18 16:33:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 18 November 2014.