



SPEC® CFP2006 Result

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Lenovo Group Limited

IBM System x iDataPlex dx360 M4
(Intel Xeon E5-2650L v2, 1.70 GHz)

SPECfp®_rate2006 = 470

SPECfp_rate_base2006 = 460

CPU2006 license: 9017

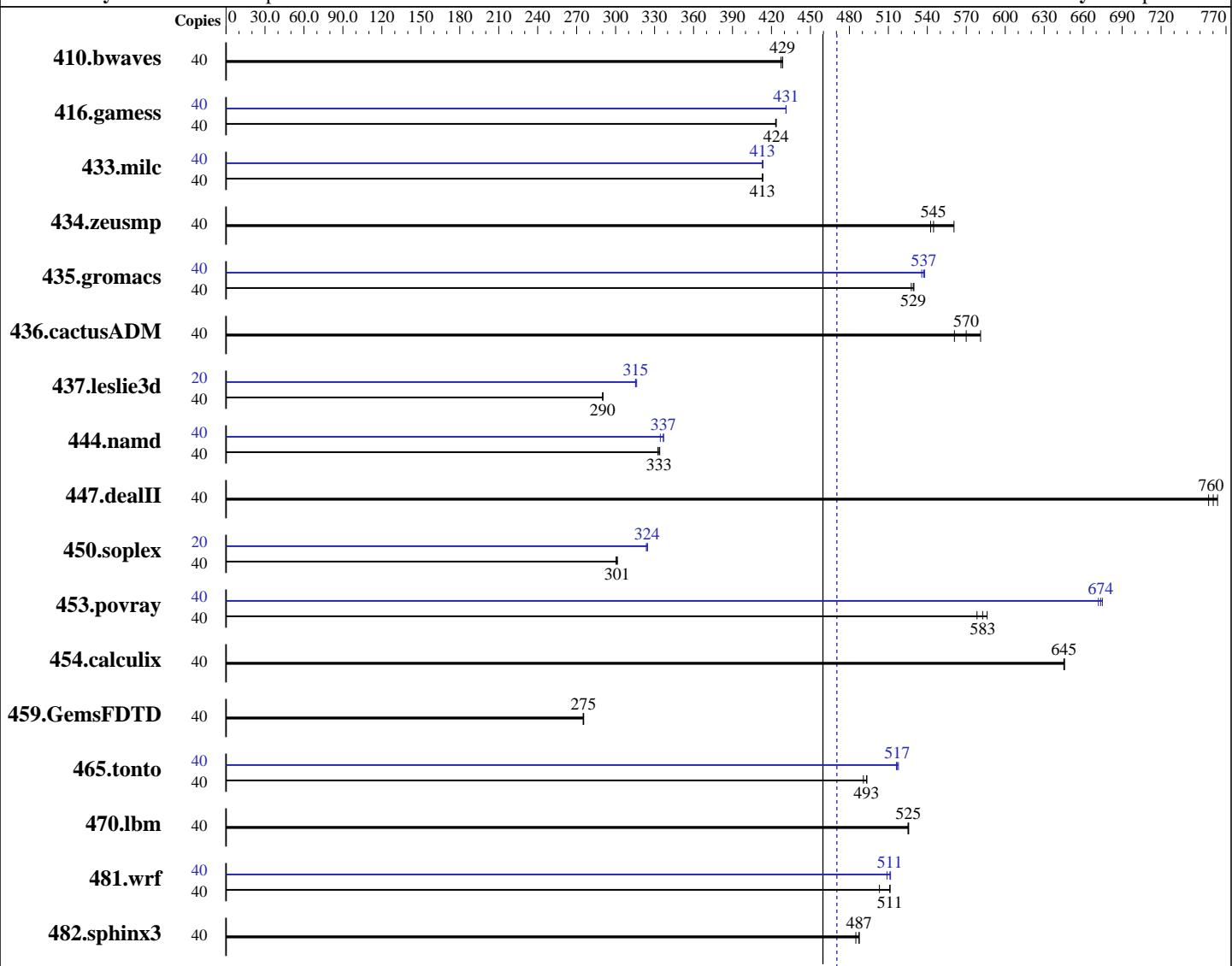
Test sponsor: Lenovo Group Limited

Tested by: IBM Corporation

Test date: Oct-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



SPECfp_rate_base2006 = 460

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Hardware

CPU Name: Intel Xeon E5-2650L v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz
CPU MHz: 1700
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
Compiler: 2.6.32-358.el6.x86_64
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|------------|------------|-------------|------------|-------------|------------|--------|------------|------------|-------------|------------|-------------|------------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 40 | 1268 | 429 | <u>1268</u> | <u>429</u> | 1272 | 427 | 40 | 1268 | 429 | <u>1268</u> | <u>429</u> | 1272 | 427 | | |
| 416.gamess | 40 | 1851 | 423 | <u>1849</u> | <u>424</u> | 1849 | 424 | 40 | 1816 | 431 | 1817 | 431 | <u>1816</u> | <u>431</u> | | |
| 433.milc | 40 | <u>889</u> | <u>413</u> | 889 | 413 | 888 | 413 | 40 | <u>889</u> | <u>413</u> | 889 | 413 | 888 | 414 | | |
| 434.zeusmp | 40 | <u>668</u> | <u>545</u> | 649 | 561 | 671 | 543 | 40 | <u>668</u> | <u>545</u> | 649 | 561 | 671 | 543 | | |
| 435.gromacs | 40 | 541 | 528 | <u>540</u> | <u>529</u> | 539 | 530 | 40 | 533 | 536 | 531 | 538 | <u>532</u> | <u>537</u> | | |
| 436.cactusADM | 40 | <u>839</u> | <u>570</u> | 823 | 581 | 852 | 561 | 40 | <u>839</u> | <u>570</u> | 823 | 581 | 852 | 561 | | |
| 437.leslie3d | 40 | 1296 | 290 | 1297 | 290 | <u>1296</u> | <u>290</u> | 20 | 595 | 316 | <u>596</u> | <u>315</u> | 596 | 315 | | |
| 444.namd | 40 | 961 | 334 | <u>962</u> | <u>333</u> | 965 | 333 | 40 | 952 | 337 | <u>953</u> | <u>337</u> | 959 | 334 | | |
| 447.dealII | 40 | <u>602</u> | <u>760</u> | 605 | 757 | 599 | 763 | 40 | <u>602</u> | <u>760</u> | 605 | 757 | 599 | 763 | | |
| 450.soplex | 40 | 1111 | 300 | <u>1108</u> | <u>301</u> | 1107 | 301 | 20 | 514 | 325 | 515 | 324 | <u>514</u> | <u>324</u> | | |
| 453.povray | 40 | <u>365</u> | <u>583</u> | 363 | 586 | 368 | 578 | 40 | <u>316</u> | <u>674</u> | 315 | 675 | 317 | 672 | | |
| 454.calculix | 40 | 511 | 646 | <u>511</u> | <u>645</u> | 512 | 645 | 40 | 511 | 646 | <u>511</u> | <u>645</u> | 512 | 645 | | |
| 459.GemsFDTD | 40 | 1541 | 275 | 1543 | 275 | <u>1542</u> | <u>275</u> | 40 | 1541 | 275 | 1543 | 275 | <u>1542</u> | <u>275</u> | | |
| 465.tonto | 40 | 798 | 493 | 802 | 491 | <u>798</u> | <u>493</u> | 40 | <u>762</u> | <u>517</u> | 760 | 518 | 763 | 516 | | |
| 470.lbm | 40 | 1047 | 525 | 1046 | 526 | <u>1046</u> | <u>525</u> | 40 | 1047 | 525 | 1046 | 526 | <u>1046</u> | <u>525</u> | | |
| 481.wrf | 40 | 874 | 511 | <u>874</u> | <u>511</u> | 888 | 503 | 40 | 878 | 509 | 873 | 512 | <u>874</u> | <u>511</u> | | |
| 482.sphinx3 | 40 | 1599 | 488 | <u>1599</u> | <u>487</u> | 1608 | 485 | 40 | 1599 | 488 | <u>1599</u> | <u>487</u> | 1608 | 485 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:
intel_idle.max_cstate=0



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Platform Notes

BIOS setting:

```
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6874
$Rev: 6874 $ $Date:: 2013-11-20 ## 654bd3fcf53b06faef0efe54ed011998
running on dx360M4 Sat Oct 25 10:03:12 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v2 @ 1.70GHz
        2 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 10
        siblings : 20
        physical 0: cores 0 1 2 3 4 8 9 10 11 12
        physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      264640496 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux dx360M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 24 16:26
```

```
SPEC is set to: /home/SPECcpu-20140116-ic14.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_td2-lv_home
                  ext4   380G  174G  187G  49%  /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS IBM -[TDE139OUS-1.50]- 02/21/2014

Memory:

16x Samsung M393B2G70QH0-CMA 16 GB 2 rank 1866 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/SPECcpu-20140116-ic14.0/lib32:/home/SPECcpu-20140116-ic14.0/lib64:/home/SPECcpu-20140116-ic14.0/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.xml>

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