



SPEC® CFP2006 Result

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Lenovo Group Limited

IBM System x3500 M4
(Intel Xeon E5-2620 v2, 2.10 GHz)

SPECfp®2006 = 75.8

SPECfp_base2006 = 73.0

CPU2006 license: 9017

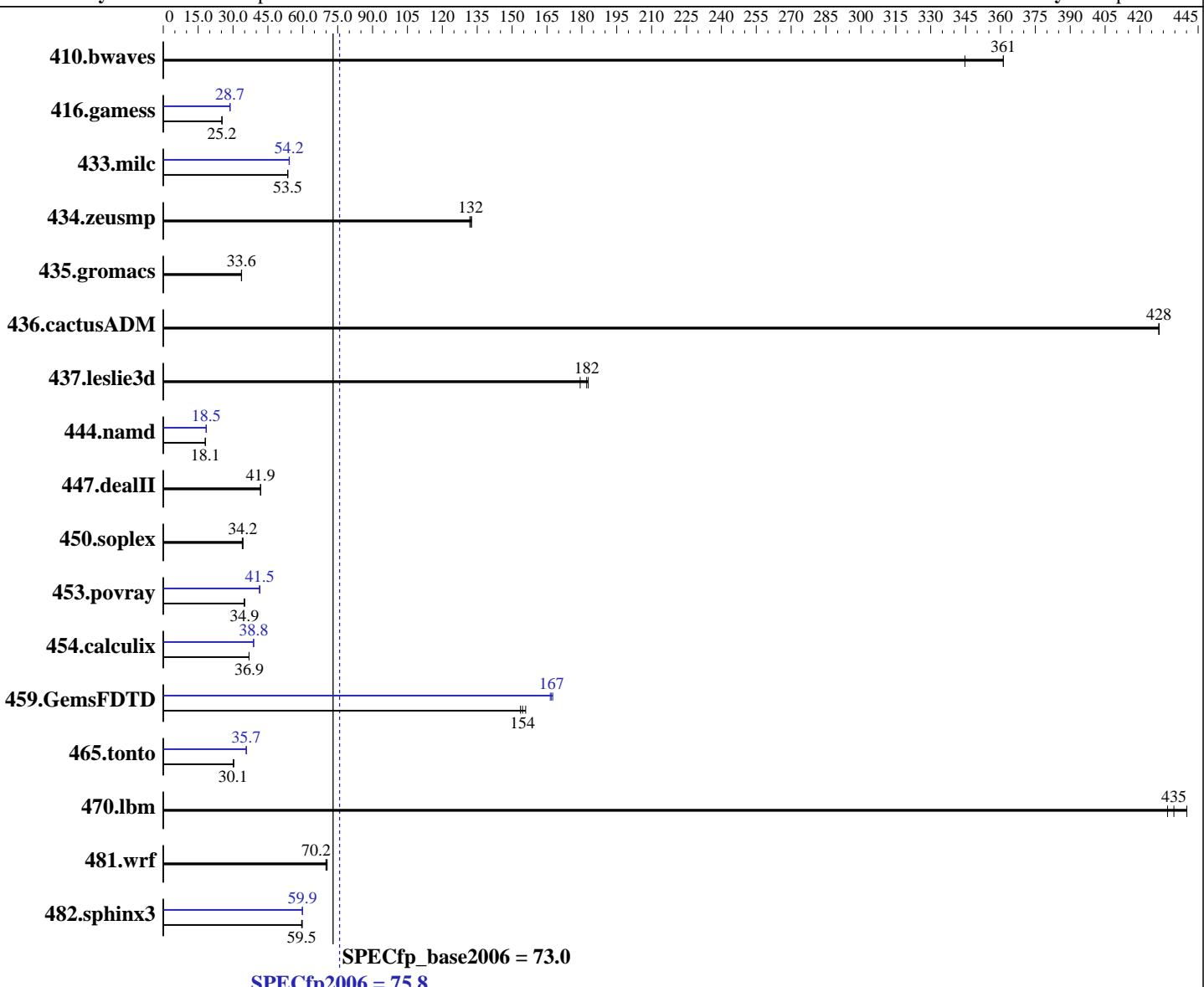
Test sponsor: Lenovo Group Limited

Tested by: IBM Corporation

Test date: Oct-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2620 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.60 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz)
Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	39.4	345	37.6	361	37.6	361	39.4	345	37.6	361	37.6	361
416.gamess	776	25.2	776	25.2	775	25.3	682	28.7	682	28.7	682	28.7
433.milc	172	53.5	172	53.5	171	53.6	169	54.2	169	54.2	170	54.1
434.zeusmp	68.6	133	69.0	132	68.8	132	68.6	133	69.0	132	68.8	132
435.gromacs	212	33.6	212	33.7	212	33.6	212	33.6	212	33.7	212	33.6
436.cactusADM	27.9	428	27.9	428	27.9	428	27.9	428	27.9	428	27.9	428
437.leslie3d	52.4	179	51.6	182	51.4	183	52.4	179	51.6	182	51.4	183
444.namd	443	18.1	443	18.1	443	18.1	434	18.5	434	18.5	434	18.5
447.dealII	273	41.9	273	41.8	273	41.9	273	41.9	273	41.8	273	41.9
450.soplex	244	34.2	245	34.0	244	34.2	244	34.2	245	34.0	244	34.2
453.povray	153	34.8	153	34.9	152	35.0	128	41.5	128	41.5	129	41.4
454.calculix	224	36.9	224	36.9	224	36.9	212	38.8	213	38.8	212	38.8
459.GemsFDTD	68.1	156	69.1	154	68.7	154	63.6	167	63.8	166	63.4	167
465.tonto	327	30.1	324	30.4	327	30.1	276	35.7	276	35.7	275	35.8
470.lbm	31.6	435	31.8	432	31.2	440	31.6	435	31.8	432	31.2	440
481.wrf	158	70.5	159	70.2	160	70.0	158	70.5	159	70.2	160	70.0
482.sphinx3	327	59.5	327	59.5	326	59.8	326	59.8	325	59.9	326	59.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:
intel_idle.max_cstate=0

Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Hyper-Threading set to Disable

Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6874

\$Rev: 6874 \$ \$Date::: 2013-11-20 ## 654bd3fcf53b06faef0efe54ed011998

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Platform Notes (Continued)

running on x3500M4 Sat Oct 4 15:25:59 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2620 v2 @ 2.10GHz
        2 "physical id"s (chips)
        12 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 6
        siblings   : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
    cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264656948 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3500M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 22 01:53
```

```
SPEC is set to: /home/SPECcpu-20140116-ic14.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_intelcrb-lv_home
                ext4   863G   40G  780G   5%  /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[Y5E139ZUS-1.70]- 06/25/2014

Memory:

8x Not Specified Not Specified

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Platform Notes (Continued)

16x Samsung M393B2G70QH0-CMA 16 GB 2 rank 1866 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,0,1"

LD_LIBRARY_PATH = "/home/SPECCpu-20140116-ic14.0/libs/32:/home/SPECCpu-20140116-ic14.0/libs/64:/home/SPECCpu-20140116-ic14.0/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

```

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Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.xml>

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