SPEC® CFP2006 Result

Sugon

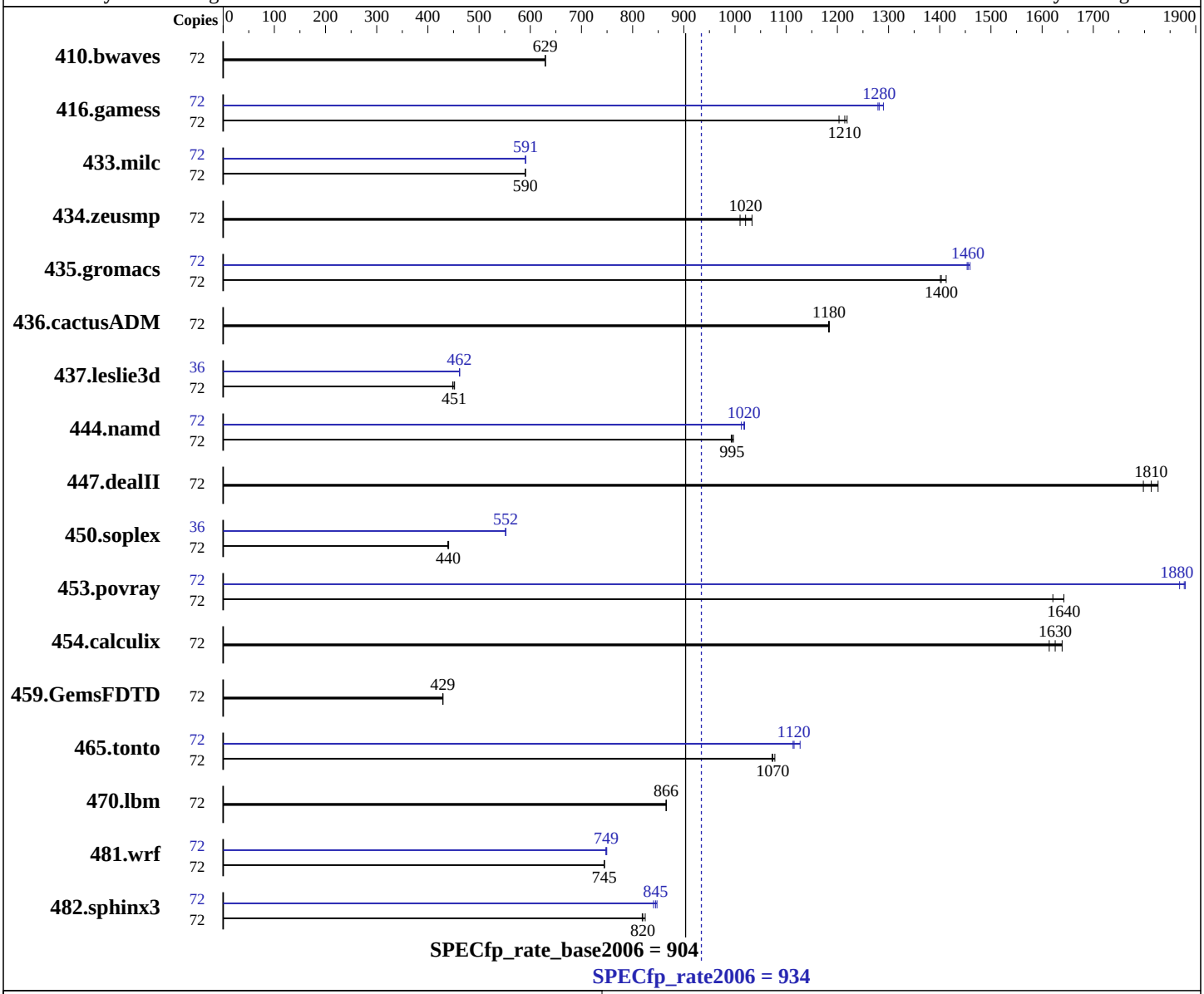
Sugon I620-G20 (Intel Xeon E5-2699 v3)

SPECfp®_rate2006 = 934

SPECfp_rate_base2006 = 904

CPU2006 license: 9046
Test sponsor: Sugon
Tested by: Sugon

Test date: Oct-2014
Hardware Availability: Sep-2014
Software Availability: Aug-2014



Hardware

CPU Name:	Intel Xeon E5-2699 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	36 cores, 2 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache:	45 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 2.0 TB SATA Disk	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	72	1555	629	**1554**	**629**	1554	630	72	1555	629	**1554**	**629**	1554	630
416.gamess	72	**1161**	**1210**	1172	1200	1156	1220	72	**1100**	**1280**	1102	1280	1093	1290
433.milc	72	1120	590	**1120**	**590**	1119	591	72	1119	591	**1119**	**591**	1119	591
434.zeusmp	72	**642**	**1020**	649	1010	634	1030	72	**642**	**1020**	649	1010	634	1030
435.gromacs	72	364	1410	367	1400	**366**	**1400**	72	**353**	**1460**	354	1450	352	1460
436.cactusADM	72	**727**	**1180**	727	1180	727	1180	72	**727**	**1180**	727	1180	727	1180
437.leslie3d	72	1497	452	1509	448	**1500**	**451**	36	733	462	**733**	**462**	732	462
444.namd	72	579	997	582	993	**581**	**995**	72	**568**	**1020**	570	1010	567	1020
447.dealII	72	458	1800	451	1830	**454**	**1810**	72	458	1800	451	1830	**454**	**1810**
450.soplex	72	**1365**	**440**	1368	439	1363	441	36	544	552	**544**	**552**	544	552
453.povray	72	233	1640	**233**	**1640**	236	1620	72	204	1880	**204**	**1880**	205	1870
454.calculix	72	368	1610	362	1640	**365**	**1630**	72	368	1610	362	1640	**365**	**1630**
459.GemsFDTD	72	1780	429	**1780**	**429**	1779	429	72	1780	429	**1780**	**429**	1779	429
465.tonto	72	**660**	**1070**	657	1080	661	1070	72	636	1110	629	1130	**635**	**1120**
470.lbm	72	**1143**	**866**	1143	866	1143	865	72	**1143**	**866**	1143	866	1143	865
481.wrf	72	**1080**	**745**	1080	745	1081	744	72	**1074**	**749**	1076	748	1073	749
482.sphinx3	72	1702	825	**1712**	**820**	1714	819	72	**1661**	**845**	1669	841	1656	848

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Enforce POR set to disabled
Memory Frequency set to 2133
Early Snoop set to disabled
```

Continued on next page

Platform Notes (Continued)

```
COD set to enable
Power Technology set to performance
 Sysinfo program /home/cpu2006/config/sysinfo.rev6874
 $Rev: 6874 $ $Date:: 2013-11-20 #$ 654bd3fcf53b06faef0efe54ed011998
 running on localhost Mon Oct 20 07:33:51 2014

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz
       2 "physical id"s (chips)
       72 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 18
       siblings  : 36
       physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
       physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    cache size : 23040 KB

 From /proc/meminfo
    MemTotal:       264473908 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)

 From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
    Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
    x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 Oct 20 07:29

 SPEC is set to: /home/cpu2006
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda3      ext4  1.8T  458G  1.3T  27% /
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.
```

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Platform Notes (Continued)

```
    BIOS American Megatrends Inc. 068 08/15/2014
    Memory:
     16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz
     8x NO DIMM NO DIMM

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

Submitted_by: Tian Yuwan <tianyw@sugon.com>
Submitted: Wed Oct 22 23:30:22 EDT 2014
Submission: cpu2006-20141022-32562.sub
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

Continued on next page

Peak Optimization Flags (Continued)

450.soplex (continued):
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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