



# SPEC® CFP2006 Result

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**Huawei**

**SPECfp®2006 = 90.3**

Huawei RH2285 V3 (Intel Xeon E5-2680 v3)

**SPECfp\_base2006 = 85.7**

CPU2006 license: 3175

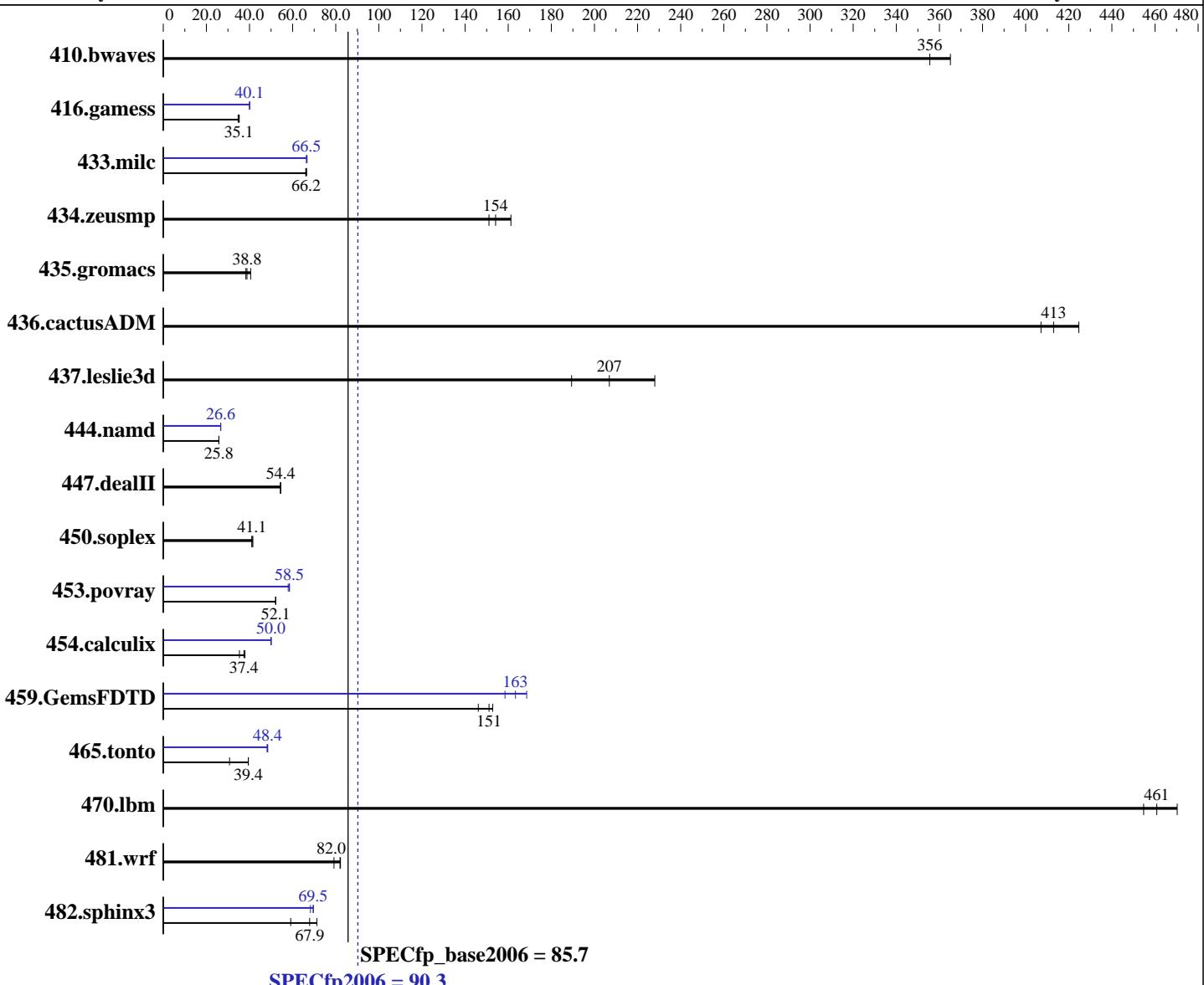
Test date: Jan-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Nov-2013



## Hardware

CPU Name: Intel Xeon E5-2680 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
CPU MHz: 2500  
FPU: Integrated  
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
Compiler: 2.6.32-431.el6.x86\_64  
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio								
410.bwaves	<b>38.2</b>	<b>356</b>	37.2	365	38.2	356	<b>38.2</b>	<b>356</b>	37.2	365	38.2	356
416.gamess	556	35.2	564	34.7	<b>558</b>	<b>35.1</b>	489	40.0	<b>489</b>	<b>40.1</b>	489	40.1
433.milc	<b>139</b>	<b>66.2</b>	138	66.5	139	66.1	<b>138</b>	<b>66.4</b>	<b>138</b>	<b>66.5</b>	138	66.7
434.zeusmp	60.2	151	56.4	161	<b>59.0</b>	<b>154</b>	60.2	151	56.4	161	<b>59.0</b>	<b>154</b>
435.gromacs	<b>184</b>	<b>38.8</b>	186	38.3	176	40.5	<b>184</b>	<b>38.8</b>	186	38.3	176	40.5
436.cactusADM	28.1	425	<b>28.9</b>	<b>413</b>	29.4	407	28.1	425	<b>28.9</b>	<b>413</b>	29.4	407
437.leslie3d	41.2	228	<b>45.4</b>	<b>207</b>	49.6	189	41.2	228	<b>45.4</b>	<b>207</b>	49.6	189
444.namd	311	25.8	311	25.8	<b>311</b>	<b>25.8</b>	<b>301</b>	<b>26.6</b>	301	26.6	301	26.6
447.dealII	<b>210</b>	<b>54.4</b>	210	54.4	210	54.4	<b>210</b>	<b>54.4</b>	210	54.4	210	54.4
450.soplex	<b>203</b>	<b>41.1</b>	203	41.1	201	41.6	<b>203</b>	<b>41.1</b>	203	41.1	201	41.6
453.povray	102	52.1	<b>102</b>	<b>52.1</b>	102	52.2	<b>90.9</b>	<b>58.5</b>	91.8	57.9	90.9	58.5
454.calculix	233	35.3	<b>221</b>	<b>37.4</b>	218	37.9	<b>165</b>	<b>50.0</b>	165	50.0	165	50.0
459.GemsFDTD	69.4	153	72.6	146	<b>70.2</b>	<b>151</b>	62.9	169	<b>64.9</b>	<b>163</b>	66.9	159
465.tonto	320	30.7	249	39.5	<b>250</b>	<b>39.4</b>	204	48.1	<b>203</b>	<b>48.4</b>	203	48.4
470.lbm	29.2	470	<b>29.8</b>	<b>461</b>	30.2	455	29.2	470	<b>29.8</b>	<b>461</b>	30.2	455
481.wrf	141	79.2	<b>136</b>	<b>82.0</b>	136	82.2	141	79.2	<b>136</b>	<b>82.0</b>	136	82.2
482.sphinx3	330	59.1	<b>287</b>	<b>67.9</b>	273	71.3	<b>280</b>	<b>69.5</b>	280	69.6	285	68.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to HS

Set Hyper-Threading to Disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on localhost Thu Jan 16 05:29:06 2014

This section contains SUT (System Under Test) info as seen by

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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz
        2 "physical id"s (chips)
        24 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 12
        siblings : 12
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
    cache size : 30720 KB
```

```
From /proc/meminfo
    MemTotal:      264276292 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
    Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
    x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 15 12:52
```

```
SPEC is set to: /spec
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sdal      ext4   433G   98G  314G  24% /
```

Additional information from dmidecode:

```
BIOS Insyde Corp. 1.13 08/01/2014
Memory:
    8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 1 rank
    8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"  
OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel
```

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.1.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.1.xml>

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