



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

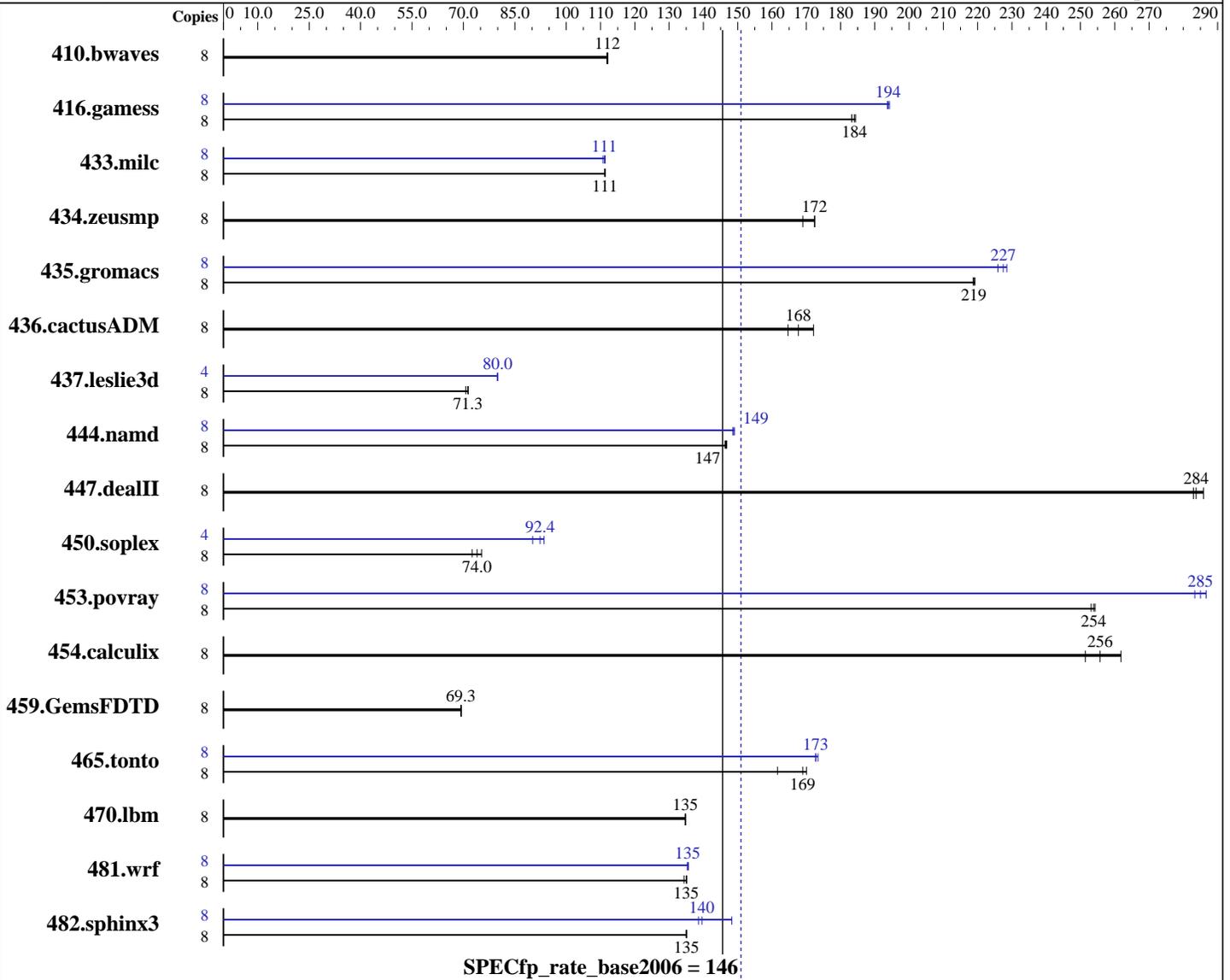
Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E3-1231 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext2  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014

L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11)  
Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	969	112	<b>970</b>	<b>112</b>	972	112	8	969	112	<b>970</b>	<b>112</b>	972	112
416.gamess	8	<b>851</b>	<b>184</b>	850	184	855	183	8	809	194	806	194	<b>808</b>	<b>194</b>
433.milc	8	659	111	<b>660</b>	<b>111</b>	660	111	8	659	111	<b>661</b>	<b>111</b>	663	111
434.zeusmp	8	<b>422</b>	<b>172</b>	422	172	431	169	8	<b>422</b>	<b>172</b>	422	172	431	169
435.gromacs	8	261	219	261	219	<b>261</b>	<b>219</b>	8	253	226	<b>251</b>	<b>227</b>	250	229
436.cactusADM	8	555	172	<b>570</b>	<b>168</b>	580	165	8	555	172	<b>570</b>	<b>168</b>	580	165
437.leslie3d	8	1064	70.7	1053	71.4	<b>1055</b>	<b>71.3</b>	4	<b>470</b>	<b>80.0</b>	471	79.9	470	80.0
444.namd	8	437	147	<b>438</b>	<b>147</b>	438	146	8	432	149	430	149	<b>431</b>	<b>149</b>
447.dealII	8	320	286	<b>323</b>	<b>284</b>	323	283	8	320	286	<b>323</b>	<b>284</b>	323	283
450.soplex	8	<b>902</b>	<b>74.0</b>	920	72.5	886	75.3	4	370	90.2	<b>361</b>	<b>92.4</b>	357	93.5
453.povray	8	167	254	168	253	<b>168</b>	<b>254</b>	8	150	283	148	287	<b>149</b>	<b>285</b>
454.calculix	8	<b>258</b>	<b>256</b>	263	251	252	262	8	<b>258</b>	<b>256</b>	263	251	252	262
459.GemsFDTD	8	<b>1226</b>	<b>69.3</b>	1226	69.2	1223	69.4	8	<b>1226</b>	<b>69.3</b>	1226	69.2	1223	69.4
465.tonto	8	463	170	487	162	<b>466</b>	<b>169</b>	8	456	173	<b>456</b>	<b>173</b>	454	173
470.lbm	8	<b>816</b>	<b>135</b>	816	135	816	135	8	<b>816</b>	<b>135</b>	816	135	816	135
481.wrf	8	661	135	665	134	<b>662</b>	<b>135</b>	8	659	136	660	135	<b>660</b>	<b>135</b>
482.sphinx3	8	<b>1154</b>	<b>135</b>	1154	135	1155	135	8	1051	148	1125	139	<b>1117</b>	<b>140</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:  
Virtualization Technology disabled  
Execute Disable disabled

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014

## Platform Notes (Continued)

System Profile set to Performance  
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818  
 \$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
 running on linux Thu Oct 9 04:03:01 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1231 v3 @ 3.40GHz
 1 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
  cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      32809896 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 8 18:55 last=S

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  267G   16G  251G   6% /
```

```
Additional information from dmidecode:
BIOS Dell Inc. 1.3.2 09/11/2014
Memory:
4x Hynix/Hyundai HMT41GU7AFR8A-PB 8 GB 1600 MHz
```

(End of data from sysinfo program)



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-ansi-alias -opt-mem-layout-trans=3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014

## Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014

## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3  
-unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 151

PowerEdge R220 (Intel Xeon E3-1231 v3, 3.40 GHz)

SPECfp\_rate\_base2006 = 146

CPU2006 license: 55

Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Sep-2014

## Peak Optimization Flags (Continued)

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Nov 5 10:23:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 4 November 2014.