# SPEC® CFP2006 Result

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## Dell Inc.

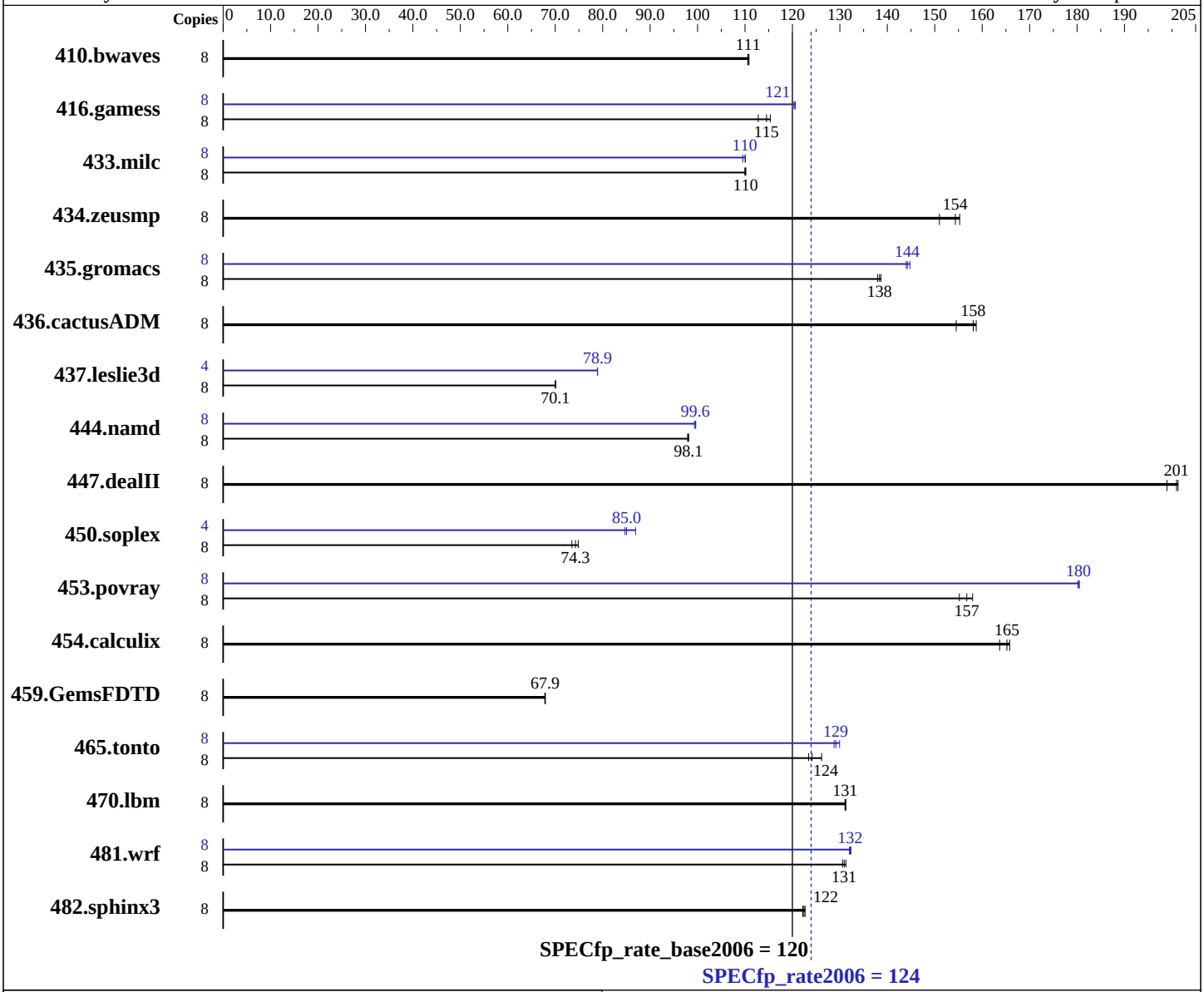
PowerEdge R220 (Intel Xeon E3-1240L v3, 2.00 GHz)

SPECfp®_rate2006 = 124

SPECfp_rate_base2006 = 120

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Oct-2014
**Hardware Availability:** Mar-2014
**Software Availability:** Sep-2014




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3-1240L v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.00 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext2 |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11)
Disk Subsystem: 1 x 300 GB 15000 RPM SAS
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | **982** | **111** | 981 | 111 | 983 | 111 | 8 | **982** | **111** | 981 | 111 | 983 | 111 |
| 416.gamess | 8 | 1358 | 115 | **1367** | **115** | 1389 | 113 | 8 | 1302 | 120 | 1299 | 121 | **1300** | **121** |
| 433.milc | 8 | 667 | 110 | **667** | **110** | 668 | 110 | 8 | 667 | 110 | 670 | 110 | **668** | **110** |
| 434.zeusmp | 8 | 469 | 155 | 482 | 151 | **472** | **154** | 8 | 469 | 155 | 482 | 151 | **472** | **154** |
| 435.gromacs | 8 | **413** | **138** | 412 | 139 | 414 | 138 | 8 | 397 | 144 | 395 | 145 | **396** | **144** |
| 436.cactusADM | 8 | **605** | **158** | 619 | 155 | 602 | 159 | 8 | **605** | **158** | 619 | 155 | 602 | 159 |
| 437.leslie3d | 8 | 1074 | 70.0 | 1073 | 70.1 | **1073** | **70.1** | 4 | 476 | 78.9 | **477** | **78.9** | 477 | 78.9 |
| 444.namd | 8 | 654 | 98.1 | 655 | 97.9 | **654** | **98.1** | 8 | 646 | 99.3 | 644 | 99.6 | **644** | **99.6** |
| 447.dealII | 8 | 460 | 199 | 455 | 201 | **455** | **201** | 8 | 460 | 199 | 455 | 201 | **455** | **201** |
| 450.soplex | 8 | 891 | 74.8 | **898** | **74.3** | 908 | 73.5 | 4 | 394 | 84.7 | **392** | **85.0** | 384 | 86.9 |
| 453.povray | 8 | 269 | 158 | **272** | **157** | 274 | 155 | 8 | **236** | **180** | 236 | 180 | 236 | 180 |
| 454.calculix | 8 | 398 | 166 | 403 | 164 | **399** | **165** | 8 | 398 | 166 | 403 | 164 | **399** | **165** |
| 459.GemsFDTD | 8 | **1251** | **67.9** | 1252 | 67.8 | 1250 | 67.9 | 8 | **1251** | **67.9** | 1252 | 67.8 | 1250 | 67.9 |
| 465.tonto | 8 | 638 | 123 | **634** | **124** | 624 | 126 | 8 | 606 | 130 | **609** | **129** | 611 | 129 |
| 470.lbm | 8 | 839 | 131 | **838** | **131** | 837 | 131 | 8 | 839 | 131 | **838** | **131** | 837 | 131 |
| 481.wrf | 8 | **683** | **131** | 681 | 131 | 684 | 131 | 8 | 675 | 132 | 677 | 132 | **676** | **132** |
| 482.sphinx3 | 8 | **1274** | **122** | 1276 | 122 | 1271 | 123 | 8 | **1274** | **122** | 1276 | 122 | 1271 | 123 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS Configuration:
Virtualization Technology disabled
Execute Disable disabled
```

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## Platform Notes (Continued)

```
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux Thu Oct  9 07:17:49 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1240L v3 @ 2.00GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       32809896 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 8 18:59 last=S

SPEC is set to: /root/cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext2  267G   19G  247G   8% /

Additional information from dmidecode:
  BIOS Dell Inc. 1.3.2 09/11/2014
  Memory:
   4x Hynix/Hyundai HMT41GU7AFR8A-PB 8 GB 1600 MHz

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Continued on next page

## Base Optimization Flags (Continued)

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

## Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

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# Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml


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