



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **101**

## Huawei CH222 (Intel Xeon E5-2697 v2)

SPECfp\_base2006 = **96.5**

CPU2006 license: 3175

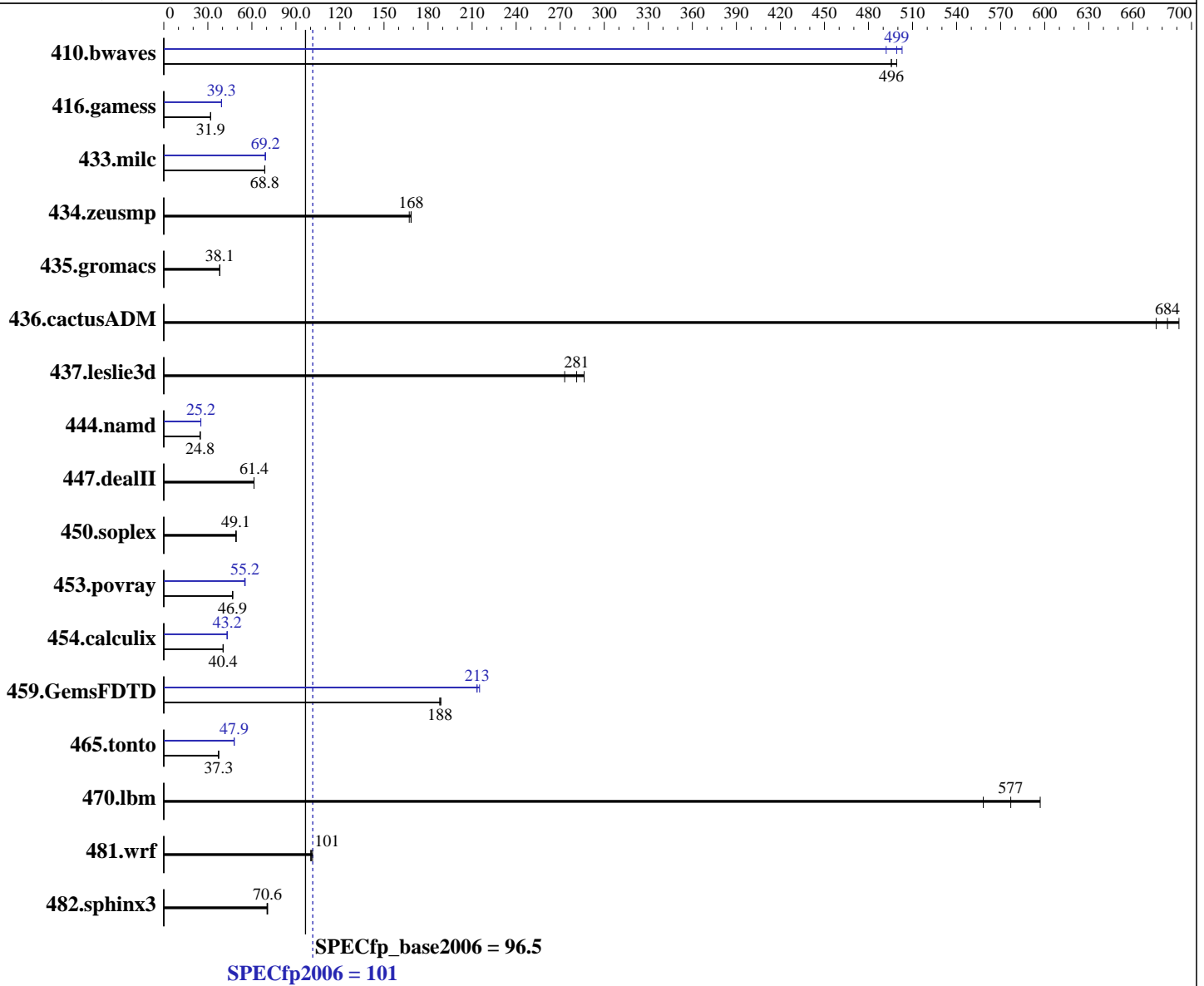
Test sponsor: Huawei

Tested by: Huawei

Test date: Oct-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2697 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	27.4	496	27.2	499	<b><u>27.4</u></b>	<b><u>496</u></b>	<b><u>27.2</u></b>	<b><u>499</u></b>	27.0	503	27.6	492
416.gamess	<b><u>614</u></b>	<b><u>31.9</u></b>	613	31.9	614	31.9	498	39.3	<b><u>498</u></b>	<b><u>39.3</u></b>	498	39.3
433.milc	134	68.7	<b><u>134</u></b>	<b><u>68.8</u></b>	133	68.8	<b><u>133</u></b>	<b><u>69.2</u></b>	132	69.3	133	69.1
434.zeusmp	54.4	167	54.0	168	<b><u>54.0</u></b>	<b><u>168</u></b>	54.4	167	54.0	168	<b><u>54.0</u></b>	<b><u>168</u></b>
435.gromacs	187	38.1	187	38.2	<b><u>187</u></b>	<b><u>38.1</u></b>	187	38.1	187	38.2	<b><u>187</u></b>	<b><u>38.1</u></b>
436.cactusADM	17.7	676	<b><u>17.5</u></b>	<b><u>684</u></b>	17.3	691	17.7	676	<b><u>17.5</u></b>	<b><u>684</u></b>	17.3	691
437.leslie3d	<b><u>33.4</u></b>	<b><u>281</u></b>	34.4	273	32.8	286	<b><u>33.4</u></b>	<b><u>281</u></b>	34.4	273	32.8	286
444.namd	<b><u>324</u></b>	<b><u>24.8</u></b>	323	24.8	324	24.8	318	25.2	<b><u>318</u></b>	<b><u>25.2</u></b>	318	25.2
447.dealII	186	61.5	<b><u>186</u></b>	<b><u>61.4</u></b>	186	61.4	186	61.5	<b><u>186</u></b>	<b><u>61.4</u></b>	186	61.4
450.soplex	<b><u>170</u></b>	<b><u>49.1</u></b>	170	49.1	169	49.3	<b><u>170</u></b>	<b><u>49.1</u></b>	170	49.1	169	49.3
453.povray	<b><u>114</u></b>	<b><u>46.9</u></b>	113	47.0	114	46.7	96.6	55.1	<b><u>96.3</u></b>	<b><u>55.2</u></b>	96.0	55.4
454.calculix	204	40.4	<b><u>204</u></b>	<b><u>40.4</u></b>	204	40.5	<b><u>191</u></b>	<b><u>43.2</u></b>	191	43.1	191	43.2
459.GemsFDTD	56.2	189	<b><u>56.4</u></b>	<b><u>188</u></b>	56.4	188	<b><u>49.7</u></b>	<b><u>213</u></b>	49.7	213	49.3	215
465.tonto	<b><u>264</u></b>	<b><u>37.3</u></b>	264	37.3	264	37.3	<b><u>205</u></b>	<b><u>47.9</u></b>	205	47.9	205	47.9
470.lbm	23.0	597	24.6	558	<b><u>23.8</u></b>	<b><u>577</u></b>	23.0	597	24.6	558	<b><u>23.8</u></b>	<b><u>577</u></b>
481.wrf	112	100	110	101	<b><u>111</u></b>	<b><u>101</u></b>	112	100	110	101	<b><u>111</u></b>	<b><u>101</u></b>
482.sphinx3	<b><u>276</u></b>	<b><u>70.6</u></b>	276	70.7	277	70.3	<b><u>276</u></b>	<b><u>70.6</u></b>	276	70.7	277	70.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Baseboard Management Controller used to adjust the fan speed to 100%  
 Set Hyper-Threading to Disabled  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on localhost Wed Oct 15 12:31:16 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Hardware Availability: Sep-2013

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
 2 "physical id"s (chips)
 24 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings   : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

From /proc/meminfo

```
MemTotal:      264478184 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 13 09:28

SPEC is set to: /spec

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  272G   33G  226G  13% /
```

Additional information from dmidecode:

Memory:

```
4x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
12x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "24"
```

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## General Notes (Continued)

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Nov 6 13:48:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 6 November 2014.