



# SPEC® CINT2006 Result

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## Huawei

SPECint®\_rate2006 = 970

### Huawei CH220 (Intel Xeon E5-2697 v2)

SPECint\_rate\_base2006 = 940

CPU2006 license: 3175

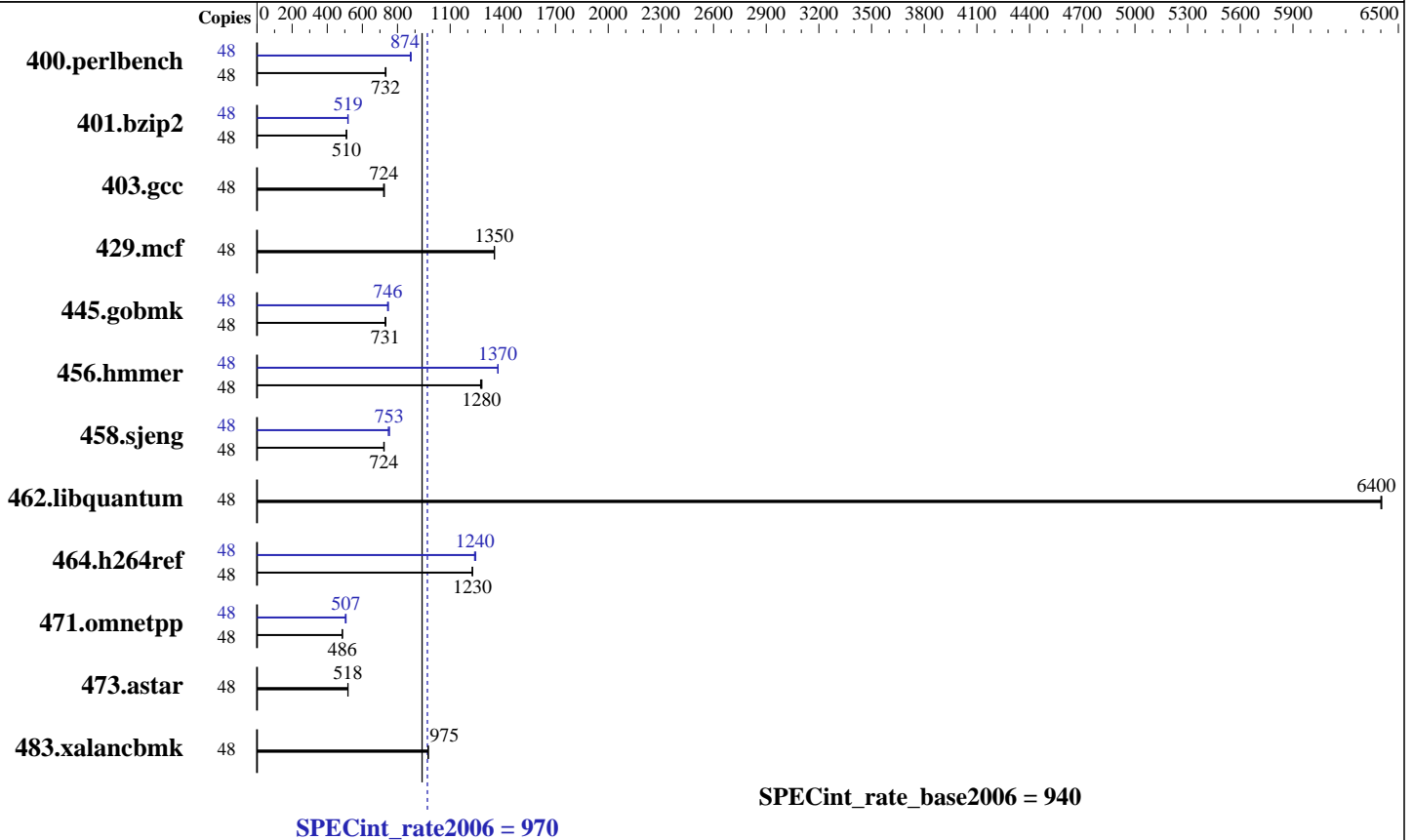
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2697 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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### Results Table

| Benchmark      | Base   |            |             |            |             |            | Peak        |        |            |             |            |             |            |             |
|----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
|                | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       |
| 400.perlbench  | 48     | <b>641</b> | <b>732</b>  | 638        | 735         | 641        | 731         | 48     | <b>537</b> | <b>874</b>  | 534        | 878         | 537        | 873         |
| 401.bzip2      | 48     | <b>908</b> | <b>510</b>  | 911        | 508         | 908        | 510         | 48     | 893        | 519         | 896        | 517         | <b>893</b> | <b>519</b>  |
| 403.gcc        | 48     | <b>534</b> | <b>724</b>  | 533        | 726         | 536        | 721         | 48     | <b>534</b> | <b>724</b>  | 533        | 726         | 536        | 721         |
| 429.mcf        | 48     | 324        | 1350        | 324        | 1350        | <b>324</b> | <b>1350</b> | 48     | 324        | 1350        | 324        | 1350        | <b>324</b> | <b>1350</b> |
| 445.gobmk      | 48     | <b>689</b> | <b>731</b>  | 686        | 734         | 689        | 730         | 48     | <b>675</b> | <b>746</b>  | 673        | 748         | 677        | 743         |
| 456.hammer     | 48     | 352        | 1270        | <b>350</b> | <b>1280</b> | 350        | 1280        | 48     | 326        | 1370        | <b>326</b> | <b>1370</b> | 327        | 1370        |
| 458.sjeng      | 48     | 805        | 722         | <b>802</b> | <b>724</b>  | 802        | 724         | 48     | 771        | 754         | <b>771</b> | <b>753</b>  | 777        | 748         |
| 462.libquantum | 48     | <b>155</b> | <b>6400</b> | 155        | 6400        | 155        | 6400        | 48     | <b>155</b> | <b>6400</b> | 155        | 6400        | 155        | 6400        |
| 464.h264ref    | 48     | <b>866</b> | <b>1230</b> | 865        | 1230        | 866        | 1230        | 48     | 857        | 1240        | 853        | 1250        | <b>855</b> | <b>1240</b> |
| 471.omnetpp    | 48     | 618        | 485         | <b>618</b> | <b>486</b>  | 616        | 487         | 48     | <b>592</b> | <b>507</b>  | 592        | 507         | 597        | 503         |
| 473.astar      | 48     | 650        | 518         | 652        | 517         | <b>650</b> | <b>518</b>  | 48     | 650        | 518         | 652        | 517         | <b>650</b> | <b>518</b>  |
| 483.xalancbmk  | 48     | 339        | 976         | <b>340</b> | <b>975</b>  | 340        | 974         | 48     | 339        | 976         | <b>340</b> | <b>975</b>  | 340        | 974         |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec14/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on localhost Wed Sep 24 21:37:23 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz  
2 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
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## Platform Notes (Continued)

```
caution.)
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 24 09:35
```

```
SPEC is set to: /spec14
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  272G   74G  184G  29% /
```

```
Additional information from dmidecode:
BIOS Insyde Corp. RMIBV629 05/12/2014
Memory:
4x Hynix HMT42GR7AFR4C-RD 16 GB 1866 MHz 2 rank
8x NO DIMM NO DIMM
12x Samsung M393B2G70DB0-CMA 16 GB 1866 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec14/libs/32:/spec14/libs/64:/spec14/sh"

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
```

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## General Notes (Continued)

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

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## Peak Compiler Invocation (Continued)

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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