



# SPEC® CINT2006 Result

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## Supermicro

SuperServer 6028R-WTR  
(X10DRW-iT , Intel Xeon E5-2699 v3)

**SPECint®2006 = 66.1**

**SPECint\_base2006 = 63.4**

**CPU2006 license:** 001176

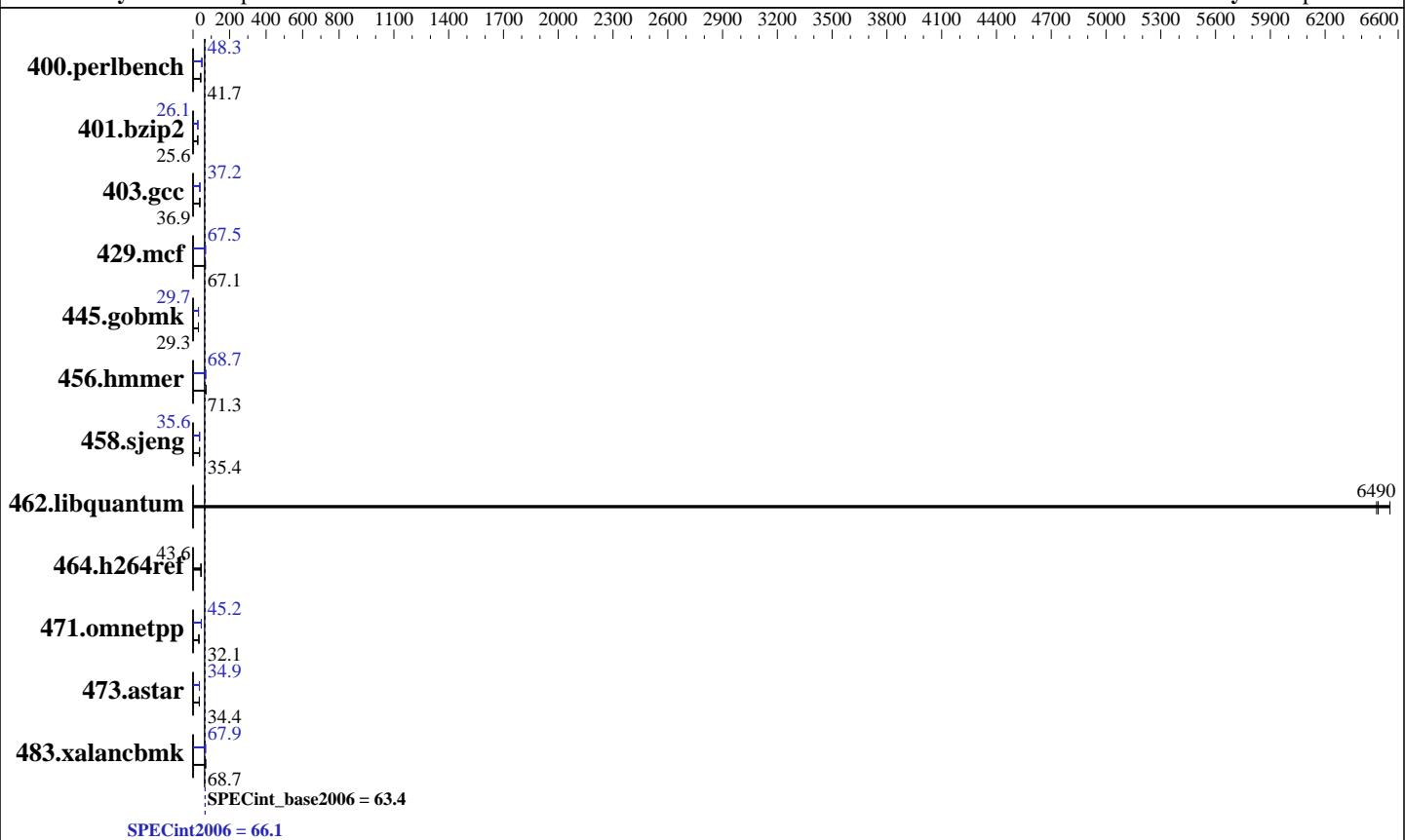
**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Oct-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2699 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
CPU MHz: 2300  
FPU: Integrated  
CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 45 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 3000 GB SATA III, 7200 RPM  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0, Kernel 3.10.0-123.8.1.el7.x86\_64  
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>234</b>	<b>41.7</b>	234	41.8	235	41.6	<b>202</b>	<b>48.3</b>	202	48.4	<b>202</b>	<b>48.3</b>
401.bzip2	378	25.6	376	25.7	<b>377</b>	<b>25.6</b>	369	26.1	<b>370</b>	<b>26.1</b>	370	26.1
403.gcc	218	36.9	218	36.9	<b>218</b>	<b>36.9</b>	<b>216</b>	<b>37.2</b>	216	37.2	216	37.3
429.mcf	134	68.1	137	66.3	<b>136</b>	<b>67.1</b>	136	67.3	133	68.4	<b>135</b>	<b>67.5</b>
445.gobmk	<b>357</b>	<b>29.3</b>	356	29.5	358	29.3	353	29.7	<b>353</b>	<b>29.7</b>	353	29.7
456.hammer	<b>131</b>	<b>71.3</b>	131	71.3	131	71.2	136	68.7	136	68.8	<b>136</b>	<b>68.7</b>
458.sjeng	342	35.3	<b>342</b>	<b>35.4</b>	342	35.4	340	35.6	<b>340</b>	<b>35.6</b>	340	35.6
462.libquantum	<b>3.19</b>	<b>6490</b>	3.20	6480	3.16	6560	<b>3.19</b>	<b>6490</b>	3.20	6480	3.16	6560
464.h264ref	507	43.7	<b>507</b>	<b>43.6</b>	508	43.6	507	43.7	<b>507</b>	<b>43.6</b>	508	43.6
471.omnetpp	192	32.5	196	31.9	<b>194</b>	<b>32.1</b>	139	45.0	<b>138</b>	<b>45.2</b>	138	45.3
473.astar	206	34.0	203	34.6	<b>204</b>	<b>34.4</b>	201	34.9	203	34.6	<b>201</b>	<b>34.9</b>
483.xalancbmk	<b>100</b>	<b>68.7</b>	102	67.7	100	68.8	<b>102</b>	<b>67.9</b>	102	67.8	101	68.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Early Snoop = Disabled

Enforce POR = Disabled

Hyper-Threading (ALL) = Disabled

Memory Frequency = 2133

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "36"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:

  icc -m64

C++ benchmarks:

  icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
  -Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

  403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

  icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hammer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hammer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

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## Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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