



# SPEC<sup>®</sup> CINT2006 Result

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## Huawei

SPECint<sup>®</sup>\_rate2006 = **858**

### Huawei CH222 (Intel Xeon E5-2680 v2)

SPECint\_rate\_base2006 = **830**

CPU2006 license: 3175

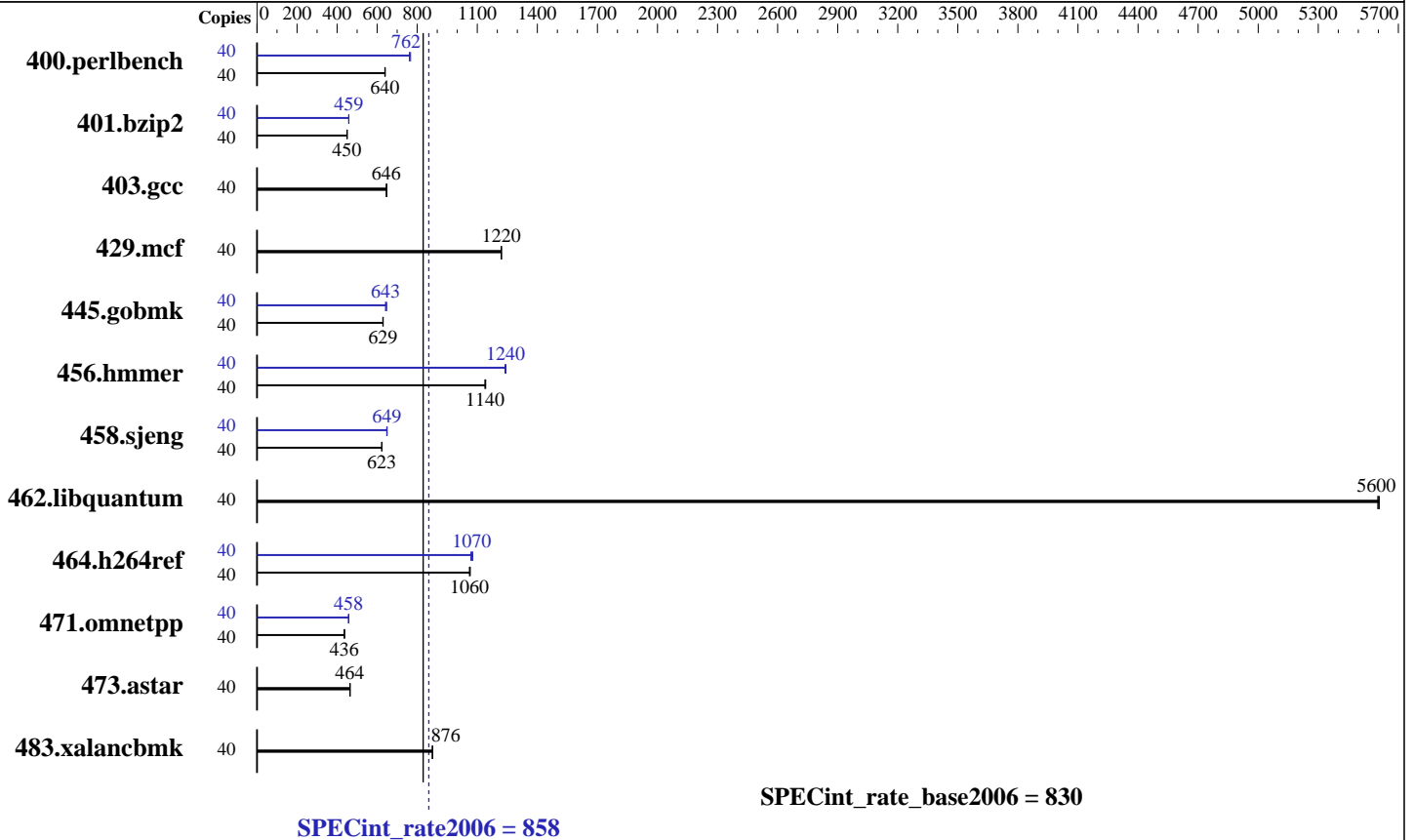
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



#### Hardware

CPU Name: Intel Xeon E5-2680 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

#### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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SPECint\_rate\_base2006 = 830

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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	<b>611</b>	<b>640</b>	612	638	610	640	40	<b>513</b>	<b>762</b>	510	767	513	761
401.bzip2	40	<b>859</b>	<b>450</b>	859	449	857	451	40	844	458	841	459	<b>841</b>	<b>459</b>
403.gcc	40	499	646	<b>498</b>	<b>646</b>	496	649	40	499	646	<b>498</b>	<b>646</b>	496	649
429.mcf	40	<b>299</b>	<b>1220</b>	299	1220	299	1220	40	<b>299</b>	<b>1220</b>	299	1220	299	1220
445.gobmk	40	668	628	<b>667</b>	<b>629</b>	666	630	40	<b>653</b>	<b>643</b>	653	642	648	648
456.hammer	40	328	1140	327	1140	<b>328</b>	<b>1140</b>	40	301	1240	300	1240	<b>301</b>	<b>1240</b>
458.sjeng	40	<b>777</b>	<b>623</b>	775	624	777	623	40	745	650	747	648	<b>746</b>	<b>649</b>
462.libquantum	40	148	5600	148	5600	<b>148</b>	<b>5600</b>	40	148	5600	148	5600	<b>148</b>	<b>5600</b>
464.h264ref	40	834	1060	831	1070	<b>834</b>	<b>1060</b>	40	<b>825</b>	<b>1070</b>	829	1070	822	1080
471.omnetpp	40	573	436	<b>573</b>	<b>436</b>	572	437	40	<b>546</b>	<b>458</b>	546	458	547	457
473.astar	40	606	464	603	466	<b>605</b>	<b>464</b>	40	606	464	603	466	<b>605</b>	<b>464</b>
483.xalancbmk	40	315	877	315	875	<b>315</b>	<b>876</b>	40	315	877	315	875	<b>315</b>	<b>876</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec14/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Tue Feb 25 04:59:38 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2680 v2 @ 2.80GHz  
2 "physical id"s (chips)  
40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



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## Platform Notes (Continued)

```

caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 25 04:52

```

SPEC is set to: /spec14
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  254G   13G  229G   6% /

```

```

Additional information from dmidecode:
BIOS Insyde Corp. RMIBV629 05/12/2014
Memory:
8x NO DIMM NO DIMM
16x Samsung M393B2G70DB0-CMA 16 GB 1866 MHz 2 rank

```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec14/libs/32:/spec14/libs/64:/spec14/sh"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

```



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## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
  
400.perlbench: icc -m64  
  
401.bzip2: icc -m64  
  
456.hmmer: icc -m64  
  
458.sjeng: icc -m64  
  
C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/sh -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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