



# SPEC® CFP2006 Result

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Huawei

**SPECfp®2006 = 77.5**

Huawei CH222 V3 (Intel Xeon E5-2630L v3)

**SPECfp\_base2006 = 71.5**

CPU2006 license: 3175

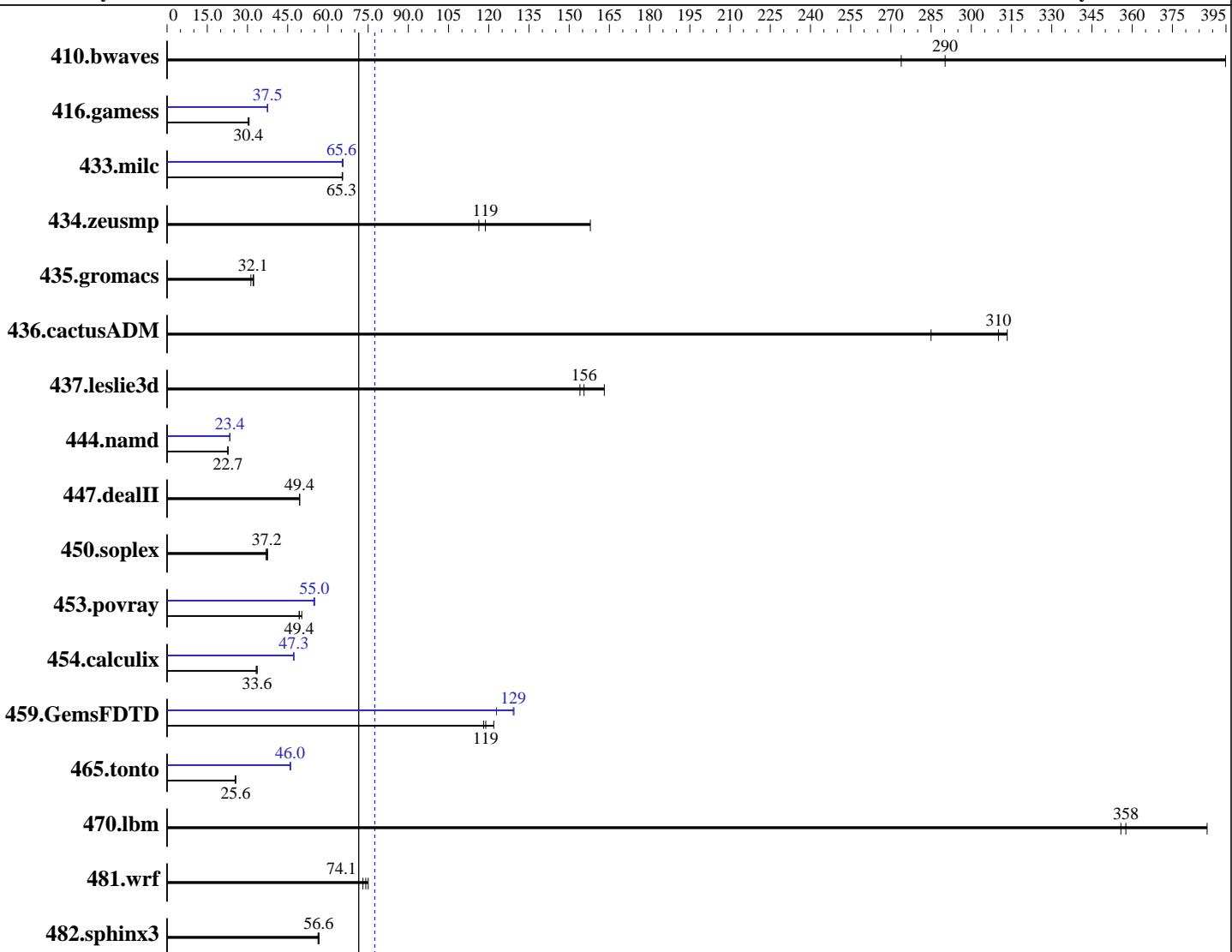
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Nov-2013



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## Hardware

CPU Name: Intel Xeon E5-2630L v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Compiler: 2.6.32-431.el6.x86\_64  
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R , running at 1867 MHz)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>46.8</b>	<b>290</b>	34.4	395	49.6	274	<b>46.8</b>	<b>290</b>	34.4	395	49.6	274
416.gamess	646	30.3	<b>645</b>	<b>30.4</b>	640	30.6	<b>522</b>	<b>37.5</b>	521	37.6	524	37.4
433.milc	<b>140</b>	<b>65.3</b>	141	65.3	140	65.5	<b>140</b>	<b>65.7</b>	<b>140</b>	<b>65.6</b>	140	65.4
434.zeusmp	57.6	158	78.2	116	<b>76.6</b>	<b>119</b>	57.6	158	78.2	116	<b>76.6</b>	<b>119</b>
435.gromacs	<b>222</b>	<b>32.1</b>	228	31.3	221	32.3	<b>222</b>	<b>32.1</b>	228	31.3	221	32.3
436.cactusADM	<b>38.5</b>	<b>310</b>	38.1	313	41.9	285	<b>38.5</b>	<b>310</b>	38.1	313	41.9	285
437.leslie3d	57.6	163	61.0	154	<b>60.4</b>	<b>156</b>	57.6	163	61.0	154	<b>60.4</b>	<b>156</b>
444.namd	<b>353</b>	<b>22.7</b>	354	22.7	353	22.7	<b>342</b>	<b>23.4</b>	342	23.4	342	23.4
447.dealII	232	49.4	231	49.5	<b>231</b>	<b>49.4</b>	232	49.4	231	49.5	<b>231</b>	<b>49.4</b>
450.soplex	222	37.5	<b>224</b>	<b>37.2</b>	226	37.0	<b>222</b>	<b>37.5</b>	<b>224</b>	<b>37.2</b>	226	37.0
453.povray	106	50.3	108	49.3	<b>108</b>	<b>49.4</b>	<b>96.8</b>	<b>55.0</b>	97.2	54.7	96.7	55.0
454.calculix	248	33.2	246	33.6	<b>246</b>	<b>33.6</b>	174	47.3	<b>175</b>	<b>47.3</b>	175	47.2
459.GemsFDTD	87.1	122	89.9	118	<b>89.3</b>	<b>119</b>	86.3	123	82.0	129	<b>82.2</b>	<b>129</b>
465.tonto	<b>385</b>	<b>25.6</b>	385	25.6	387	25.4	214	46.1	214	45.9	<b>214</b>	<b>46.0</b>
470.lbm	35.4	388	<b>38.4</b>	<b>358</b>	38.6	356	<b>35.4</b>	388	<b>38.4</b>	<b>358</b>	38.6	356
481.wrf	149	75.0	153	73.1	<b>151</b>	<b>74.1</b>	149	75.0	153	73.1	<b>151</b>	<b>74.1</b>
482.sphinx3	<b>344</b>	<b>56.6</b>	344	56.6	346	56.3	<b>344</b>	<b>56.6</b>	344	56.6	346	56.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to HS

Set Hyper-Threading to Disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on administrator Thu Sep 18 18:19:39 2014

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2630L v3 @ 1.80GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 8
    siblings   : 8
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      264277304 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux administrator 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 18 05:22
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal      ext4  231G  122G   98G  56% /
```

```
Additional information from dmidecode:
```

```
BIOS Insyde Corp. 1.16 09/02/2014
Memory:
 8x NO DIMM NO DIMM      3 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 1 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3

are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
    433.milc: -DSPEC\_CPU\_LP64  
    434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
    437.leslie3d: -DSPEC\_CPU\_LP64  
        444.namd: -DSPEC\_CPU\_LP64  
        447.dealII: -DSPEC\_CPU\_LP64  
        450.soplex: -DSPEC\_CPU\_LP64  
        453.povray: -DSPEC\_CPU\_LP64  
        454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
    465.tonto: -DSPEC\_CPU\_LP64  
    470.lbm: -DSPEC\_CPU\_LP64  
        481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias
```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
               -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -inline-calloc -opt-malloc-options=3 -auto -unroll14
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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