



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 90.8

Huawei CH222 V3 (Intel Xeon E5-2699 v3)

SPECfp_base2006 = 83.4

CPU2006 license: 3175

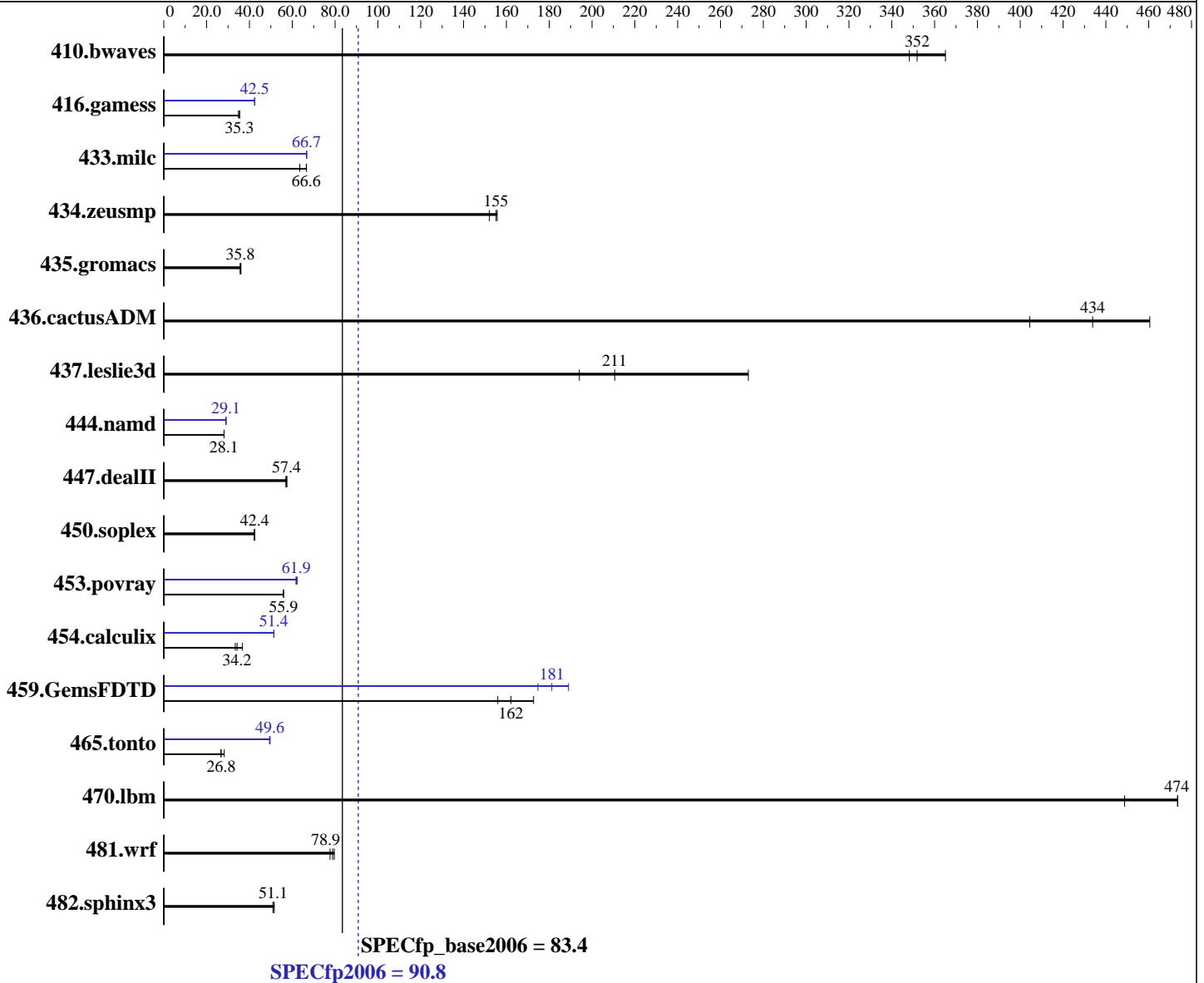
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2699 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	39.0	348	37.2	365	<u>38.6</u>	<u>352</u>	39.0	348	37.2	365	<u>38.6</u>	<u>352</u>
416.gamess	<u>554</u>	<u>35.3</u>	561	34.9	553	35.4	<u>461</u>	<u>42.5</u>	461	42.5	462	42.4
433.milc	138	66.6	145	63.5	<u>138</u>	<u>66.6</u>	<u>138</u>	<u>66.7</u>	137	66.8	138	66.6
434.zeusmp	<u>58.6</u>	<u>155</u>	59.8	152	58.4	156	<u>58.6</u>	<u>155</u>	59.8	152	58.4	156
435.gromacs	200	35.7	<u>200</u>	<u>35.8</u>	198	36.0	200	35.7	<u>200</u>	<u>35.8</u>	198	36.0
436.cactusADM	29.5	404	<u>27.5</u>	<u>434</u>	25.9	461	29.5	404	<u>27.5</u>	<u>434</u>	25.9	461
437.leslie3d	48.4	194	34.4	273	<u>44.6</u>	<u>211</u>	48.4	194	34.4	273	<u>44.6</u>	<u>211</u>
444.namd	285	28.1	<u>285</u>	<u>28.1</u>	285	28.2	276	29.0	276	29.1	<u>276</u>	<u>29.1</u>
447.dealII	199	57.5	<u>199</u>	<u>57.4</u>	201	57.0	199	57.5	<u>199</u>	<u>57.4</u>	201	57.0
450.soplex	198	42.2	196	42.5	<u>196</u>	<u>42.4</u>	198	42.2	196	42.5	<u>196</u>	<u>42.4</u>
453.povray	95.2	55.9	<u>95.2</u>	<u>55.9</u>	95.2	55.9	<u>86.0</u>	<u>61.9</u>	85.4	62.3	86.1	61.8
454.calculix	224	36.8	<u>241</u>	<u>34.2</u>	247	33.4	160	51.4	<u>160</u>	<u>51.4</u>	161	51.4
459.GemsFDTD	<u>65.4</u>	<u>162</u>	61.4	173	68.0	156	56.1	189	60.7	175	<u>58.5</u>	<u>181</u>
465.tonto	349	28.2	369	26.7	<u>367</u>	<u>26.8</u>	199	49.4	<u>199</u>	<u>49.6</u>	198	49.6
470.lbm	<u>29.0</u>	<u>474</u>	30.6	449	29.0	474	<u>29.0</u>	<u>474</u>	30.6	449	29.0	474
481.wrf	140	79.6	144	77.6	<u>142</u>	<u>78.9</u>	140	79.6	144	77.6	<u>142</u>	<u>78.9</u>
482.sphinx3	382	51.0	378	51.6	<u>381</u>	<u>51.1</u>	382	51.0	378	51.6	<u>381</u>	<u>51.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS
 Set Hyper-Threading to Disabled
 Baseboard Management Controller used to adjust the fan speed to 100%
 Sysinfo program /spec/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191
 running on huawei Sat Sep 20 02:20:41 2014

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz
 2 "physical id"s (chips)
 36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores     : 18
  siblings      : 18
  physical 0:   cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1:   cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size      : 46080 KB

```

```

From /proc/meminfo
MemTotal:       264274824 kB
HugePages_Total: 0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux huawei 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux

```

run-level 3 Sep 19 15:22

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext4  438G  138G  278G  34% /

```

Additional information from dmidecode:

```

BIOS Insyde Corp. 1.16 09/02/2014
Memory:
 8x NO DIMM NO DIMM      3 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 1 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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