



# SPEC® CFP2006 Result

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**Huawei**

**SPECfp®2006 = 89.7**

Huawei RH2288 V3 (Intel Xeon E5-2690 v3)

**SPECfp\_base2006 = 84.5**

**CPU2006 license:** 3175

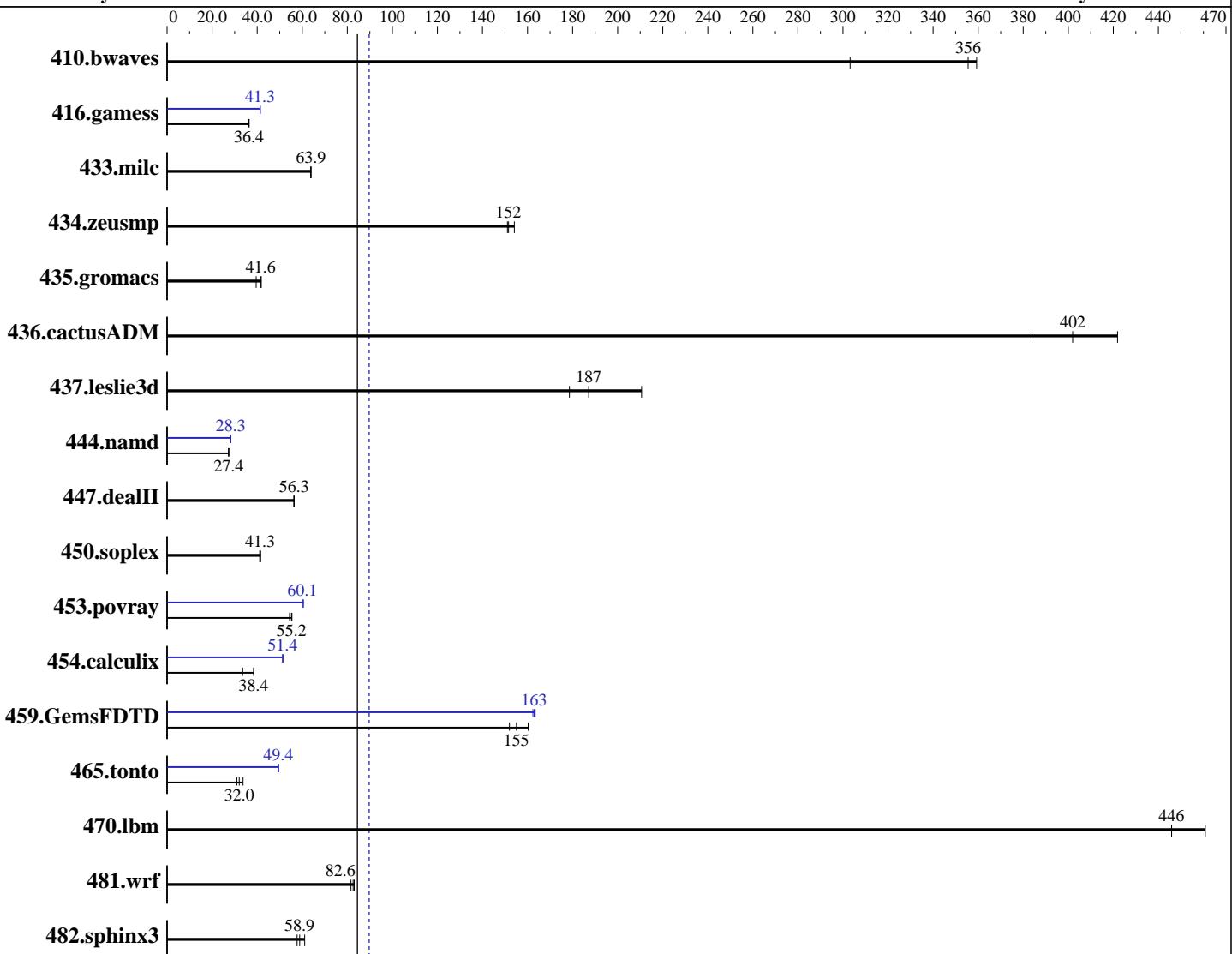
**Test sponsor:** Huawei

**Tested by:** Huawei

**Test date:** Sep-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Nov-2013



**SPECfp\_base2006 = 84.5**

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## Hardware

CPU Name: Intel Xeon E5-2690 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
CPU MHz: 2600  
FPU: Integrated  
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
Compiler: 2.6.32-431.el6.x86\_64  
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	37.8	359	<b>38.2</b>	<b>356</b>	44.8	303	37.8	359	<b>38.2</b>	<b>356</b>	44.8	303
416.gamess	<b>539</b>	<b>36.4</b>	538	36.4	543	36.1	<b>474</b>	<b>41.3</b>	<b>474</b>	<b>41.3</b>	474	41.4
433.milc	<b>144</b>	<b>63.9</b>	144	63.9	144	63.8	<b>144</b>	<b>63.9</b>	144	63.9	144	63.8
434.zeusmp	60.2	151	59.0	154	<b>60.0</b>	<b>152</b>	60.2	151	59.0	154	<b>60.0</b>	<b>152</b>
435.gromacs	<b>172</b>	<b>41.6</b>	171	41.8	181	39.6	<b>172</b>	<b>41.6</b>	171	41.8	181	39.6
436.cactusADM	28.3	422	31.1	384	<b>29.7</b>	<b>402</b>	28.3	422	31.1	384	<b>29.7</b>	<b>402</b>
437.leslie3d	44.6	211	<b>50.2</b>	<b>187</b>	52.6	179	44.6	211	<b>50.2</b>	<b>187</b>	52.6	179
444.namd	293	27.4	<b>293</b>	<b>27.4</b>	293	27.4	284	28.3	<b>284</b>	<b>28.3</b>	284	28.3
447.dealII	203	56.4	203	56.3	<b>203</b>	<b>56.3</b>	203	56.4	203	56.3	<b>203</b>	<b>56.3</b>
450.soplex	201	41.5	203	41.1	<b>202</b>	<b>41.3</b>	201	41.5	203	41.1	<b>202</b>	<b>41.3</b>
453.povray	97.8	54.4	<b>96.3</b>	<b>55.2</b>	96.0	55.4	88.5	60.1	<b>88.5</b>	<b>60.1</b>	87.9	60.5
454.calculix	246	33.6	214	38.5	<b>215</b>	<b>38.4</b>	161	51.4	161	51.3	<b>161</b>	<b>51.4</b>
459.GemsFDTD	66.2	160	69.8	152	<b>68.4</b>	<b>155</b>	64.9	163	65.3	162	<b>65.1</b>	<b>163</b>
465.tonto	292	33.7	318	31.0	<b>307</b>	<b>32.0</b>	199	49.4	199	49.5	<b>199</b>	<b>49.4</b>
470.lbm	<b>30.8</b>	<b>446</b>	30.8	446	29.8	461	<b>30.8</b>	<b>446</b>	30.8	446	29.8	461
481.wrf	134	83.1	137	81.6	<b>135</b>	<b>82.6</b>	134	83.1	137	81.6	<b>135</b>	<b>82.6</b>
482.sphinx3	<b>331</b>	<b>58.9</b>	319	61.1	338	57.7	<b>331</b>	<b>58.9</b>	319	61.1	338	57.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to HS

Set Hyper-Threading to Disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on huawei Sat Sep 6 07:35:50 2014

This section contains SUT (System Under Test) info as seen by

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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2690 v3 @ 2.60GHz
        2 "physical id"s (chips)
        24 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 12
        siblings : 12
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
    cache size : 30720 KB
```

```
From /proc/meminfo
    MemTotal:      264301908 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
    Linux huawei 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
    x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 5 15:38
```

```
SPEC is set to: /spec
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sdal      ext4  266G   97G  156G  39% /
```

Additional information from dmidecode:

```
BIOS Insyde Corp. 8.09 07/14/2014
Memory:
    8x NO DIMM NO DIMM      3 rank
    8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 1 rank
    8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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