



SPEC® CFP2006 Result

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Huawei

SPECfp®_rate2006 = 739

Huawei RH5885 V3 (Intel Xeon E7-4820 v2)

SPECfp_rate_base2006 = 724

CPU2006 license: 3175

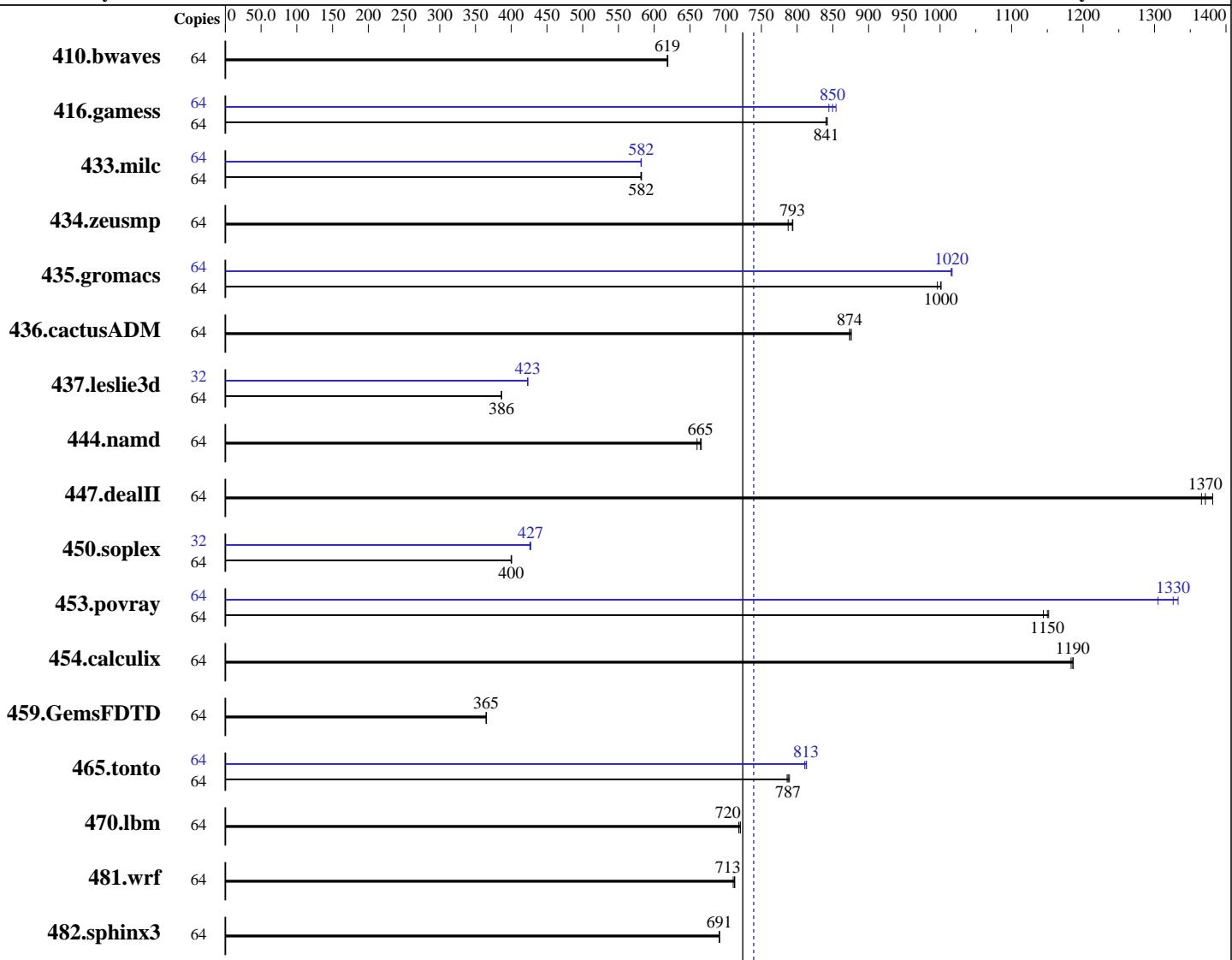
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Feb-2014

Tested by: Huawei

Software Availability: Nov-2013



CPU Name: Intel Xeon E7-4820 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Hardware

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: 2.6.32-431.el6.x86_64
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
File System: Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
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Software

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L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)
 Disk Subsystem: 2 x 600 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1405	619	1407	618	<u>1406</u>	<u>619</u>	64	1405	619	1407	618	<u>1406</u>	<u>619</u>		
416.gamess	64	1491	841	1488	842	<u>1490</u>	<u>841</u>	64	1467	854	<u>1475</u>	<u>850</u>	1484	844		
433.milc	64	1011	581	1009	582	<u>1009</u>	<u>582</u>	64	1009	582	1010	582	<u>1010</u>	<u>582</u>		
434.zeusmp	64	740	787	<u>734</u>	<u>793</u>	734	794	64	740	787	<u>734</u>	<u>793</u>	734	794		
435.gromacs	64	456	1000	459	996	<u>456</u>	<u>1000</u>	64	450	1020	449	1020	<u>450</u>	<u>1020</u>		
436.cactusADM	64	873	876	875	874	<u>875</u>	<u>874</u>	64	873	876	875	874	<u>875</u>	<u>874</u>		
437.leslie3d	64	<u>1557</u>	<u>386</u>	1555	387	1558	386	32	<u>711</u>	<u>423</u>	711	423	712	423		
444.namd	64	771	666	<u>772</u>	<u>665</u>	778	660	64	771	666	<u>772</u>	<u>665</u>	778	660		
447.dealII	64	530	1380	536	1370	<u>534</u>	<u>1370</u>	64	530	1380	536	1370	<u>534</u>	<u>1370</u>		
450.soplex	64	1333	400	1335	400	<u>1334</u>	<u>400</u>	32	625	427	627	426	<u>625</u>	<u>427</u>		
453.povray	64	297	1140	296	1150	<u>296</u>	<u>1150</u>	64	<u>257</u>	<u>1330</u>	261	1300	255	1330		
454.calculix	64	445	1190	446	1180	<u>445</u>	<u>1190</u>	64	445	1190	446	1180	<u>445</u>	<u>1190</u>		
459.GemsFDTD	64	<u>1860</u>	<u>365</u>	1862	365	1858	365	64	<u>1860</u>	<u>365</u>	1862	365	1858	365		
465.tonto	64	801	786	<u>800</u>	<u>787</u>	798	789	64	<u>775</u>	<u>813</u>	774	813	777	811		
470.lbm	64	<u>1221</u>	<u>720</u>	1220	721	1224	718	64	<u>1221</u>	<u>720</u>	1220	721	1224	718		
481.wrf	64	1003	713	<u>1003</u>	<u>713</u>	1006	711	64	1003	713	<u>1003</u>	<u>713</u>	1006	711		
482.sphinx3	64	<u>1805</u>	<u>691</u>	1805	691	1804	691	64	<u>1805</u>	<u>691</u>	1805	691	1804	691		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

BIOS configuration:

```
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on RH5885V3 Tue Sep 9 11:16:23 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
        4 "physical id"s (chips)
        64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
physical 2: cores 0 1 2 3 4 5 6 7
physical 3: cores 0 1 2 3 4 5 6 7
cache size : 16384 KB
```

```
From /proc/meminfo
MemTotal:      264356460 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RH5885V3 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 9 11:09
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda2       ext4  385G  125G  241G  35%  /spec
```

Additional information from dmidecode:

BIOS American Megatrends Inc. BLISV395 07/25/2014
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Platform Notes (Continued)

Memory:

16x 16 GB
14x Hynix HMT42GR7AFR4C-PB 16 GB 1066 MHz 2 rank
2x Hynix HMT42GR7MFR4C-PB 16 GB 1066 MHz 2 rank
32x NO DIMM NO DIMM

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have two lines reading as:

14x Hynix HMT42GR7AFR4C-PB 16 GB 1066 MHz 2 rank
2x Hynix HMT42GR7MFR4C-PB 16 GB 1066 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -unroll14 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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