



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R730 (Intel Xeon E5-2609 v3, 1.90 GHz)

SPECint®2006 = 34.8

SPECint_base2006 = 33.4

CPU2006 license: 55

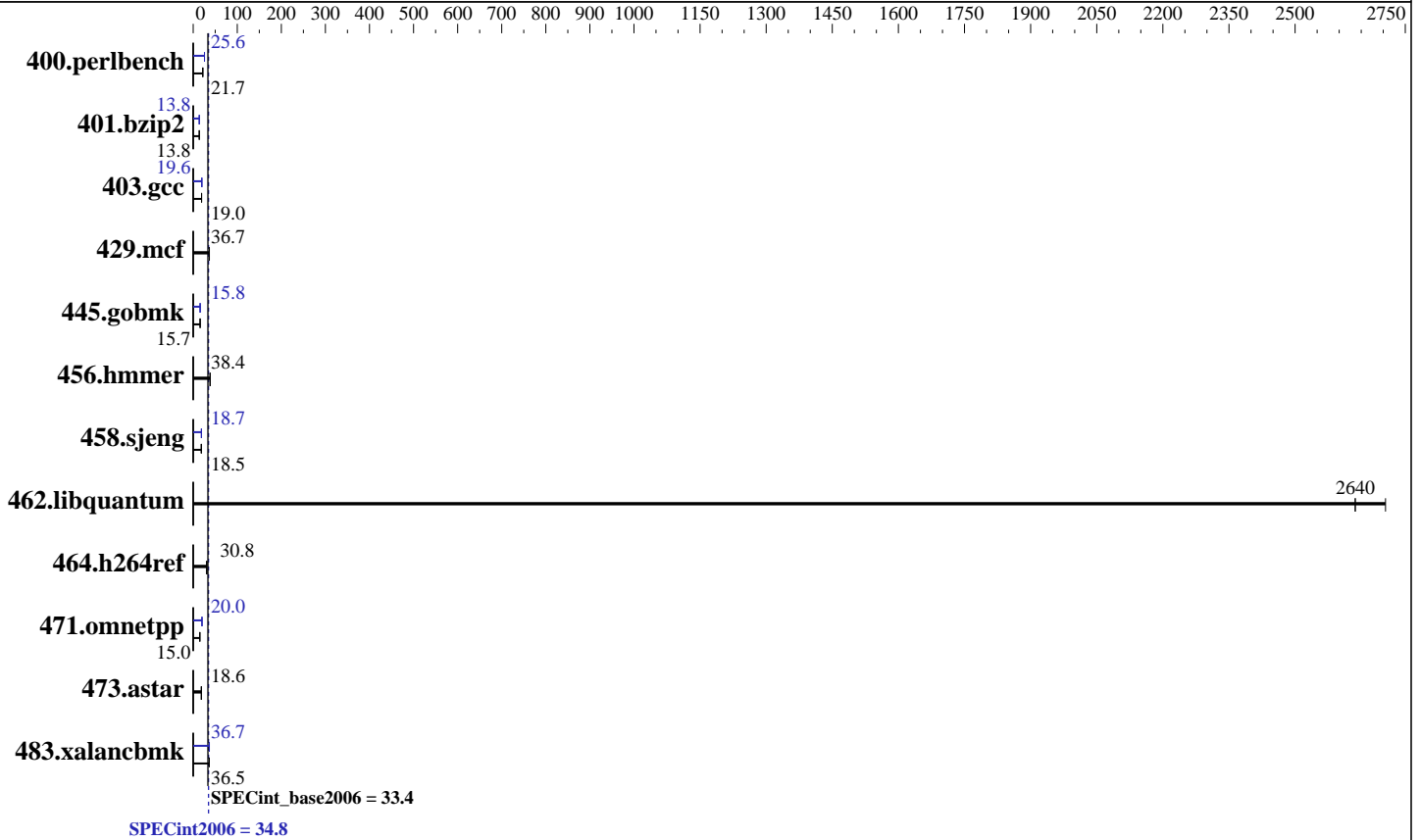
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2609 v3
 CPU Characteristics:
 CPU MHz: 1900
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (8 x 16 GB, 8 x 8 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 1 x 1000 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	450	21.7	449	21.7	448	21.8	381	25.6	381	25.6	381	25.6
401.bzip2	699	13.8	698	13.8	698	13.8	697	13.8	698	13.8	698	13.8
403.gcc	421	19.1	424	19.0	425	18.9	411	19.6	411	19.6	412	19.6
429.mcf	248	36.8	248	36.7	248	36.7	248	36.8	248	36.7	248	36.7
445.gobmk	666	15.7	666	15.7	668	15.7	666	15.8	666	15.8	666	15.8
456.hammer	243	38.3	243	38.4	243	38.5	243	38.3	243	38.4	243	38.5
458.sjeng	654	18.5	654	18.5	656	18.5	648	18.7	648	18.7	648	18.7
462.libquantum	7.66	2710	7.86	2640	7.86	2640	7.66	2710	7.86	2640	7.86	2640
464.h264ref	718	30.8	721	30.7	716	30.9	718	30.8	721	30.7	716	30.9
471.omnetpp	415	15.1	417	15.0	416	15.0	313	20.0	313	20.0	313	20.0
473.astar	378	18.6	378	18.5	378	18.6	378	18.6	378	18.5	378	18.6
483.xalancbmk	189	36.6	190	36.4	189	36.5	188	36.7	188	36.7	188	36.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:

Snoop Mode set to Home Snoop
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on linux-khqw Wed Sep 17 10:59:50 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz
2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The

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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings  : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      198249732 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 11 (x86_64)
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

uname -a:

```
Linux linux-khqw 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 17 10:10 last=S

SPEC is set to: /root/cpu2006-1.2

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext3  915G   10G  904G   2% /
```

Additional information from dmidecode:

```
BIOS Dell Inc. 1.0.4 08/28/2014
Memory:
1x 00AD00B300AD HMA42GR7MFR4N-TFTD 16 GB 1600 MHz
5x 00AD063200AD HMA42GR7MFR4N-TFT1 16 GB 1600 MHz
8x 00CE00B300CE M393A1G43DB0-CPB 8 GB 1600 MHz
2x 00CE00B300CE M393A2G40DB0-CPB 16 GB 1600 MHz
8x Not Specified Not Specified
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "12"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

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General Notes (Continued)

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmarthheap64

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml>



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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Oct 8 19:41:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 October 2014.