SPEC® CFP2006 Result
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Dell Inc.

PowerEdge R920 (Intel Xeon E7-4850 v2, 2.30 GHz)

$SPECfp^{®}2006 =$ 85.1

$SPECfp\_base2006 =$ 81.2
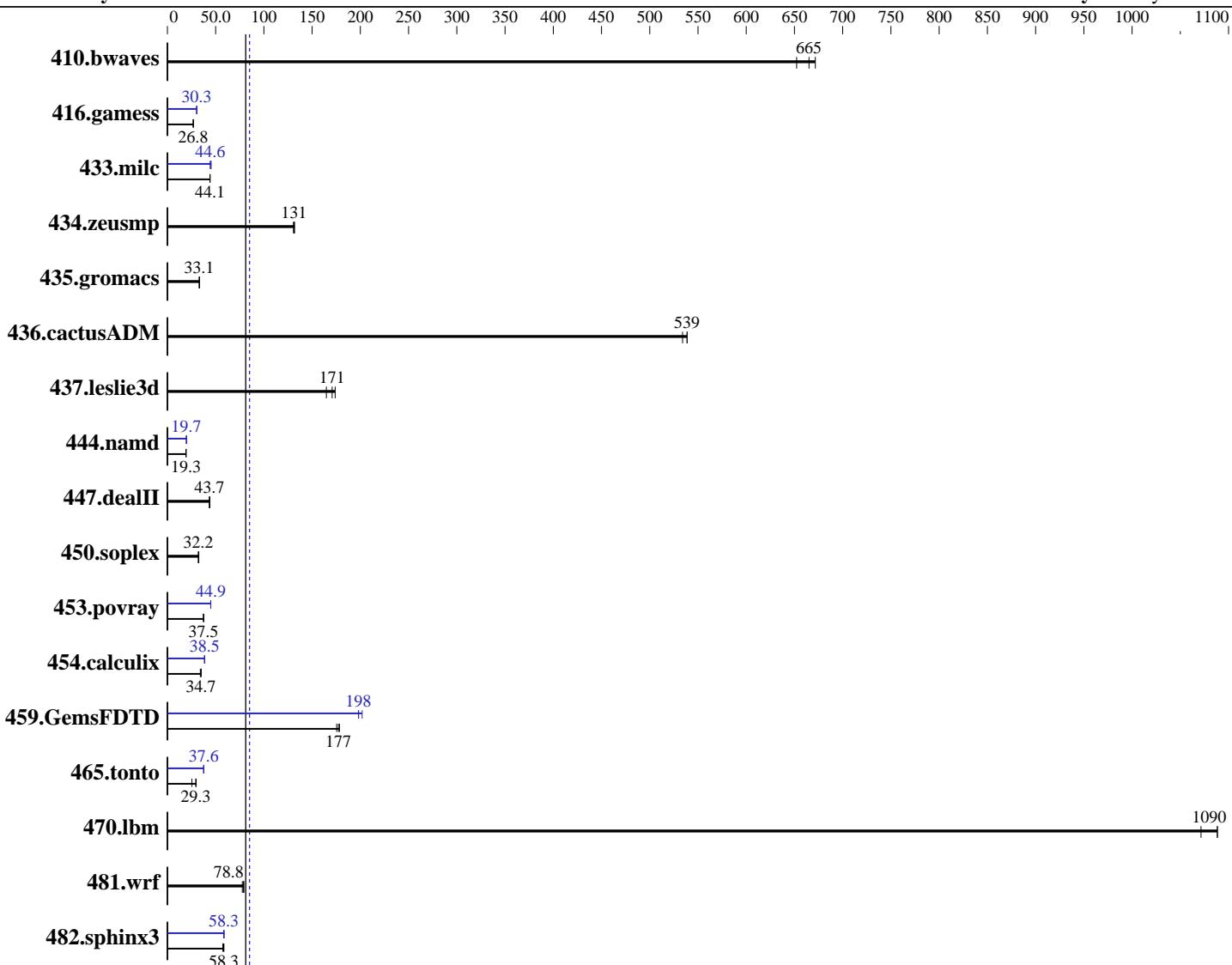
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2014
Hardware Availability: Mar-2014
Software Availability: May-2014

SPECfp_base2006 = 81.2

SPECfp2006 = 85.1

Hardware

CPU Name:	Intel Xeon E7-4850 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	48 cores, 4 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)

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L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (64 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)
Disk Subsystem:	1 x 400 GB SAS SSD SSD
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**20.4**	**665**	20.2	672	20.8	652	**20.4**	**665**	20.2	672	20.8	652
416.gamess	730	26.8	**729**	**26.8**	729	26.9	647	30.3	**645**	**30.3**	643	30.5
433.milc	**208**	**44.1**	207	44.3	208	44.0	207	44.4	**206**	**44.6**	203	45.3
434.zeusmp	**69.3**	**131**	69.1	132	69.7	131	**69.3**	**131**	69.1	132	69.7	131
435.gromacs	216	33.0	214	33.3	**216**	**33.1**	216	33.0	214	33.3	**216**	**33.1**
436.cactusADM	22.4	534	**22.2**	**539**	22.2	539	22.4	534	**22.2**	**539**	22.2	539
437.leslie3d	57.0	165	54.1	174	**55.0**	**171**	57.0	165	54.1	174	**55.0**	**171**
444.namd	415	19.3	**416**	**19.3**	416	19.3	**407**	**19.7**	407	19.7	407	19.7
447.dealII	**262**	**43.7**	261	43.8	263	43.4	**262**	**43.7**	261	43.8	263	43.4
450.soplex	256	32.6	260	32.1	**259**	**32.2**	256	32.6	260	32.1	**259**	**32.2**
453.povray	142	37.5	**142**	**37.5**	142	37.4	118	45.0	**118**	**44.9**	119	44.9
454.calculix	236	35.0	242	34.1	**238**	**34.7**	**214**	**38.5**	214	38.5	214	38.6
459.GemsFDTD	59.5	178	**59.9**	**177**	60.5	175	52.6	202	53.6	198	**53.6**	**198**
465.tonto	330	29.9	**335**	**29.3**	387	25.4	262	37.6	262	37.6	**262**	**37.6**
470.lbm	12.6	1090	**12.6**	**1090**	12.8	1070	12.6	1090	**12.6**	**1090**	12.8	1070
481.wrf	**142**	**78.8**	144	77.6	141	79.3	**142**	**78.8**	144	77.6	141	79.3
482.sphinx3	333	58.6	**334**	**58.3**	339	57.4	**334**	**58.3**	331	58.9	335	58.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Patrol Scrub set to disabled
 Sysinfo program
 /root/Desktop/Performance/ic14.0_Oct17_2013/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191

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Platform Notes (Continued)

```
running on slesperf3 Sun Sep 21 04:52:44 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
      4 "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 24576 KB

From /proc/meminfo
   MemTotal:       1058789108 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux slesperf3 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
   (ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 21 04:49 last=S

SPEC is set to: /root/Desktop/Performance/ic14.0_Oct17_2013
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext2  365G  263G  101G  73% /

Additional information from dmidecode:
  BIOS Dell Inc. 1.2.2 05/05/2014
  Memory:
   64x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1066 MHz

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/root/Desktop/Performance/ic14.0_Oct17_2013/libs/32:/root/Desktop/Performance/ic14.0_Oct17_2013/libs/64:/root/Desktop/Performance/ic14.0_Oct17_2013/sh"
OMP_NUM_THREADS = "48"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
 410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
   433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64  -nofor_main
436.cactusADM: -DSPEC_CPU_LP64  -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
   444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64  -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
   470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64  -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64
```

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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
          -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
          -parallel
```

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Peak Optimization Flags (Continued)

C++ benchmarks:

    444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
            `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias`
            `-auto-ilp32`

    447.dealII: basepeak = yes

    450.soplex: basepeak = yes

    453.povray: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
             `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -ansi-alias`

Fortran benchmarks:

    410.bwaves: basepeak = yes

    416.gamess: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
             `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2`
             `-inline-level=0 -scalar-rep-`

    434.zeusmp: basepeak = yes

    437.leslie3d: basepeak = yes

  459.GemsFDTD: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
             `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2`
             `-inline-level=0 -opt-prefetch -parallel`

    465.tonto: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
             `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-inline-calloc`
             `-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

    435.gromacs: basepeak = yes

    436.cactusADM: basepeak = yes

    454.calculix: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

    481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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