



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R920 (Intel Xeon E7-4850 v2, 2.30 GHz)

SPECfp®_rate2006 = 1250

SPECfp_rate_base2006 = 1220

CPU2006 license: 55

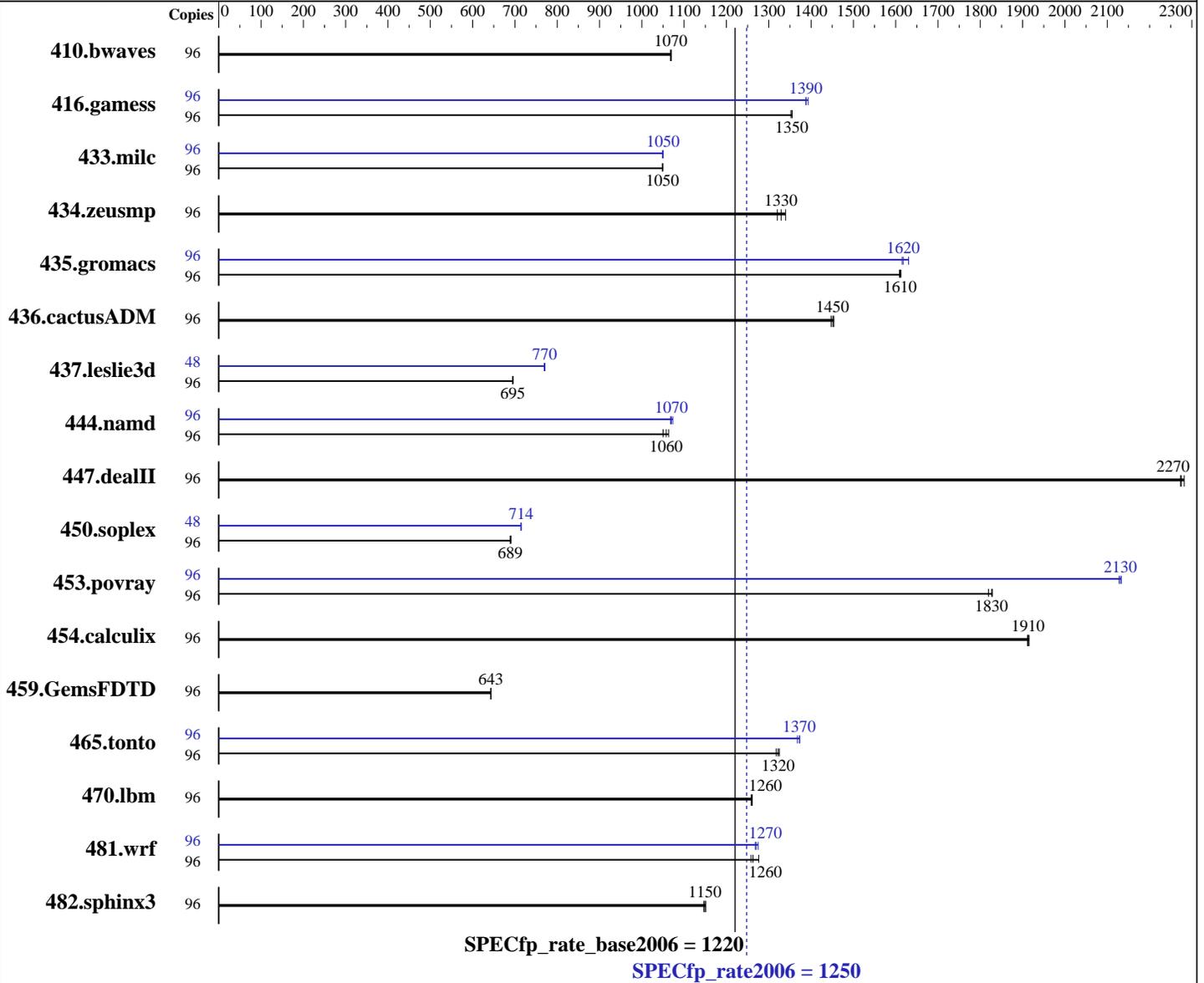
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2014

Hardware Availability: Mar-2014

Software Availability: May-2014



Hardware

CPU Name: Intel Xeon E7-4850 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext2
 System State: Run level 3 (multi-user)

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	96	1222	1070	1220	1070	<u>1221</u>	<u>1070</u>	96	1222	1070	1220	1070	<u>1221</u>	<u>1070</u>		
416.gamess	96	1390	1350	<u>1388</u>	<u>1350</u>	1387	1360	96	<u>1354</u>	<u>1390</u>	1355	1390	1349	1390		
433.milc	96	<u>840</u>	<u>1050</u>	841	1050	840	1050	96	<u>839</u>	<u>1050</u>	840	1050	839	1050		
434.zeusmp	96	662	1320	652	1340	<u>657</u>	<u>1330</u>	96	662	1320	652	1340	<u>657</u>	<u>1330</u>		
435.gromacs	96	426	1610	425	1610	<u>425</u>	<u>1610</u>	96	<u>424</u>	<u>1620</u>	420	1630	424	1620		
436.cactusADM	96	<u>791</u>	<u>1450</u>	789	1450	793	1450	96	<u>791</u>	<u>1450</u>	789	1450	793	1450		
437.leslie3d	96	<u>1299</u>	<u>695</u>	1297	696	1299	695	48	586	769	585	771	<u>586</u>	<u>770</u>		
444.namd	96	724	1060	733	1050	<u>728</u>	<u>1060</u>	96	<u>720</u>	<u>1070</u>	721	1070	717	1070		
447.dealII	96	483	2270	481	2280	<u>483</u>	<u>2270</u>	96	483	2270	481	2280	<u>483</u>	<u>2270</u>		
450.soplex	96	1159	691	1162	689	<u>1161</u>	<u>689</u>	48	560	715	<u>560</u>	<u>714</u>	560	714		
453.povray	96	<u>280</u>	<u>1830</u>	281	1820	279	1830	96	239	2130	<u>240</u>	<u>2130</u>	240	2130		
454.calculix	96	<u>414</u>	<u>1910</u>	414	1920	414	1910	96	<u>414</u>	<u>1910</u>	414	1920	414	1910		
459.GemsFDTD	96	1585	643	<u>1584</u>	<u>643</u>	1582	644	96	1585	643	<u>1584</u>	<u>643</u>	1582	644		
465.tonto	96	713	1320	717	1320	<u>715</u>	<u>1320</u>	96	688	1370	691	1370	<u>688</u>	<u>1370</u>		
470.lbm	96	1048	1260	<u>1047</u>	<u>1260</u>	1046	1260	96	1048	1260	<u>1047</u>	<u>1260</u>	1046	1260		
481.wrf	96	<u>849</u>	<u>1260</u>	852	1260	840	1280	96	<u>844</u>	<u>1270</u>	846	1270	841	1270		
482.sphinx3	96	<u>1628</u>	<u>1150</u>	1625	1150	1632	1150	96	<u>1628</u>	<u>1150</u>	1625	1150	1632	1150		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled

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Platform Notes (Continued)

System Profile set to Custom
 CPU Power Management set to Maximum Performance
 ClE set to disabled
 C States set to disabled
 Memory Patrol Scrub set to disabled
 Sysinfo program
 /root/Desktop/Performance/ic14.0_Oct17_2013/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on slesperf3 Thu Sep 18 10:52:05 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
 4 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 24576 KB
```

```
From /proc/meminfo
MemTotal:      1058789108 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux slesperf3 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 18 18:15 last=S

```
SPEC is set to: /root/Desktop/Performance/ic14.0_Oct17_2013
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2  365G  260G  104G  72% /
Continued on next page
```



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Platform Notes (Continued)

Additional information from dmidecode:

BIOS Dell Inc. 1.2.2 05/05/2014

Memory:

64x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1066 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

`LD_LIBRARY_PATH = */root/Desktop/Performance/ic14.0_Oct17_2013/libs/32:/root/Desktop/Performance/ic14.0_Oct17_2013/libs/64:/root/Desktop/Performance/ic14.0_Oct17_2013/sh*`

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

`echo always > /sys/kernel/mm/transparent_hugepage/enabled`

Filesystem page cache cleared with:

`echo 1> /proc/sys/vm/drop_caches`

runspec command invoked through numactl i.e.:

`numactl --interleave=all runspec <etc>`

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

416.gamess: `-DSPEC_CPU_LP64`

433.milc: `-DSPEC_CPU_LP64`

434.zeusmp: `-DSPEC_CPU_LP64`

435.gromacs: `-DSPEC_CPU_LP64 -noFOR_main`

436.cactusADM: `-DSPEC_CPU_LP64 -noFOR_main`

437.lelie3d: `-DSPEC_CPU_LP64`

444.namd: `-DSPEC_CPU_LP64`

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Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll14 -ansi-alias

```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Wed Oct 8 19:40:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 8 October 2014.