



# SPEC® CFP2006 Result

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Huawei

**SPECfp®2006 = 92.5**

Huawei RH2288E V2 (Intel Xeon E5-2660 V2)

**SPECfp\_base2006 = 88.6**

CPU2006 license: 3175

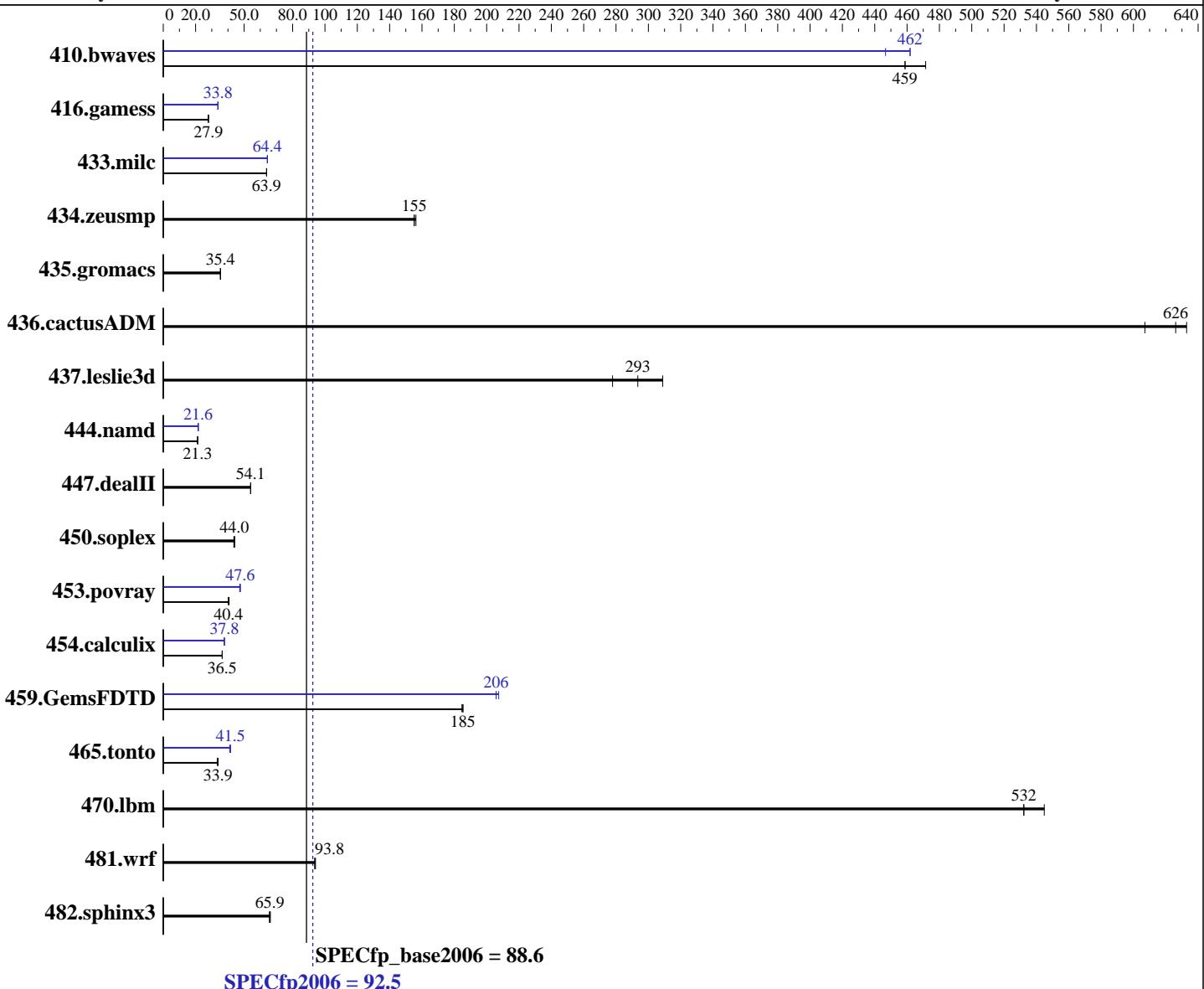
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



## Hardware

CPU Name: Intel Xeon E5-2660 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Compiler: 2.6.32-431.el6.x86\_64  
 Auto Parallel: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 File System: Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Software: ext4

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	28.8	472	<b>29.6</b>	<b>459</b>	29.6	459	30.4	447	29.4	462	<b>29.4</b>	<b>462</b>
416.gamess	<b>701</b>	<b>27.9</b>	702	27.9	701	27.9	<b>579</b>	33.8	<b>579</b>	33.8	<b>579</b>	<b>33.8</b>
433.milc	144	63.9	144	64.0	<b>144</b>	<b>63.9</b>	<b>143</b>	64.4	<b>142</b>	64.5	<b>143</b>	<b>64.4</b>
434.zeusmp	58.2	156	<b>58.6</b>	<b>155</b>	58.6	155	<b>58.2</b>	156	<b>58.6</b>	<b>155</b>	58.6	155
435.gromacs	202	35.4	<b>202</b>	<b>35.4</b>	202	35.3	202	35.4	<b>202</b>	<b>35.4</b>	202	35.3
436.cactusADM	19.7	607	<b>19.1</b>	<b>626</b>	18.9	633	19.7	607	<b>19.1</b>	<b>626</b>	18.9	633
437.leslie3d	33.8	278	<b>32.0</b>	<b>293</b>	30.4	309	33.8	278	<b>32.0</b>	<b>293</b>	30.4	309
444.namd	377	21.3	377	21.3	<b>377</b>	<b>21.3</b>	<b>371</b>	<b>21.6</b>	371	21.6	371	21.6
447.dealII	211	54.1	212	54.0	<b>212</b>	<b>54.1</b>	211	54.1	212	54.0	<b>212</b>	<b>54.1</b>
450.soplex	189	44.2	<b>189</b>	<b>44.0</b>	191	43.7	189	44.2	<b>189</b>	<b>44.0</b>	191	43.7
453.povray	131	40.7	132	40.2	<b>132</b>	<b>40.4</b>	112	47.3	<b>112</b>	<b>47.6</b>	112	47.6
454.calculix	<b>226</b>	<b>36.5</b>	226	36.5	226	36.5	218	37.8	<b>218</b>	<b>37.8</b>	218	37.8
459.GemsFDTD	<b>57.3</b>	<b>185</b>	57.4	185	57.2	185	51.5	206	<b>51.5</b>	<b>206</b>	51.1	207
465.tonto	290	33.9	295	33.4	<b>291</b>	<b>33.9</b>	239	41.1	<b>237</b>	<b>41.5</b>	237	41.6
470.lbm	25.8	532	25.2	545	<b>25.8</b>	<b>532</b>	25.8	532	25.2	545	<b>25.8</b>	<b>532</b>
481.wrf	119	93.7	119	94.1	<b>119</b>	<b>93.8</b>	119	93.7	119	94.1	<b>119</b>	<b>93.8</b>
482.sphinx3	294	66.2	<b>296</b>	<b>65.9</b>	297	65.6	294	66.2	<b>296</b>	<b>65.9</b>	297	65.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Hyper-Threading to Disabled

Sysinfo program /spec/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Sun Sep 21 01:23:35 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2660 v2 @ 2.20GHz
        2 "physical id"s (chips)
        20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings   : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 19 18:13
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  265G   14G  238G   6%  /
```

```
Additional information from dmidecode:
```

```
Memory:
16x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank
```

```
(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"  
OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory  
using RHEL 6.1

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## General Notes (Continued)

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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