# SPEC® CFP2006 Result

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## Huawei

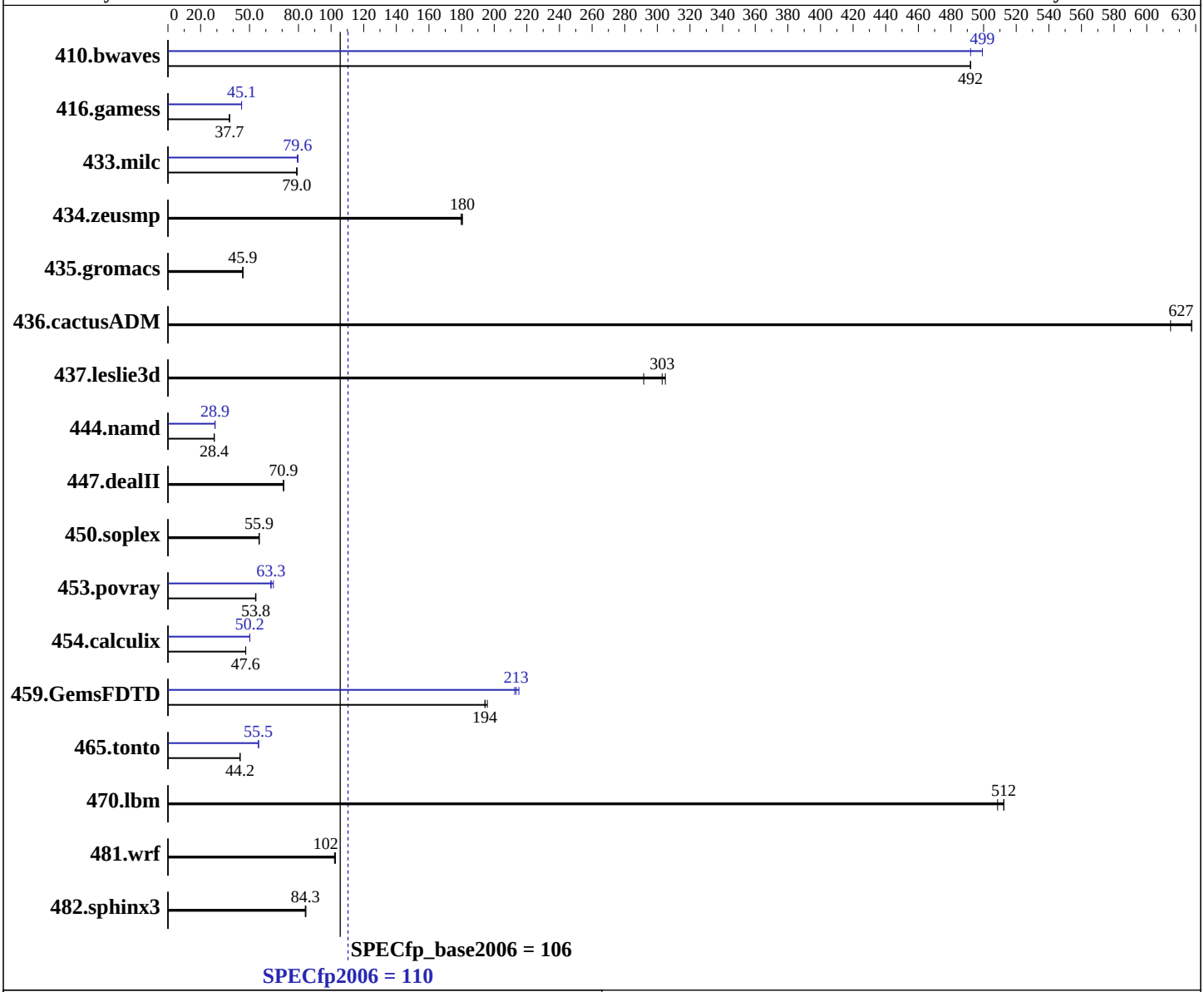
## Huawei CH222 (Intel Xeon E5-2667 v2)

SPECfp®2006 = 110

SPECfp_base2006 = 106

**CPU2006 license:** 3175
**Test sponsor:** Huawei
**Tested by:** Huawei

**Test date:** Sep-2014
**Hardware Availability:** Sep-2013
**Software Availability:** Nov-2013




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2667 v2 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 4.00 GHz |
| CPU MHz: | 3300 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.5 (Santiago)<br>2.6.32-431.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;<br>Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext4 |

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| | |
|---|---|
| L3 Cache: | 25 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC) |
| Disk Subsystem: | 1 x 300 GB SATA, 7200RPM |
| Other Hardware: | None |

| | |
|---|---|
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | None |

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | **27.6** | **492** | 27.6 | 492 | 27.6 | 492 | **27.2** | **499** | 27.2 | 499 | 27.6 | 492 |
| 416.gamess | 519 | 37.7 | **519** | **37.7** | 519 | 37.7 | 434 | 45.1 | 434 | 45.2 | **434** | **45.1** |
| 433.milc | 116 | 78.9 | 116 | 79.2 | **116** | **79.0** | **115** | **79.6** | 116 | 79.3 | 115 | 79.6 |
| 434.zeusmp | **50.4** | **180** | 50.4 | 180 | 50.6 | 180 | **50.4** | **180** | 50.4 | 180 | 50.6 | 180 |
| 435.gromacs | 156 | 45.9 | **156** | **45.9** | 156 | 45.9 | 156 | 45.9 | **156** | **45.9** | 156 | 45.9 |
| 436.cactusADM | 19.4 | 615 | 19.0 | 628 | **19.0** | **627** | 19.4 | 615 | 19.0 | 628 | **19.0** | **627** |
| 437.leslie3d | **31.0** | **303** | 32.2 | 292 | 30.8 | 305 | **31.0** | **303** | 32.2 | 292 | 30.8 | 305 |
| 444.namd | **283** | **28.4** | 283 | 28.4 | 283 | 28.4 | **278** | **28.9** | 278 | 28.9 | 278 | 28.9 |
| 447.dealII | **161** | **70.9** | 161 | 70.9 | 161 | 70.9 | **161** | **70.9** | 161 | 70.9 | 161 | 70.9 |
| 450.soplex | 149 | 56.0 | 149 | 55.8 | **149** | **55.9** | 149 | 56.0 | 149 | 55.8 | **149** | **55.9** |
| 453.povray | **98.9** | **53.8** | 98.6 | 53.9 | 99.1 | 53.7 | 84.4 | 63.1 | **84.1** | **63.3** | 82.3 | 64.6 |
| 454.calculix | 173 | 47.6 | **173** | **47.6** | 173 | 47.7 | 164 | 50.2 | **164** | **50.2** | 164 | 50.2 |
| 459.GemsFDTD | 54.2 | 196 | **54.6** | **194** | 54.6 | 194 | **49.7** | **213** | 49.3 | 215 | 49.9 | 213 |
| 465.tonto | 223 | 44.2 | 223 | 44.1 | **223** | **44.2** | **177** | **55.5** | 178 | 55.4 | 177 | 55.5 |
| 470.lbm | 27.0 | 509 | **26.8** | **512** | 26.8 | 512 | 27.0 | 509 | **26.8** | **512** | 26.8 | 512 |
| 481.wrf | 109 | 102 | 109 | 103 | **109** | **102** | 109 | 102 | 109 | 103 | **109** | **102** |
| 482.sphinx3 | **231** | **84.3** | 231 | 84.2 | 230 | 84.6 | **231** | **84.3** | 231 | 84.2 | 230 | 84.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Set Hyper-Threading to Disabled
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Tue Sep  9 14:26:47 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

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## Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2667 v2 @ 3.30GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 1 2 3 4 8 9 10 11
      physical 1: cores 1 2 3 4 8 9 10 11
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       264478184 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
   EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 5 19:55

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  265G   16G  236G   7% /

Additional information from dmidecode:
  Memory:
   10x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
   6x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "16"
```

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## General Notes (Continued)

```
Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory
using RHEL 6.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

## Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

Continued on next page

## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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