



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint®2006 = 36.0

Huawei BH640 V2 (Intel Xeon E5-4603 v2)

SPECint_base2006 = 33.9

CPU2006 license: 3175

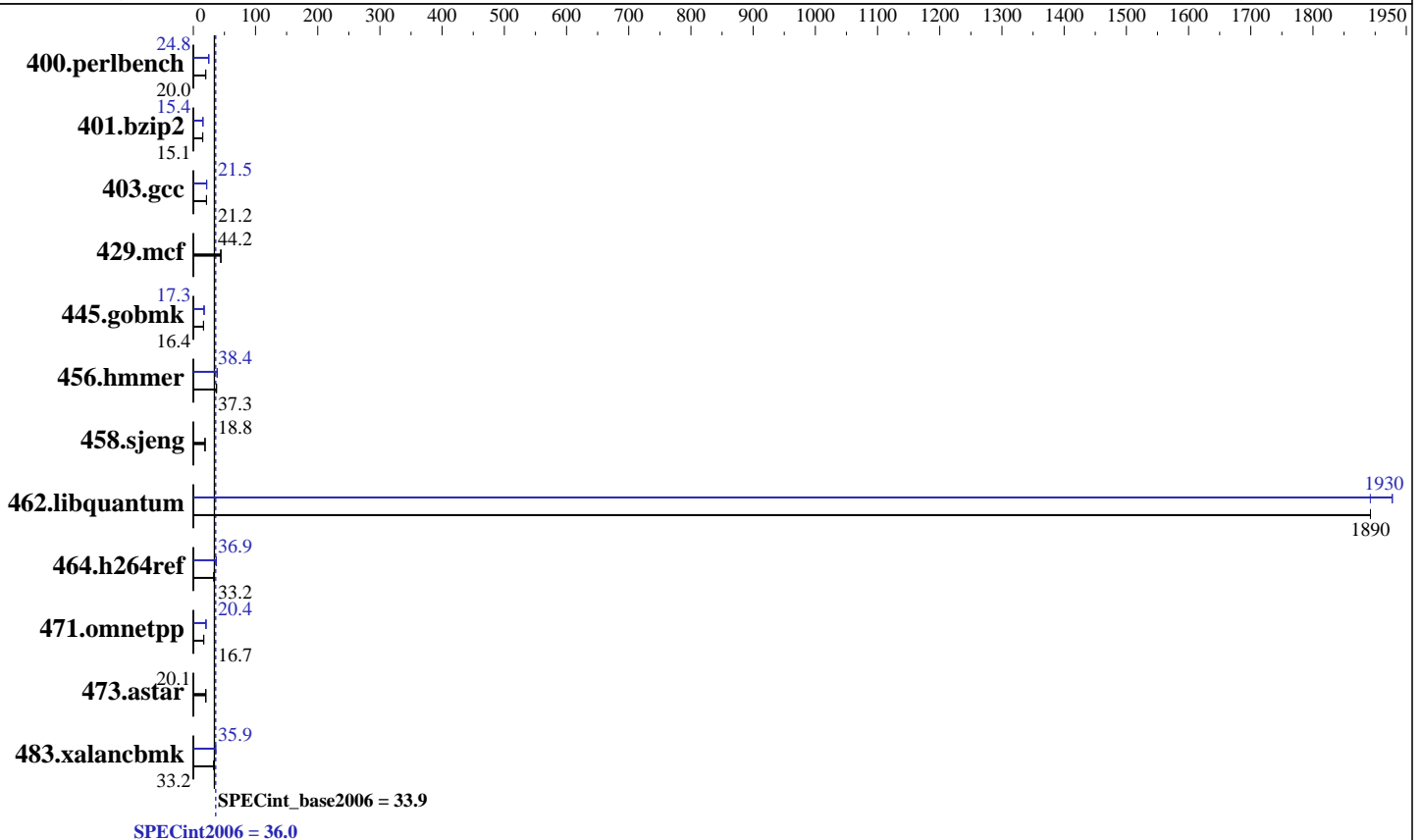
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Apr-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-4603 v2
 CPU Characteristics:
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 600 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 36.0

Huawei BH640 V2 (Intel Xeon E5-4603 v2)

SPECint_base2006 = 33.9

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Sep-2014
Hardware Availability: Apr-2014
Software Availability: Nov-2013

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	489	20.0	<u>489</u>	<u>20.0</u>	496	19.7	394	24.8	394	24.8	<u>394</u>	<u>24.8</u>
401.bzip2	640	15.1	639	15.1	<u>640</u>	<u>15.1</u>	626	15.4	<u>627</u>	<u>15.4</u>	627	15.4
403.gcc	379	21.2	<u>379</u>	<u>21.2</u>	381	21.1	375	21.5	375	21.5	<u>375</u>	<u>21.5</u>
429.mcf	206	44.3	<u>206</u>	<u>44.2</u>	206	44.2	206	44.3	<u>206</u>	<u>44.2</u>	206	44.2
445.gobmk	638	16.4	638	16.4	<u>638</u>	<u>16.4</u>	606	17.3	606	17.3	<u>606</u>	<u>17.3</u>
456.hammer	<u>250</u>	<u>37.3</u>	251	37.2	250	37.3	243	38.4	<u>243</u>	<u>38.4</u>	243	38.3
458.sjeng	<u>644</u>	<u>18.8</u>	644	18.8	644	18.8	<u>644</u>	<u>18.8</u>	644	18.8	644	18.8
462.libquantum	10.9	1890	<u>10.9</u>	<u>1890</u>	10.9	1890	<u>10.7</u>	<u>1930</u>	11.0	1890	10.7	1930
464.h264ref	668	33.1	<u>667</u>	<u>33.2</u>	666	33.2	<u>600</u>	<u>36.9</u>	600	36.9	599	36.9
471.omnetpp	374	16.7	374	16.7	<u>374</u>	<u>16.7</u>	<u>307</u>	<u>20.4</u>	306	20.4	308	20.3
473.astar	351	20.0	<u>350</u>	<u>20.1</u>	350	20.1	351	20.0	<u>350</u>	<u>20.1</u>	350	20.1
483.xalancbmk	<u>208</u>	<u>33.2</u>	211	32.7	207	33.4	<u>192</u>	<u>35.9</u>	192	35.9	192	35.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /spec/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Tue Sep 2 07:09:54 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4603 v2 @ 2.20GHz
 4 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
physical 2: cores 0 1 2 3
physical 3: cores 0 1 2 3
cache size : 10240 KB
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 36.0

Huawei BH640 V2 (Intel Xeon E5-4603 v2)

SPECint_base2006 = 33.9

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Sep-2014
Hardware Availability: Apr-2014
Software Availability: Nov-2013

Platform Notes (Continued)

```

From /proc/meminfo
MemTotal:      264477564 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 2 06:59

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda6       ext4  355G  6.7G  330G   2% /

Additional information from dmidecode:
Memory:
16x Hynix HMT42GR7MFR4C-PB 16 GB 1600 MHz 2 rank

(End of data from sysinfo program)

```

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "16"

```

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1
Transparent Huge Pages enabled with:

```

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 36.0

Huawei BH640 V2 (Intel Xeon E5-4603 v2)

SPECint_base2006 = 33.9

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Apr-2014

Software Availability: Nov-2013

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 36.0

Huawei BH640 V2 (Intel Xeon E5-4603 v2)

SPECint_base2006 = 33.9

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Apr-2014

Software Availability: Nov-2013

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -auto-p32

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-ra-region-strategy=block -ansi-alias
 -Wl,-z,muldefs -L/smartheap -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint2006 = 36.0

Huawei BH640 V2 (Intel Xeon E5-4603 v2)

SPECint_base2006 = 33.9

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Apr-2014

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Fri Oct 17 18:22:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 17 October 2014.