



# SPEC® CINT2006 Result

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## Dell Inc.

PowerEdge R730 (Intel Xeon E5-2680 v3, 2.50 GHz)

SPECint®\_rate2006 = 1060

SPECint\_rate\_base2006 = 1030

CPU2006 license: 55

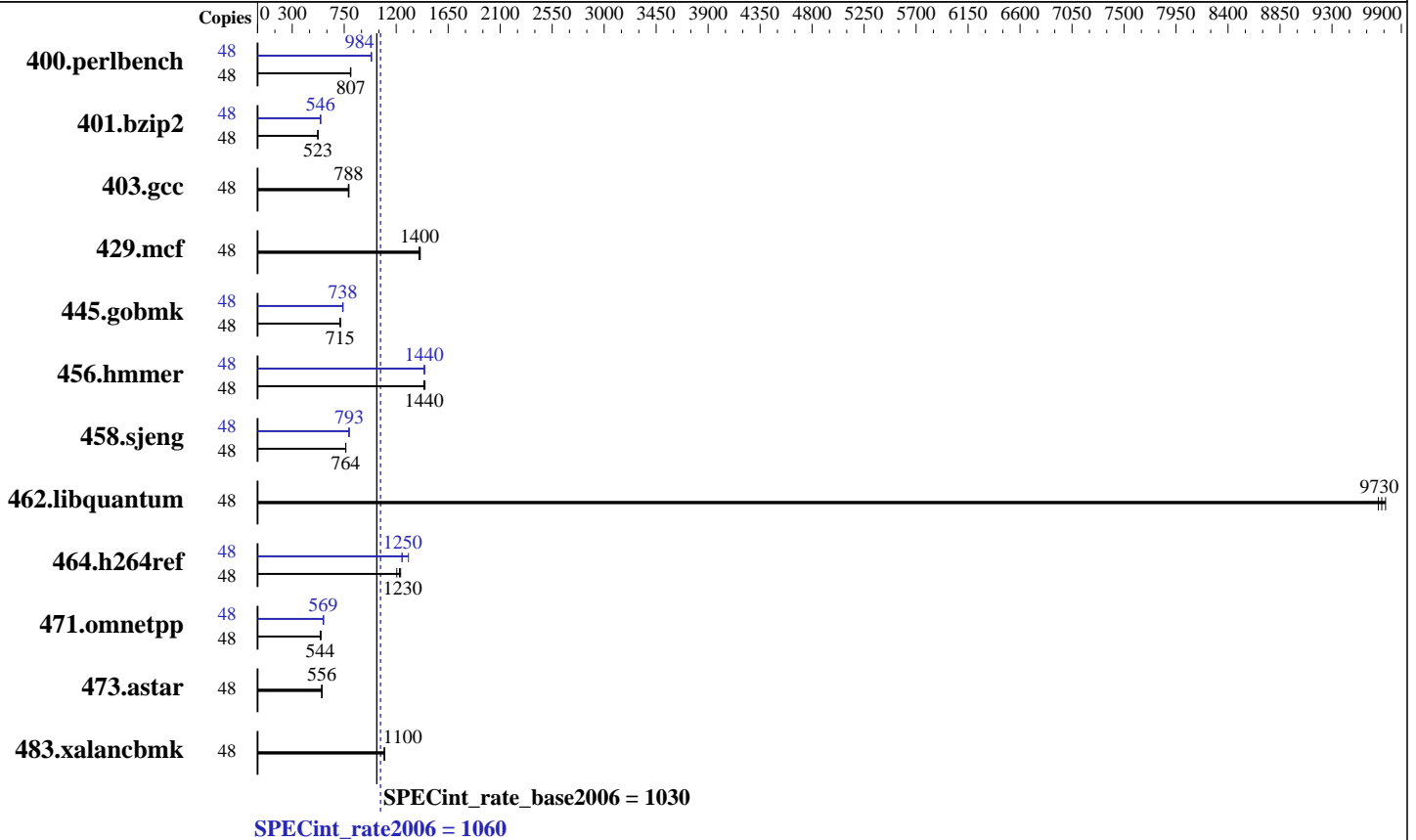
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Aug-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2680 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)  
 64 GB (8 x 8 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 1000 GB 7200 RPM SATA  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	583	804	<u>581</u>	<u>807</u>	581	807	48	477	982	475	987	<u>477</u>	<u>984</u>
401.bzip2	48	889	521	886	523	<u>886</u>	<u>523</u>	48	<u>849</u>	<u>546</u>	849	546	851	544
403.gcc	48	488	791	492	786	<u>490</u>	<u>788</u>	48	488	791	492	786	<u>490</u>	<u>788</u>
429.mcf	48	311	1410	313	1400	<u>312</u>	<u>1400</u>	48	311	1410	313	1400	<u>312</u>	<u>1400</u>
445.gobmk	48	702	717	<u>704</u>	<u>715</u>	704	715	48	680	740	683	737	<u>682</u>	<u>738</u>
456.hammer	48	311	1440	309	1450	<u>310</u>	<u>1440</u>	48	310	1440	310	1450	<u>310</u>	<u>1440</u>
458.sjeng	48	762	763	<u>761</u>	<u>764</u>	760	764	48	732	793	<u>733</u>	<u>793</u>	735	790
462.libquantum	48	103	9700	102	9760	<u>102</u>	<u>9730</u>	48	103	9700	102	9760	<u>102</u>	<u>9730</u>
464.h264ref	48	<u>865</u>	<u>1230</u>	882	1200	858	1240	48	813	1310	<u>849</u>	<u>1250</u>	849	1250
471.omnetpp	48	552	543	<u>551</u>	<u>544</u>	546	550	48	524	572	<u>527</u>	<u>569</u>	528	568
473.astar	48	<u>606</u>	<u>556</u>	604	558	607	556	48	<u>606</u>	<u>556</u>	604	558	607	556
483.xalancbmk	48	301	1100	<u>302</u>	<u>1100</u>	302	1100	48	301	1100	<u>302</u>	<u>1100</u>	302	1100

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Snoop Mode set to Cluster on Die  
Virtualization Technology disabled  
Execute Disable disabled  
System Profile set to Performance  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on linux-khqw Fri Aug 29 17:01:55 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz  
2 "physical id"s (chips)  
48 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      198249228 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 11 (x86_64)
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

uname -a:

```
Linux linux-khqw 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Aug 29 16:55 last=S

SPEC is set to: /root/cpu2006-1.2

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext3  915G   10G  905G   2% /
```

Additional information from dmidecode:

```
BIOS Dell Inc. 1.0.4 08/28/2014
Memory:
1x 00AD00B300AD HMA42GR7MFR4N-TFTD 16 GB 2133 MHz
5x 00AD063200AD HMA42GR7MFR4N-TFT1 16 GB 2133 MHz
8x 00CE00B300CE M393A1G43DB0-CPB 8 GB 2133 MHz
2x 00CE00B300CE M393A2G40DB0-CPB 16 GB 2133 MHz
8x Not Specified Not Specified
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`  
401.bzip2: `icc -m64`  
456.hmmer: `icc -m64`  
458.sjeng: `icc -m64`

C++ benchmarks:  
`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`  
401.bzip2: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-auto-ilp32`  
401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-opt-prefetch -auto-ilp32 -ansi-alias`  
403.gcc: `basepeak = yes`  
429.mcf: `basepeak = yes`  
445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`  
`-ansi-alias -opt-mem-layout-trans=3`  
456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`  
458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-unroll4 -auto-ilp32`

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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