



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 97.1

Huawei RH1288 V2 (Intel Xeon E5-2637 v2)

SPECfp\_base2006 = 94.0

CPU2006 license: 3175

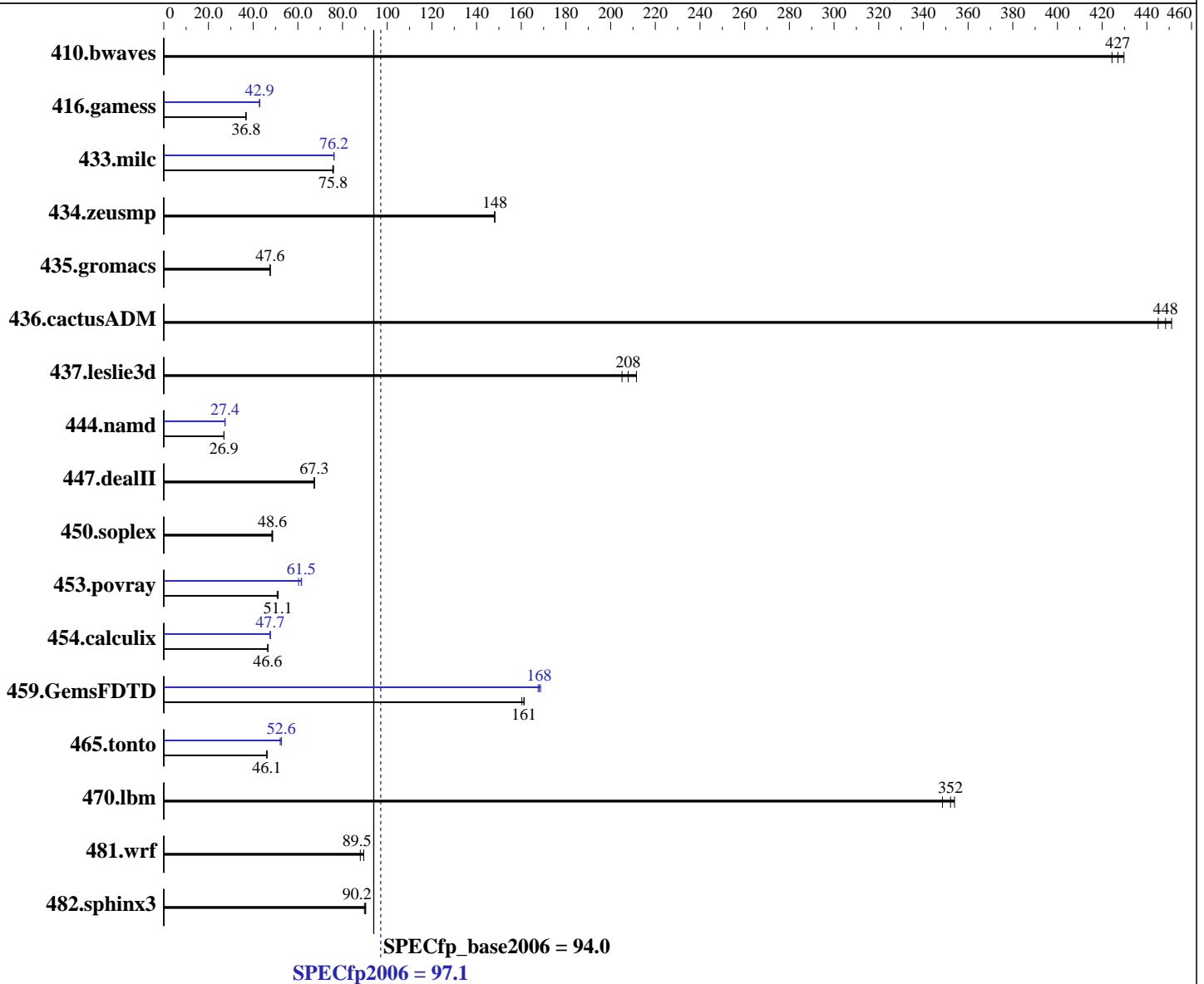
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



Hardware		Software	
CPU Name:	Intel Xeon E5-2637 v2	Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago)
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz		2.6.32-431.el6.x86_64
CPU MHz:	3500	Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
FPU:	Integrated		Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip	Auto Parallel:	Yes
CPU(s) orderable:	1,2 chip	File System:	ext4
Primary Cache:	32 KB I + 32 KB D on chip per core		
Secondary Cache:	256 KB I+D on chip per core		
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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 X 300 GB SAS 10000 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	31.6	430	32.0	424	<b><u>31.8</u></b>	<b><u>427</u></b>	31.6	430	32.0	424	<b><u>31.8</u></b>	<b><u>427</u></b>
416.gamess	532	36.8	532	36.8	<b><u>532</u></b>	<b><u>36.8</u></b>	457	42.9	457	42.9	<b><u>457</u></b>	<b><u>42.9</u></b>
433.milc	121	76.0	121	75.6	<b><u>121</u></b>	<b><u>75.8</u></b>	120	76.2	<b><u>121</u></b>	<b><u>76.2</u></b>	121	76.1
434.zeusmp	61.4	148	<b><u>61.4</u></b>	<b><u>148</u></b>	61.4	148	61.4	148	<b><u>61.4</u></b>	<b><u>148</u></b>	61.4	148
435.gromacs	150	47.6	<b><u>150</u></b>	<b><u>47.6</u></b>	150	47.6	150	47.6	<b><u>150</u></b>	<b><u>47.6</u></b>	150	47.6
436.cactusADM	<b><u>26.7</u></b>	<b><u>448</u></b>	26.5	451	26.8	445	<b><u>26.7</u></b>	<b><u>448</u></b>	26.5	451	26.8	445
437.leslie3d	45.8	205	<b><u>45.2</u></b>	<b><u>208</u></b>	44.4	212	45.8	205	<b><u>45.2</u></b>	<b><u>208</u></b>	44.4	212
444.namd	298	26.9	298	26.9	<b><u>298</u></b>	<b><u>26.9</u></b>	<b><u>293</u></b>	<b><u>27.4</u></b>	293	27.4	293	27.4
447.dealII	<b><u>170</u></b>	<b><u>67.3</u></b>	170	67.3	170	67.4	<b><u>170</u></b>	<b><u>67.3</u></b>	170	67.3	170	67.4
450.soplex	171	48.7	172	48.4	<b><u>172</u></b>	<b><u>48.6</u></b>	171	48.7	172	48.4	<b><u>172</u></b>	<b><u>48.6</u></b>
453.povray	104	51.2	105	50.7	<b><u>104</u></b>	<b><u>51.1</u></b>	<b><u>86.5</u></b>	<b><u>61.5</u></b>	86.3	61.7	88.2	60.3
454.calculix	177	46.6	<b><u>177</u></b>	<b><u>46.6</u></b>	177	46.6	<b><u>173</u></b>	<b><u>47.7</u></b>	173	47.6	173	47.7
459.GemsFDTD	65.8	161	<b><u>65.8</u></b>	<b><u>161</u></b>	66.2	160	63.3	168	<b><u>63.1</u></b>	<b><u>168</u></b>	62.9	169
465.tonto	213	46.1	<b><u>213</u></b>	<b><u>46.1</u></b>	213	46.2	187	52.6	<b><u>187</u></b>	<b><u>52.6</u></b>	189	52.1
470.lbm	39.4	349	<b><u>39.0</u></b>	<b><u>352</u></b>	38.8	354	39.4	349	<b><u>39.0</u></b>	<b><u>352</u></b>	38.8	354
481.wrf	<b><u>125</u></b>	<b><u>89.5</u></b>	125	89.5	127	88.0	<b><u>125</u></b>	<b><u>89.5</u></b>	125	89.5	127	88.0
482.sphinx3	217	89.8	<b><u>216</u></b>	<b><u>90.2</u></b>	216	90.4	217	89.8	<b><u>216</u></b>	<b><u>90.2</u></b>	216	90.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on localhost.localdomain Fri Aug 29 08:23:45 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
 model name : Intel(R) Xeon(R) CPU E5-2637 v2 @ 3.50GHz  
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## Platform Notes (Continued)

```

2 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 4
physical 0: cores 1 2 3 4
physical 1: cores 1 2 3 4
cache size : 15360 KB

From /proc/meminfo
MemTotal:          264478184 kB
HugePages_Total:   0
Hugepagesize:      2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 27 10:37

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  265G  15G  237G   6% /

Additional information from dmidecode:
Memory:
10x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
6x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "8"

```

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory  
using RHEL 6.1  
Transparent Huge Pages enabled with:

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## General Notes (Continued)

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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