



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Huawei

SPECint®\_rate2006 = 432

Huawei RH2285H v2 (Intel Xeon E5-2420 v2)

SPECint\_rate\_base2006 = 417

CPU2006 license: 3175

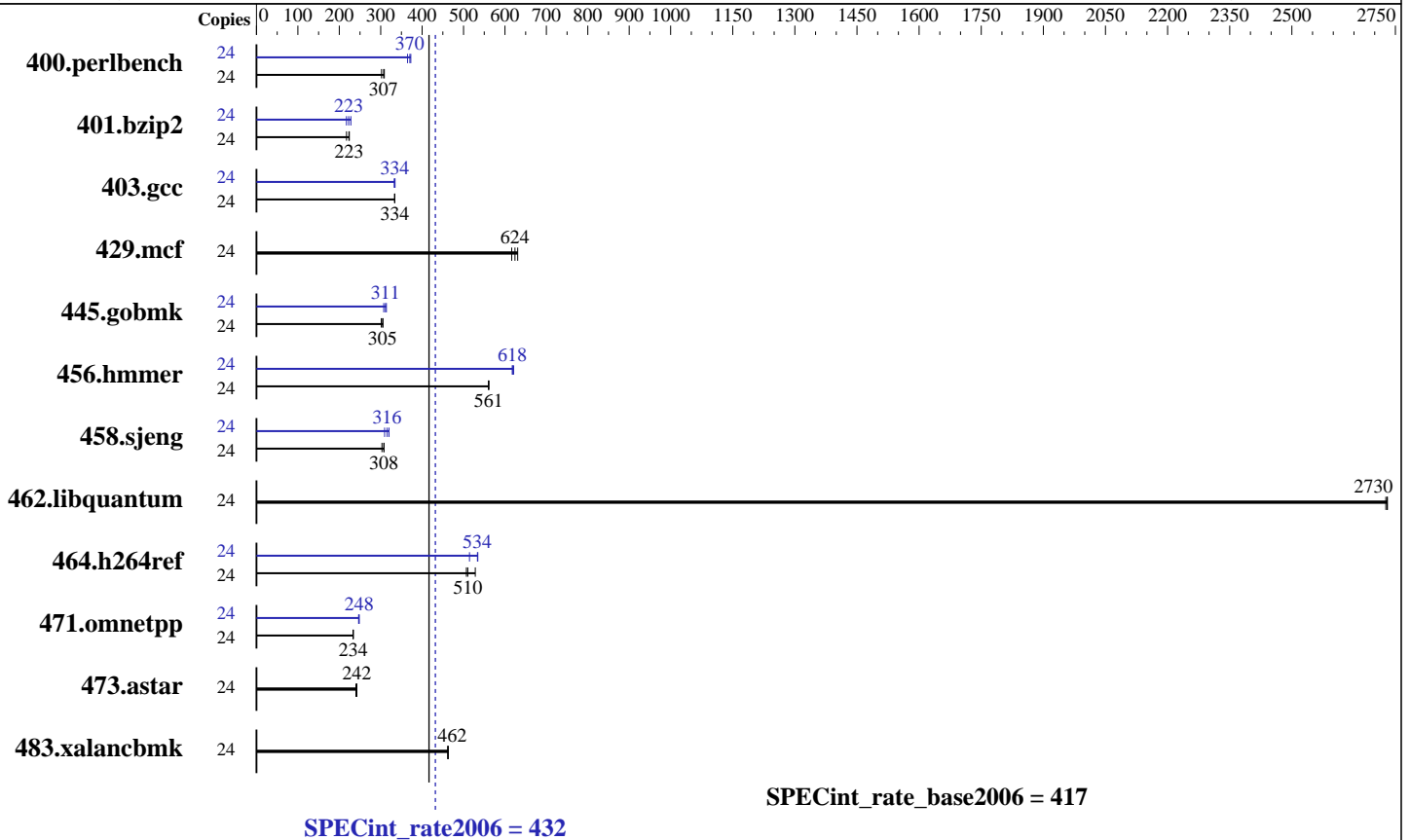
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2420 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 600 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Huawei

SPECint\_rate2006 = 432

Huawei RH2285H v2 (Intel Xeon E5-2420 v2)

SPECint\_rate\_base2006 = 417

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Aug-2014  
Hardware Availability: Mar-2014  
Software Availability: Nov-2013

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>764</b>	<b>307</b>	777	302	760	309	24	643	365	<b>633</b>	<b>370</b>	629	373
401.bzip2	24	<b>1037</b>	<b>223</b>	1033	224	1065	217	24	<b>1040</b>	<b>223</b>	1066	217	1016	228
403.gcc	24	579	334	<b>579</b>	<b>334</b>	579	334	24	577	335	<b>579</b>	<b>334</b>	581	333
429.mcf	24	355	616	<b>351</b>	<b>624</b>	347	631	24	355	616	<b>351</b>	<b>624</b>	347	631
445.gobmk	24	<b>826</b>	<b>305</b>	835	301	824	305	24	819	307	<b>808</b>	<b>311</b>	802	314
456.hammer	24	<b>399</b>	<b>561</b>	399	561	400	560	24	<b>362</b>	<b>618</b>	361	621	362	618
458.sjeng	24	958	303	943	308	<b>944</b>	<b>308</b>	24	<b>920</b>	<b>316</b>	938	309	907	320
462.libquantum	24	<b>182</b>	<b>2730</b>	182	2730	182	2730	24	<b>182</b>	<b>2730</b>	182	2730	182	2730
464.h264ref	24	1048	507	1005	528	<b>1041</b>	<b>510</b>	24	<b>995</b>	<b>534</b>	994	534	1033	514
471.omnetpp	24	<b>642</b>	<b>234</b>	642	234	643	233	24	608	247	<b>606</b>	<b>248</b>	605	248
473.astar	24	<b>696</b>	<b>242</b>	702	240	695	242	24	<b>696</b>	<b>242</b>	702	240	695	242
483.xalancbmk	24	358	463	359	462	<b>358</b>	<b>462</b>	24	358	463	359	462	<b>358</b>	<b>462</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Thu Aug 28 13:00:15 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2420 v2 @ 2.20GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 432

Huawei RH2285H v2 (Intel Xeon E5-2420 v2)

SPECint\_rate\_base2006 = 417

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013

## Platform Notes (Continued)

```

caution.)
  cpu cores : 6
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
  cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      49369748 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 18 08:29

```

SPEC is set to: /spec
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2        ext4     540G   59G  454G  12% /

```

```

Additional information from dmidecode:
BIOS Insyde Corp. RMIBV379 03/19/2014
Memory:
6x Micron 36JSF1G72PZ-1G6K1 8 GB 1600 MHz 2 rank
6x NO DIMM NO DIMM

```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

```



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 432

Huawei RH2285H v2 (Intel Xeon E5-2420 v2)

SPECint\_rate\_base2006 = 417

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Aug-2014  
Hardware Availability: Mar-2014  
Software Availability: Nov-2013

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
  
400.perlbench: icc -m64  
  
401.bzip2: icc -m64  
  
456.hmmer: icc -m64  
  
458.sjeng: icc -m64  
  
C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 432

Huawei RH2285H v2 (Intel Xeon E5-2420 v2)

SPECint\_rate\_base2006 = 417

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 432

Huawei RH2285H v2 (Intel Xeon E5-2420 v2)

SPECint\_rate\_base2006 = 417

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013

## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Sep 24 16:17:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 24 September 2014.