



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

IBM Flex System x240  
(Intel Xeon E5-2695 v2, 3.20 GHz)

SPECfp<sup>®</sup>\_rate2006 = 670

SPECfp\_rate\_base2006 = 652

CPU2006 license: 11

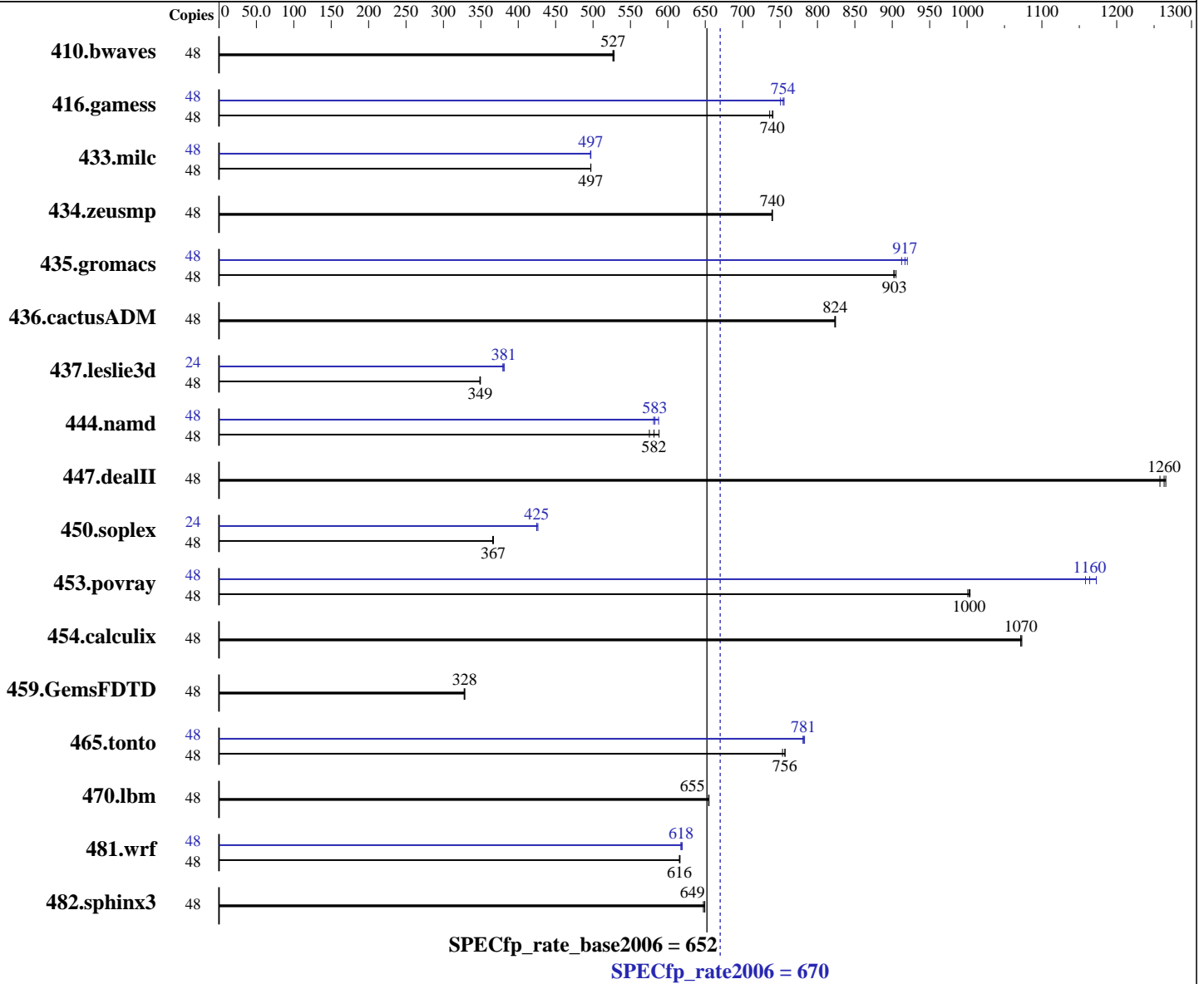
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2695 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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SPECfp\_rate2006 = **670**

SPECfp\_rate\_base2006 = **652**

CPU2006 license: 11  
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Test date: Jul-2014  
Hardware Availability: Dec-2013  
Software Availability: Sep-2013

L3 Cache: 30 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	<b><u>1237</u></b>	<b><u>527</u></b>	1235	528	1239	527	48	<b><u>1237</u></b>	<b><u>527</u></b>	1235	528	1239	527
416.gamess	48	1269	740	<b><u>1270</u></b>	<b><u>740</u></b>	1277	736	48	1244	755	<b><u>1246</u></b>	<b><u>754</u></b>	1252	751
433.milc	48	<b><u>887</u></b>	<b><u>497</u></b>	886	497	887	497	48	<b><u>887</u></b>	<b><u>497</u></b>	887	497	886	497
434.zeusmp	48	590	740	591	739	<b><u>591</u></b>	<b><u>740</u></b>	48	590	740	591	739	<b><u>591</u></b>	<b><u>740</u></b>
435.gromacs	48	379	905	380	902	<b><u>380</u></b>	<b><u>903</u></b>	48	376	912	<b><u>374</u></b>	<b><u>917</u></b>	372	920
436.cactusADM	48	<b><u>696</u></b>	<b><u>824</u></b>	696	824	697	823	48	<b><u>696</u></b>	<b><u>824</u></b>	696	824	697	823
437.leslie3d	48	1294	349	<b><u>1292</u></b>	<b><u>349</u></b>	1291	349	24	592	381	595	379	<b><u>593</u></b>	<b><u>381</u></b>
444.namd	48	654	588	669	575	<b><u>662</u></b>	<b><u>582</u></b>	48	655	588	<b><u>661</u></b>	<b><u>583</u></b>	663	581
447.dealII	48	434	1270	437	1260	<b><u>435</u></b>	<b><u>1260</u></b>	48	434	1270	437	1260	<b><u>435</u></b>	<b><u>1260</u></b>
450.soplex	48	1091	367	1095	366	<b><u>1092</u></b>	<b><u>367</u></b>	24	<b><u>471</u></b>	<b><u>425</u></b>	469	426	471	425
453.povray	48	<b><u>255</u></b>	<b><u>1000</u></b>	255	1000	254	1000	48	<b><u>219</u></b>	<b><u>1160</u></b>	220	1160	218	1170
454.calculix	48	370	1070	<b><u>369</u></b>	<b><u>1070</u></b>	369	1070	48	370	1070	<b><u>369</u></b>	<b><u>1070</u></b>	369	1070
459.GemsFDTD	48	<b><u>1551</u></b>	<b><u>328</u></b>	1550	329	1554	328	48	<b><u>1551</u></b>	<b><u>328</u></b>	1550	329	1554	328
465.tonto	48	627	753	<b><u>624</u></b>	<b><u>756</u></b>	624	757	48	<b><u>605</u></b>	<b><u>781</u></b>	605	781	603	783
470.lbm	48	1008	654	<b><u>1007</u></b>	<b><u>655</u></b>	1007	655	48	1008	654	<b><u>1007</u></b>	<b><u>655</u></b>	1007	655
481.wrf	48	870	616	<b><u>871</u></b>	<b><u>616</u></b>	871	616	48	<b><u>868</u></b>	<b><u>618</u></b>	866	619	868	618
482.sphinx3	48	1441	649	<b><u>1442</u></b>	<b><u>649</u></b>	1445	647	48	1441	649	<b><u>1442</u></b>	<b><u>649</u></b>	1445	647

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2\_14.0\_updates/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

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Test sponsor: IBM Corporation  
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Test date: Jul-2014  
Hardware Availability: Dec-2013  
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### Platform Notes (Continued)

running on BT2-IVB Tue Jul 29 14:43:28 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2695 v2 @ 2.40GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      264501256 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux BT2-IVB 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 28 17:53
```

```
SPEC is set to: /cpu2006.1.2_14.0_updates
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_bt2ivb-lv_root
                ext4      265G  100G  153G   40% /
```

```
Additional information from dmidecode:
BIOS IBM -[B2E141AUS-1.50]- 05/08/2014
Memory:
16x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank
8x Not Specified Not Specified
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2\_14.0\_updates/libs/32:/cpu2006.1.2\_14.0\_updates/libs/64:/cpu2006.1.2\_14.0\_updates/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

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**Software Availability:** Sep-2013

## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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