



# SPEC® CFP2006 Result

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## NEC Corporation

### SPECfp®\_rate2006 = 127

### Express5800/R110g-1E (Intel Xeon E3-1240L v3)

### SPECfp\_rate\_base2006 = 123

CPU2006 license: 9006

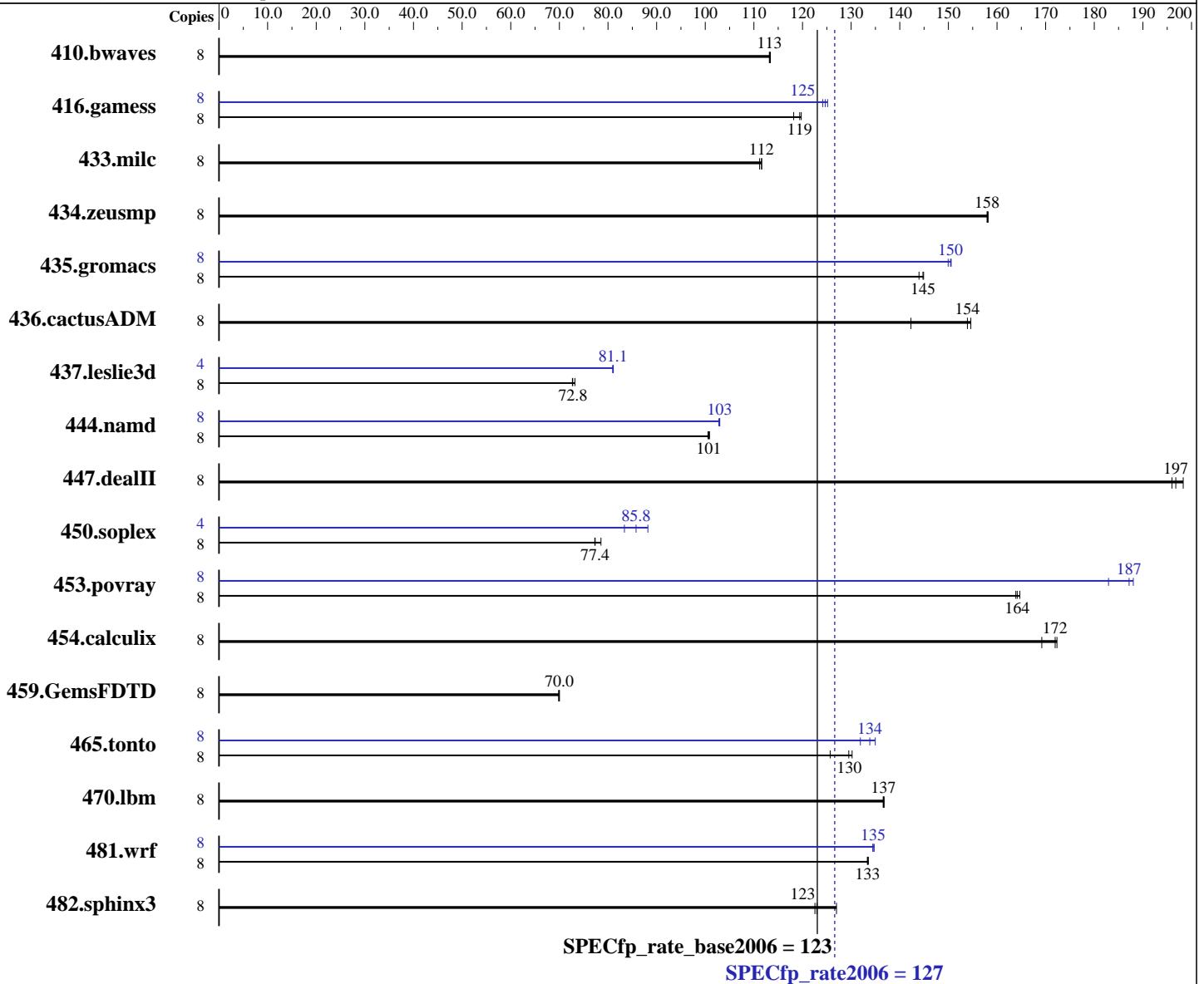
Test date: Jul-2014

Test sponsor: NEC Corporation

Hardware Availability: Jul-2014

Tested by: NEC Corporation

Software Availability: Jan-2014



#### Hardware

CPU Name: Intel Xeon E3-1240L v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Kernel 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.2.144 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<b>960</b>	<b>113</b>	961	113	959	113	8	<b>960</b>	<b>113</b>	961	113	959	113
416.gamess	8	1325	118	<b>1312</b>	<b>119</b>	1308	120	8	1252	125	1262	124	<b>1257</b>	<b>125</b>
433.milc	8	658	112	<b>658</b>	<b>112</b>	661	111	8	658	112	<b>658</b>	<b>112</b>	661	111
434.zeusmp	8	461	158	<b>461</b>	<b>158</b>	460	158	8	461	158	<b>461</b>	<b>158</b>	460	158
435.gromacs	8	394	145	<b>395</b>	<b>145</b>	397	144	8	379	151	381	150	<b>380</b>	<b>150</b>
436.cactusADM	8	672	142	<b>621</b>	<b>154</b>	618	155	8	672	142	<b>621</b>	<b>154</b>	618	155
437.leslie3d	8	<b>1034</b>	<b>72.8</b>	1027	73.2	1035	72.7	4	464	81.1	465	80.9	<b>464</b>	<b>81.1</b>
444.namd	8	638	101	<b>637</b>	<b>101</b>	636	101	8	623	103	<b>623</b>	<b>103</b>	624	103
447.dealII	8	467	196	<b>465</b>	<b>197</b>	462	198	8	467	196	<b>465</b>	<b>197</b>	462	198
450.soplex	8	<b>862</b>	<b>77.4</b>	849	78.6	863	77.3	4	378	88.2	400	83.4	<b>389</b>	<b>85.8</b>
453.povray	8	258	165	<b>259</b>	<b>164</b>	260	164	8	233	183	<b>227</b>	<b>187</b>	226	188
454.calculix	8	<b>384</b>	<b>172</b>	390	169	383	172	8	<b>384</b>	<b>172</b>	390	169	383	172
459.GemsFDTD	8	<b>1213</b>	<b>70.0</b>	1212	70.0	1215	69.8	8	<b>1213</b>	<b>70.0</b>	1212	70.0	1215	69.8
465.tonto	8	605	130	<b>608</b>	<b>130</b>	626	126	8	597	132	<b>588</b>	<b>134</b>	583	135
470.lbm	8	<b>804</b>	<b>137</b>	805	137	804	137	8	<b>804</b>	<b>137</b>	805	137	804	137
481.wrf	8	<b>670</b>	<b>133</b>	669	134	670	133	8	<b>664</b>	<b>135</b>	663	135	665	134
482.sphinx3	8	1228	127	<b>1268</b>	<b>123</b>	1272	123	8	1228	127	<b>1268</b>	<b>123</b>	1272	123

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Energy Performance: Performance



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1 > /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

459.GemsFDTD: -DSPEC\_CPU\_LP64

465.tonto: -DSPEC\_CPU\_LP64

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14  
 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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