SPEC® CFP2006 Result

Huawei

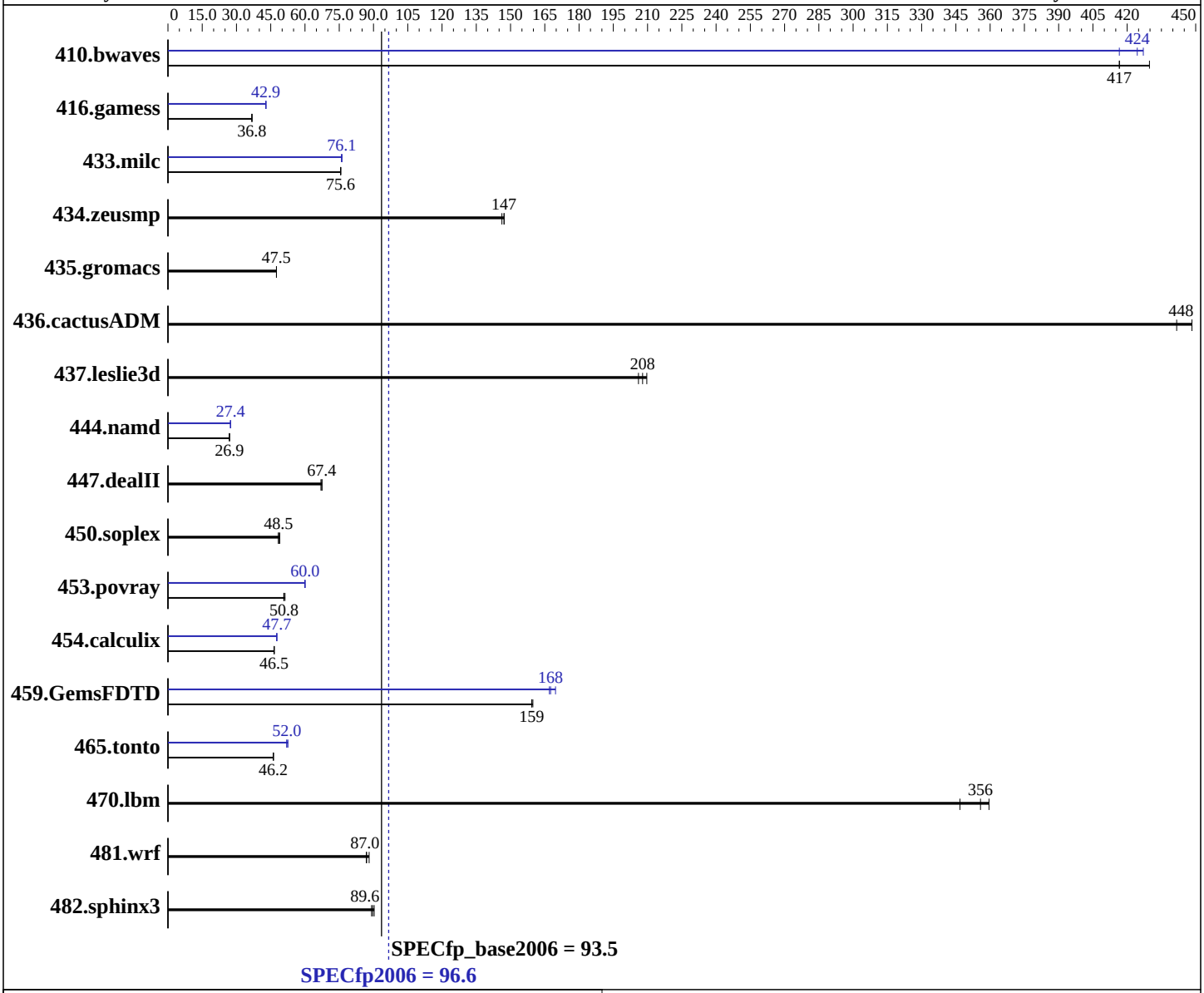
Huawei CH121 (Intel Xeon E5-2637 v2)

SPECfp®2006 = 96.6

SPECfp_base2006 = 93.5

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Jun-2014
Hardware Availability: Sep-2013
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E5-2637 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	15 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	31.6	430	32.6	417	**32.6**	**417**	32.6	417	**32.0**	**424**	31.8	427
416.gamess	533	36.7	**533**	**36.8**	532	36.8	457	42.9	456	42.9	**457**	**42.9**
433.milc	121	75.7	122	75.6	**121**	**75.6**	120	76.2	**121**	**76.1**	121	76.0
434.zeusmp	61.8	147	62.2	146	**61.8**	**147**	61.8	147	62.2	146	**61.8**	**147**
435.gromacs	150	47.5	**150**	**47.5**	150	47.5	150	47.5	**150**	**47.5**	150	47.5
436.cactusADM	27.1	442	26.7	448	**26.7**	**448**	27.1	442	26.7	448	**26.7**	**448**
437.leslie3d	**45.2**	**208**	44.8	210	45.6	206	**45.2**	**208**	44.8	210	45.6	206
444.namd	298	26.9	**298**	**26.9**	298	26.9	293	27.4	**293**	**27.4**	293	27.4
447.dealII	171	67.0	**170**	**67.4**	170	67.4	171	67.0	**170**	**67.4**	170	67.4
450.soplex	**172**	**48.5**	172	48.4	170	49.0	**172**	**48.5**	172	48.4	170	49.0
453.povray	105	50.7	**105**	**50.8**	104	51.3	88.8	59.9	**88.6**	**60.0**	88.5	60.1
454.calculix	177	46.5	177	46.5	**177**	**46.5**	173	47.6	173	47.7	**173**	**47.7**
459.GemsFDTD	66.4	160	66.6	159	**66.6**	**159**	**63.3**	**168**	63.5	167	62.5	170
465.tonto	213	46.1	213	46.2	**213**	**46.2**	**189**	**52.0**	189	52.0	187	52.5
470.lbm	**38.6**	**356**	39.6	347	38.2	360	**38.6**	**356**	39.6	347	38.2	360
481.wrf	127	88.1	129	86.9	**128**	**87.0**	127	88.1	129	86.9	**128**	**87.0**
482.sphinx3	216	90.3	**217**	**89.6**	219	89.1	216	90.3	**217**	**89.6**	219	89.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Fri Jun 27 16:25:18 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2637 v2 @ 3.50GHz
```

Continued on next page

Platform Notes (Continued)

```
        2 "physical id"s (chips)
        8 "processors"
     cores, siblings (Caution: counting these is hw and system dependent.  The
     following excerpts from /proc/cpuinfo might not be reliable.  Use with
     caution.)
        cpu cores : 4
        siblings  : 4
        physical 0: cores 1 2 3 4
        physical 1: cores 1 2 3 4
     cache size : 15360 KB

  From /proc/meminfo
     MemTotal:       264478184 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.5 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
     EST 2013 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jun 27 04:21

  SPEC is set to: /spec
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda1      ext4  410G   13G  376G   4% /

  Additional information from dmidecode:
    Memory:
     16x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "8"

 Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory
 using RHEL 6.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Continued on next page

General Notes (Continued)

```
The Huawei CH121 and CH220 and CH221 and
CH222 models are electronically equivalent.
The results have been measured on a Huawei CH121 model
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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