



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = **75.2**

Huawei RH2288 v2 (Intel Xeon E5-2620 v2)

SPECfp_base2006 = **72.5**

CPU2006 license: 3175

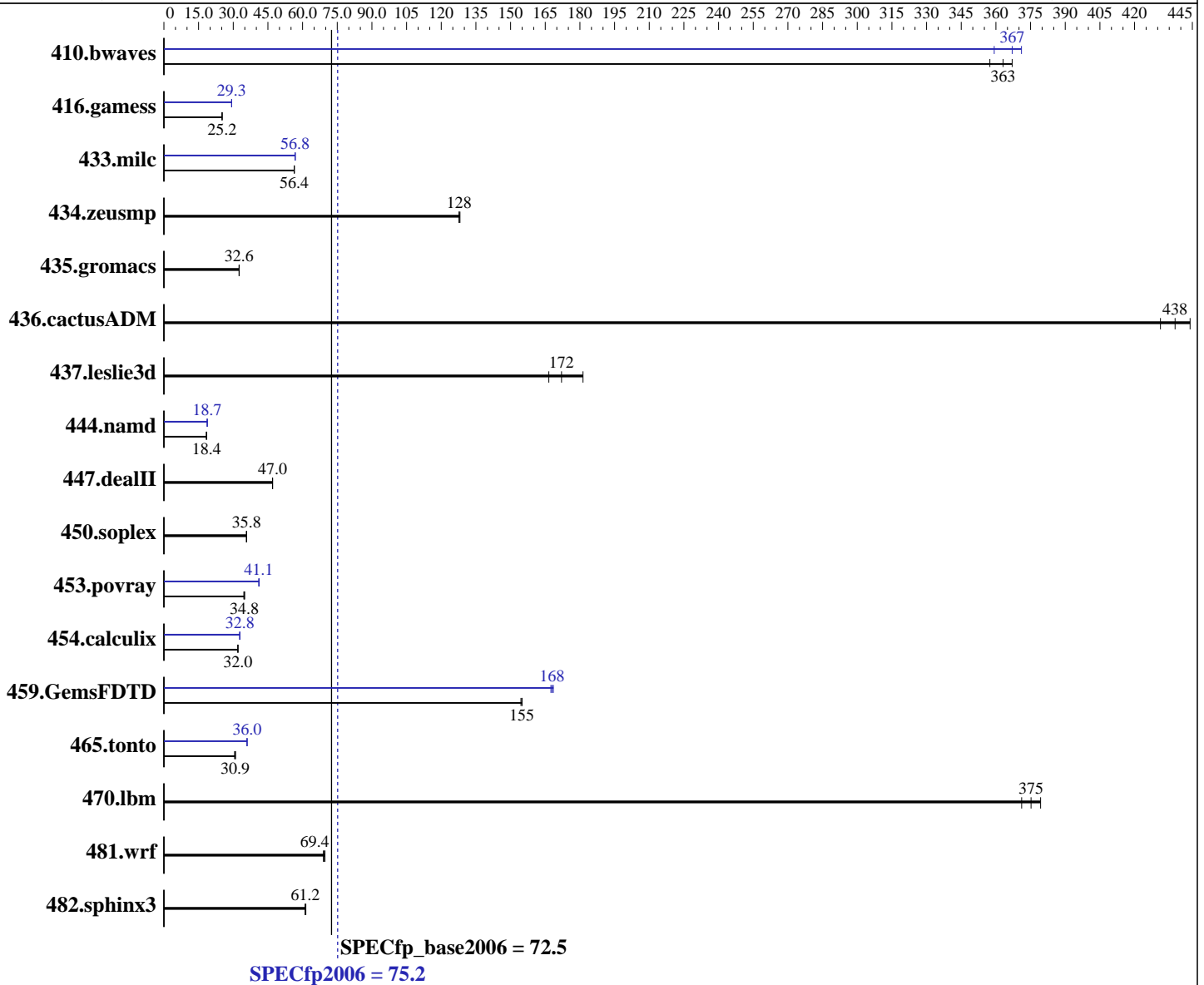
Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2620 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10000RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 38.0 | 357 | 37.0 | 367 | 37.4 | 363 | 37.0 | 367 | 36.6 | 371 | 37.8 | 359 |
| 416.gamess | 778 | 25.2 | 777 | 25.2 | 778 | 25.2 | 668 | 29.3 | 668 | 29.3 | 668 | 29.3 |
| 433.milc | 163 | 56.4 | 163 | 56.4 | 163 | 56.4 | 162 | 56.8 | 162 | 56.8 | 162 | 56.8 |
| 434.zeusmp | 71.0 | 128 | 71.2 | 128 | 71.2 | 128 | 71.0 | 128 | 71.2 | 128 | 71.2 | 128 |
| 435.gromacs | 219 | 32.6 | 219 | 32.6 | 220 | 32.5 | 219 | 32.6 | 219 | 32.6 | 220 | 32.5 |
| 436.cactusADM | 27.7 | 431 | 26.9 | 444 | 27.3 | 438 | 27.7 | 431 | 26.9 | 444 | 27.3 | 438 |
| 437.leslie3d | 54.6 | 172 | 56.4 | 167 | 51.8 | 181 | 54.6 | 172 | 56.4 | 167 | 51.8 | 181 |
| 444.namd | 435 | 18.4 | 435 | 18.4 | 436 | 18.4 | 428 | 18.7 | 428 | 18.7 | 428 | 18.7 |
| 447.dealII | 243 | 47.0 | 243 | 47.0 | 243 | 47.0 | 243 | 47.0 | 243 | 47.0 | 243 | 47.0 |
| 450.soplex | 233 | 35.8 | 234 | 35.7 | 233 | 35.8 | 233 | 35.8 | 234 | 35.7 | 233 | 35.8 |
| 453.povray | 153 | 34.8 | 154 | 34.6 | 152 | 34.9 | 129 | 41.2 | 130 | 41.1 | 130 | 41.1 |
| 454.calculix | 258 | 32.0 | 258 | 32.0 | 257 | 32.1 | 251 | 32.8 | 251 | 32.8 | 251 | 32.8 |
| 459.GemsFDTD | 68.5 | 155 | 68.5 | 155 | 68.7 | 154 | 63.2 | 168 | 63.4 | 167 | 63.0 | 169 |
| 465.tonto | 318 | 31.0 | 319 | 30.9 | 322 | 30.6 | 274 | 36.0 | 274 | 36.0 | 273 | 36.0 |
| 470.lbm | 36.2 | 379 | 37.0 | 371 | 36.6 | 375 | 36.2 | 379 | 37.0 | 371 | 36.6 | 375 |
| 481.wrf | 162 | 69.0 | 161 | 69.4 | 160 | 69.7 | 162 | 69.0 | 161 | 69.4 | 160 | 69.7 |
| 482.sphinx3 | 319 | 61.1 | 319 | 61.2 | 317 | 61.4 | 319 | 61.1 | 319 | 61.2 | 317 | 61.4 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Baseboard Management Controller used to adjust the fan speed to 100%
 Sysinfo program /spec/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on localhost Sun Jun 15 00:38:55 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2620 v2 @ 2.10GHz
 2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jun 14 17:18

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  272G   78G  180G  31% /

```

Additional information from dmidecode:

```

Memory:
13x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
3x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank

```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "12"

```

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General Notes (Continued)

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

The Huawei RH2288H v2 and Huawei RH2288 v2 and

the Huawei RH1288 v2 models are electronically equivalent.

The results have been measured on a Huawei RH2288H v2 model

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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