



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 67.1

### Huawei RH2285H V2 (Intel Xeon E5-2430)

SPECfp\_base2006 = 64.6

CPU2006 license: 3175

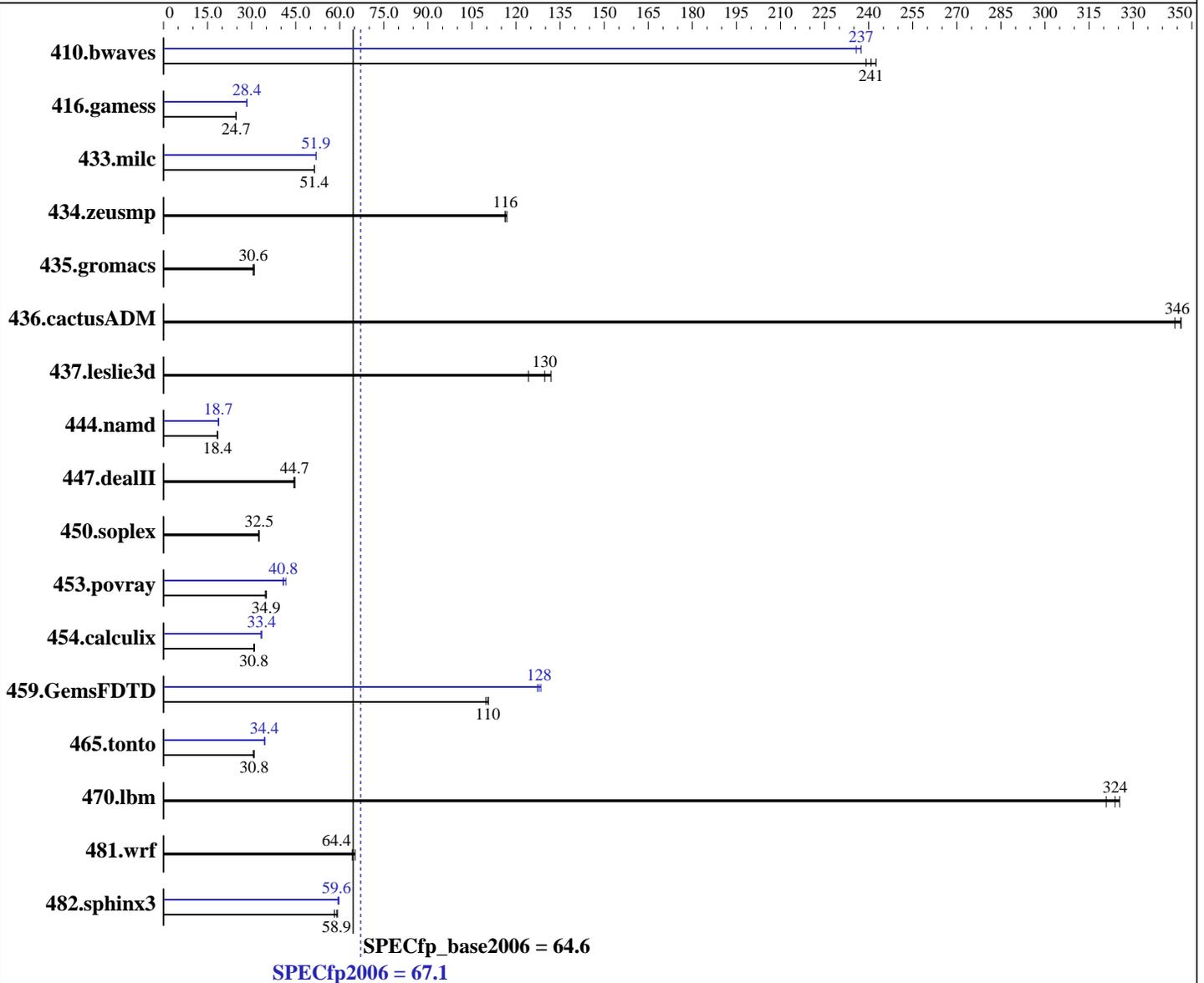
Test date: Jun-2012

Test sponsor: Huawei

Hardware Availability: May-2012

Tested by: Huawei

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2430  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>56.4</b>	<b>241</b>	56.0	243	56.8	239	57.6	236	<b>57.2</b>	<b>237</b>	57.2	237
416.gamess	793	24.7	791	24.8	<b>793</b>	<b>24.7</b>	<b>689</b>	<b>28.4</b>	691	28.3	689	28.4
433.milc	179	51.3	<b>179</b>	<b>51.4</b>	179	51.4	177	52.0	177	51.9	<b>177</b>	<b>51.9</b>
434.zeusmp	<b>78.2</b>	<b>116</b>	77.8	117	78.2	116	<b>78.2</b>	<b>116</b>	77.8	117	78.2	116
435.gromacs	231	30.9	234	30.5	<b>233</b>	<b>30.6</b>	231	30.9	234	30.5	<b>233</b>	<b>30.6</b>
436.cactusADM	34.5	346	<b>34.5</b>	<b>346</b>	34.7	344	34.5	346	<b>34.5</b>	<b>346</b>	34.7	344
437.leslie3d	71.3	132	75.7	124	<b>72.5</b>	<b>130</b>	71.3	132	75.7	124	<b>72.5</b>	<b>130</b>
444.namd	<b>436</b>	<b>18.4</b>	436	18.4	436	18.4	428	18.7	428	18.7	<b>428</b>	<b>18.7</b>
447.dealII	258	44.4	<b>256</b>	<b>44.7</b>	256	44.7	258	44.4	<b>256</b>	<b>44.7</b>	256	44.7
450.soplex	256	32.5	<b>256</b>	<b>32.5</b>	258	32.3	256	32.5	<b>256</b>	<b>32.5</b>	258	32.3
453.povray	154	34.6	152	35.0	<b>152</b>	<b>34.9</b>	128	41.6	131	40.7	<b>130</b>	<b>40.8</b>
454.calculix	266	31.0	<b>268</b>	<b>30.8</b>	268	30.8	249	33.1	247	33.4	<b>247</b>	<b>33.4</b>
459.GemsFDTD	96.7	110	95.9	111	<b>96.1</b>	<b>110</b>	<b>83.0</b>	<b>128</b>	83.4	127	82.6	128
465.tonto	322	30.6	319	30.8	<b>319</b>	<b>30.8</b>	<b>286</b>	<b>34.4</b>	287	34.3	285	34.5
470.lbm	<b>42.4</b>	<b>324</b>	42.2	325	42.8	321	<b>42.4</b>	<b>324</b>	42.2	325	42.8	321
481.wrf	174	64.3	<b>174</b>	<b>64.4</b>	171	65.2	174	64.3	<b>174</b>	<b>64.4</b>	171	65.2
482.sphinx3	<b>331</b>	<b>58.9</b>	335	58.2	329	59.2	<b>327</b>	<b>59.6</b>	326	59.7	328	59.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 Select only test related files when installing the operating system

## Platform Notes

BIOS configuration:  
 Intel Hyper-Threading set to Disabled  
 Set Power Efficiency Mode to Performance  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on DH310-2 Sat Jun 16 00:50:20 2012

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2430 0 @ 2.20GHz
 2 "physical id"s (chips)
 12 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings   : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      99030424 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux DH310-2 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 15 17:27

SPEC is set to: /spec

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdal        ext4      289G  21G  254G   8% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "12"
```

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

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## General Notes (Continued)

The Huawei RH2285 v2 and Huawei RH2285H v2 models are electronically equivalent.  
The results have been measured on a Huawei RH2285 v2 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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