



SPEC® CFP2006 Result

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Huawei

Huawei CH242 v3

SPECfp®2006 =

SPECfp_base2006 = NC

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Apr-2014

Hardware Availability: Feb-2014

Software Availability: Nov-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Huawei or Intel.

410.bwaves |

416.gamess |

433.milc |

434.zeusmp |

435.gromacs |

436.cactusADM |

437.leslie3d |

444.namd |

447.dealII |

450.soplex |

453.povray |

454.calculix |

459.GemsFDTD |

465.tonto |

470.lbm |

481.wrf |

482.sphinx3 |



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Hardware		Software	
CPU Name:	Intel Xeon E7-4809 v2	Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago)
CPU Characteristics:	1900	Compiler:	2.6.32-431.el6.x86_64
CPU MHz:	Integrated	C/C++:	Version 14.0.0.080 of Intel C++ Studio XE for Linux;
FPU:	24 cores, 4 chips, 6 cores/chip	Fortran:	Version 14.0.0.080 of Intel Fortran Studio XE for Linux
CPU(s) enabled:	2,4 chip	Auto Parallel:	Yes
CPU(s) orderable:	32 KB I + 32 KB D on chip per core	File System:	ext4
Primary Cache:	256 KB I+D on chip per core	System State:	Run level 3 (multi-user)
Secondary Cache:	12 MB I+D on chip per chip	Base Pointers:	64-bit
L3 Cache:	None	Peak Pointers:	32/64-bit
Other Cache:		Other Software:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-10600R-09, ECC)		
Disk Subsystem:	1 X 300 GB SATA 7200RPM		
Other Hardware:	None		



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	NC	NC										
416.gamess	NC	NC										
433.milc	NC	NC										
434.zeusmp	NC	NC										
435.gromacs	NC	NC										
436.cactusADM	NC	NC										
437.leslie3d	NC	NC										
444.namd	NC	NC										
447.dealII	NC	NC										
450.soplex	NC	NC										
453.povray	NC	NC										
454.calculix	NC	NC										
459.GemsFDTD	NC	NC										
465.tonto	NC	NC										
470.lbm	NC	NC										
481.wrf	NC	NC										
482.sphinx3	NC	NC										

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set VMSE LockStep mode disable

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on localhost.localdomain Sun Apr 27 05:15:54 2014

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Platform Notes (Continued)

This section contains SUT (System Under Test) information seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4809 v2 @ 1.90GHz
 4 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 6
 siblings : 6
 physical 0: cores 0 1 2 3 4 5
 physical 1: cores 0 1 2 3 4 5
 physical 2: cores 0 1 2 3 4 5
 physical 3: cores 0 1 2 3 4 5
cache size : 12288 K

From /proc/meminfo
MemTotal: 26661272 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

lsb_release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 26 17:06

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
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Platform Notes (Continued)

/dev/sda2 ext4 265G 97G 155G 39% /

Additional information from dmidecode:

BIOS American Megatrends Inc. BLISV033 02/27/2014

Memory:

32x 8 GB
8x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
24x Samsung M393B1K70DH0-CH9 8 GB 1333 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec/lib32:/spec/libs/64:/spec/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command is invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -fprec-div -parallel -fopt-prefetch -fansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -fopt-prefetch -fansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -fopt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -fopt-prefetch -fansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX -pass 2 -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

473.1m: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.lamd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

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Peak Optimization Flags (Continued)

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-optimal-alloc-options=3 -auto -unroll4

Benchmarks using both Intel and Z:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.1.1.1: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact webmaster@spec.org.

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