



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 1430

PowerEdge R920 (Intel Xeon E7-8891 v2, 3.20 GHz)

SPECfp_rate_base2006 = 1390

CPU2006 license: 55

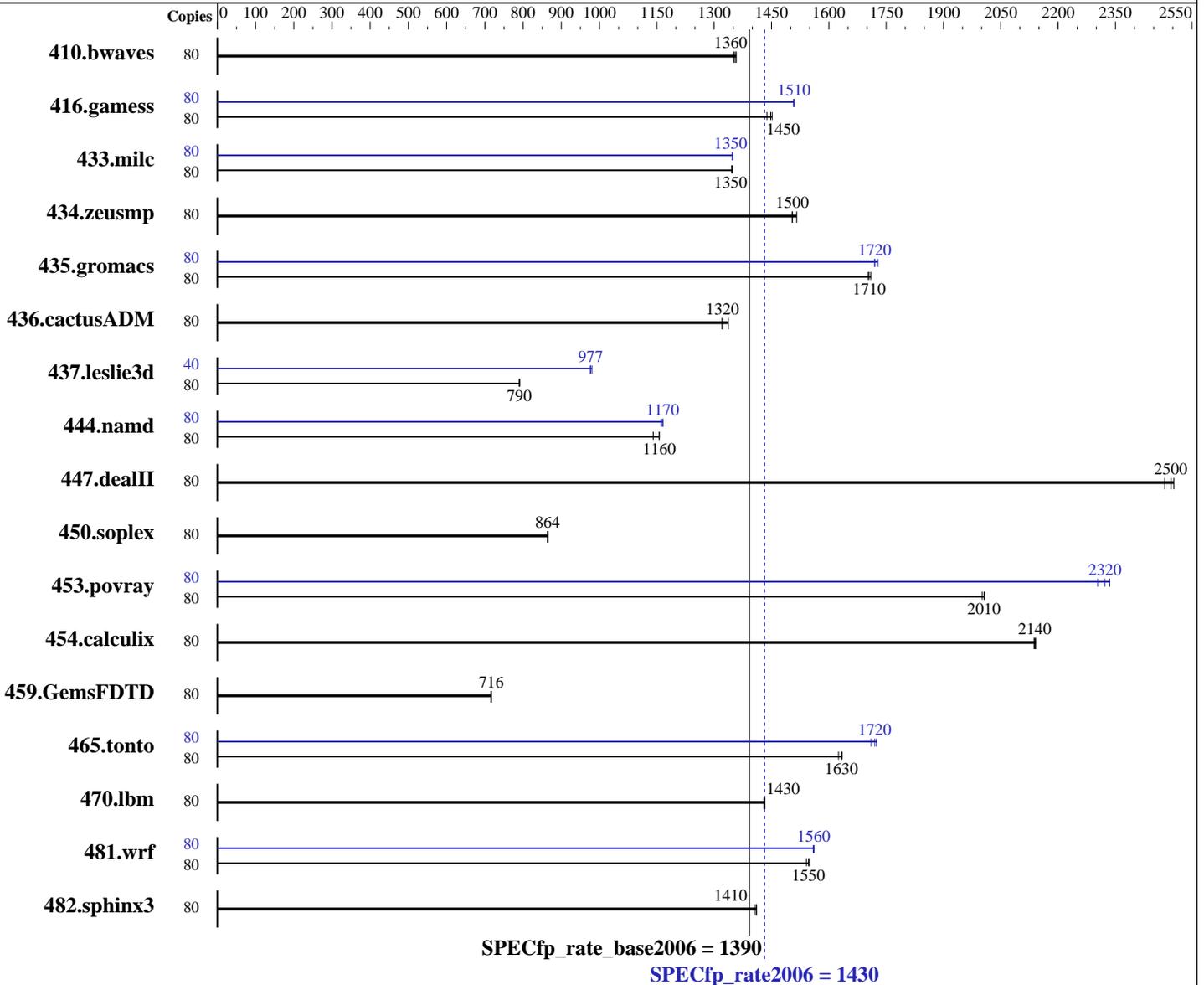
Test date: Feb-2014

Test sponsor: Dell Inc.

Hardware Availability: Mar-2014

Tested by: Dell Inc.

Software Availability: Mar-2014



Hardware

CPU Name: Intel Xeon E7-8891 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext2
 System State: Run level 3 (multi-user)

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L3 Cache: 37.5 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem: 1 x 400 GB SAS6 SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	804	1350	<u>802</u>	<u>1360</u>	801	1360	80	804	1350	<u>802</u>	<u>1360</u>	801	1360
416.gamess	80	1089	1440	<u>1082</u>	<u>1450</u>	1079	1450	80	1039	1510	1038	1510	<u>1038</u>	<u>1510</u>
433.milc	80	546	1350	<u>545</u>	<u>1350</u>	545	1350	80	545	1350	<u>545</u>	<u>1350</u>	545	1350
434.zeusmp	80	<u>484</u>	<u>1500</u>	484	1500	480	1520	80	<u>484</u>	<u>1500</u>	484	1500	480	1520
435.gromacs	80	334	1710	336	1700	<u>335</u>	<u>1710</u>	80	<u>332</u>	<u>1720</u>	330	1730	332	1720
436.cactusADM	80	<u>723</u>	<u>1320</u>	724	1320	715	1340	80	<u>723</u>	<u>1320</u>	724	1320	715	1340
437.leslie3d	80	950	791	952	790	<u>952</u>	<u>790</u>	40	384	980	<u>385</u>	<u>977</u>	385	976
444.namd	80	555	1160	<u>555</u>	<u>1160</u>	563	1140	80	<u>551</u>	<u>1170</u>	550	1170	552	1160
447.dealII	80	369	2480	<u>367</u>	<u>2500</u>	366	2500	80	369	2480	<u>367</u>	<u>2500</u>	366	2500
450.soplex	80	773	863	771	866	<u>772</u>	<u>864</u>	80	773	863	771	866	<u>772</u>	<u>864</u>
453.povray	80	213	2000	212	2010	<u>212</u>	<u>2010</u>	80	182	2340	<u>183</u>	<u>2320</u>	185	2300
454.calculix	80	308	2140	<u>309</u>	<u>2140</u>	309	2140	80	308	2140	<u>309</u>	<u>2140</u>	309	2140
459.GemsFDTD	80	<u>1185</u>	<u>716</u>	1185	716	1185	717	80	<u>1185</u>	<u>716</u>	1185	716	1185	717
465.tonto	80	484	1630	482	1630	<u>482</u>	<u>1630</u>	80	456	1720	460	1710	<u>457</u>	<u>1720</u>
470.lbm	80	767	1430	769	1430	<u>767</u>	<u>1430</u>	80	767	1430	769	1430	<u>767</u>	<u>1430</u>
481.wrf	80	577	1550	580	1540	<u>578</u>	<u>1550</u>	80	573	1560	<u>573</u>	<u>1560</u>	572	1560
482.sphinx3	80	1105	1410	<u>1106</u>	<u>1410</u>	1109	1410	80	1105	1410	<u>1106</u>	<u>1410</u>	1109	1410

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled

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Platform Notes (Continued)

System Profile set to Custom
 Memory Patrol Scrub set to disabled
 Sysinfo program
 /root/Desktop/Performance/ic14.0_Oct17_2013/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on slesperf3 Mon Feb 3 12:41:37 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E7-8891 v2 @ 3.20GHz
 4 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 10
  siblings     : 20
  physical 0: cores 2 3 4 5 6 7 8 10 11 12
  physical 1: cores 2 3 4 5 6 7 8 10 11 12
  physical 2: cores 2 3 4 5 6 7 8 10 11 12
  physical 3: cores 2 3 4 5 6 7 8 10 11 12
cache size     : 38400 KB
```

```
From /proc/meminfo
MemTotal:      1058789108 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux slesperf3 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Feb 3 12:20 last=S

```
SPEC is set to: /root/Desktop/Performance/ic14.0_Oct17_2013
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2  365G  234G  130G   65% /
```

Additional information from dmidecode:
 BIOS Dell Inc. 1.0.4 01/27/2014

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Platform Notes (Continued)

Memory:

```

31x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1333 MHz
19x 00CE00B300CE M393B2G70CB0-YK0 16 GB 1333 MHz
8x 00CE04B300CE M393B2G70BH0-YK0 16 GB 1333 MHz
6x 00CE04B300CE M393B2G70CB0-YK0 16 GB 1333 MHz

```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = */root/Desktop/Performance/ic14.0_Oct17_2013/libs/32:/root/Desktop/Performance/ic14.0_Oct17_2013/libs/64:/root/Desktop/Performance/ic14.0_Oct17_2013/sh*
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64

```

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Base Portability Flags (Continued)

```

447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml>

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