



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL560 Gen8
(2.20 GHz, Intel Xeon E5-4607)

SPECfp®_rate2006 = 152

SPECfp_rate_base2006 = 150

CPU2006 license: 3

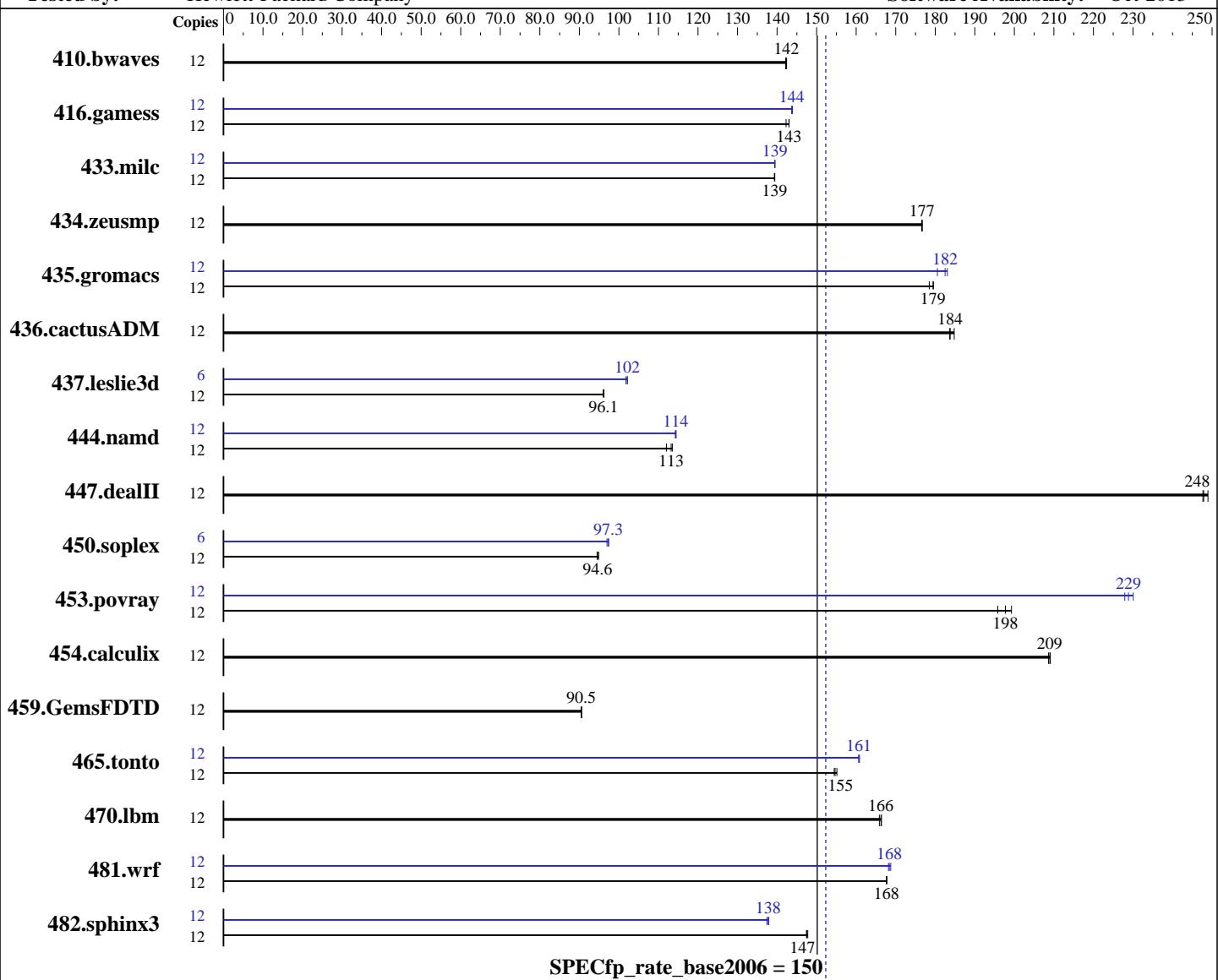
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Jan-2014

Hardware Availability: Dec-2013

Software Availability: Oct-2013



Hardware

CPU Name: Intel Xeon E5-4607
CPU Characteristics:
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.4, (Santiago)
Compiler: Kernel 2.6.32-358.el6.x86_64
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 5 x 300 GB 15 K SAS, RAID 5
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	<u>1145</u>	<u>142</u>	1147	142	1145	142	12	<u>1145</u>	<u>142</u>	1147	142	1145	142
416.gamess	12	<u>1643</u>	<u>143</u>	1643	143	1652	142	12	<u>1635</u>	<u>144</u>	1633	144	1635	144
433.milc	12	790	139	791	139	<u>790</u>	<u>139</u>	12	790	139	<u>790</u>	<u>139</u>	790	139
434.zeusmp	12	618	177	<u>618</u>	<u>177</u>	618	177	12	618	177	<u>618</u>	<u>177</u>	618	177
435.gromacs	12	<u>477</u>	<u>179</u>	477	180	480	178	12	468	183	<u>470</u>	<u>182</u>	475	181
436.cactusADM	12	<u>780</u>	<u>184</u>	781	184	776	185	12	<u>780</u>	<u>184</u>	781	184	776	185
437.leslie3d	12	1172	96.2	<u>1174</u>	<u>96.1</u>	1174	96.1	6	<u>552</u>	<u>102</u>	554	102	552	102
444.namd	12	859	112	<u>850</u>	<u>113</u>	848	114	12	842	114	<u>842</u>	<u>114</u>	841	114
447.dealII	12	551	249	<u>554</u>	<u>248</u>	554	248	12	551	249	<u>554</u>	<u>248</u>	554	248
450.soplex	12	1055	94.9	<u>1058</u>	<u>94.6</u>	1059	94.5	6	<u>514</u>	<u>97.3</u>	514	97.4	516	97.0
453.povray	12	326	196	<u>323</u>	<u>198</u>	320	199	12	277	230	<u>279</u>	<u>229</u>	280	228
454.calculix	12	475	209	<u>474</u>	<u>209</u>	473	209	12	475	209	<u>474</u>	<u>209</u>	473	209
459.GemsFDTD	12	1406	90.6	1407	90.5	<u>1407</u>	<u>90.5</u>	12	1406	90.6	1407	90.5	<u>1407</u>	<u>90.5</u>
465.tonto	12	764	154	761	155	<u>763</u>	<u>155</u>	12	734	161	<u>735</u>	<u>161</u>	735	161
470.lbm	12	991	166	<u>992</u>	<u>166</u>	994	166	12	991	166	<u>992</u>	<u>166</u>	994	166
481.wrf	12	<u>799</u>	<u>168</u>	800	168	799	168	12	794	169	797	168	<u>796</u>	<u>168</u>
482.sphinx3	12	1586	147	1583	148	<u>1586</u>	<u>147</u>	12	1696	138	<u>1698</u>	<u>138</u>	1702	137

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

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Operating System Notes (Continued)

```
numactl --interleave=all runspec <etc>
```

Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance
Memory Power Savings Mode set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled

```
Sysinfo program /home/cpu/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$
running on PL67 Sat Jan 25 07:32:31 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4607 0 @ 2.20GHz
        1 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 6
        siblings : 12
        physical 0: cores 0 1 2 3 4 5
cache size : 12288 KB
```

```
From /proc/meminfo
MemTotal:       132111656 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux PL67 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 24 15:43
```

```
SPEC is set to: /home/cpu
Filesystem      Type   Size  Used Avail Use% Mounted on
/dev/mapper/vg_pl67-lv_home
                  ext4    1.1T   50G  945G   5%  /home
```

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Platform Notes (Continued)

Additional information from dmidecode:

BIOS HP P77 12/20/2013

Memory:

8x HP 672612-081 16 GB 1067 MHz 2 rank

40x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:

8x HP 672612-081 16 GB 1067 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu/libs/32:/home/cpu/libs/64:/home/cpu/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: icpc -m32

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

```

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
    -unroll2

```

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>

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