



SPEC® CFP2006 Result

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Huawei

SPECfp®_rate2006 = Not Run

Huawei CH242 v3 (Intel Xeon E7-4890 v2)

SPECfp_rate_base2006 = 1710

CPU2006 license: 13

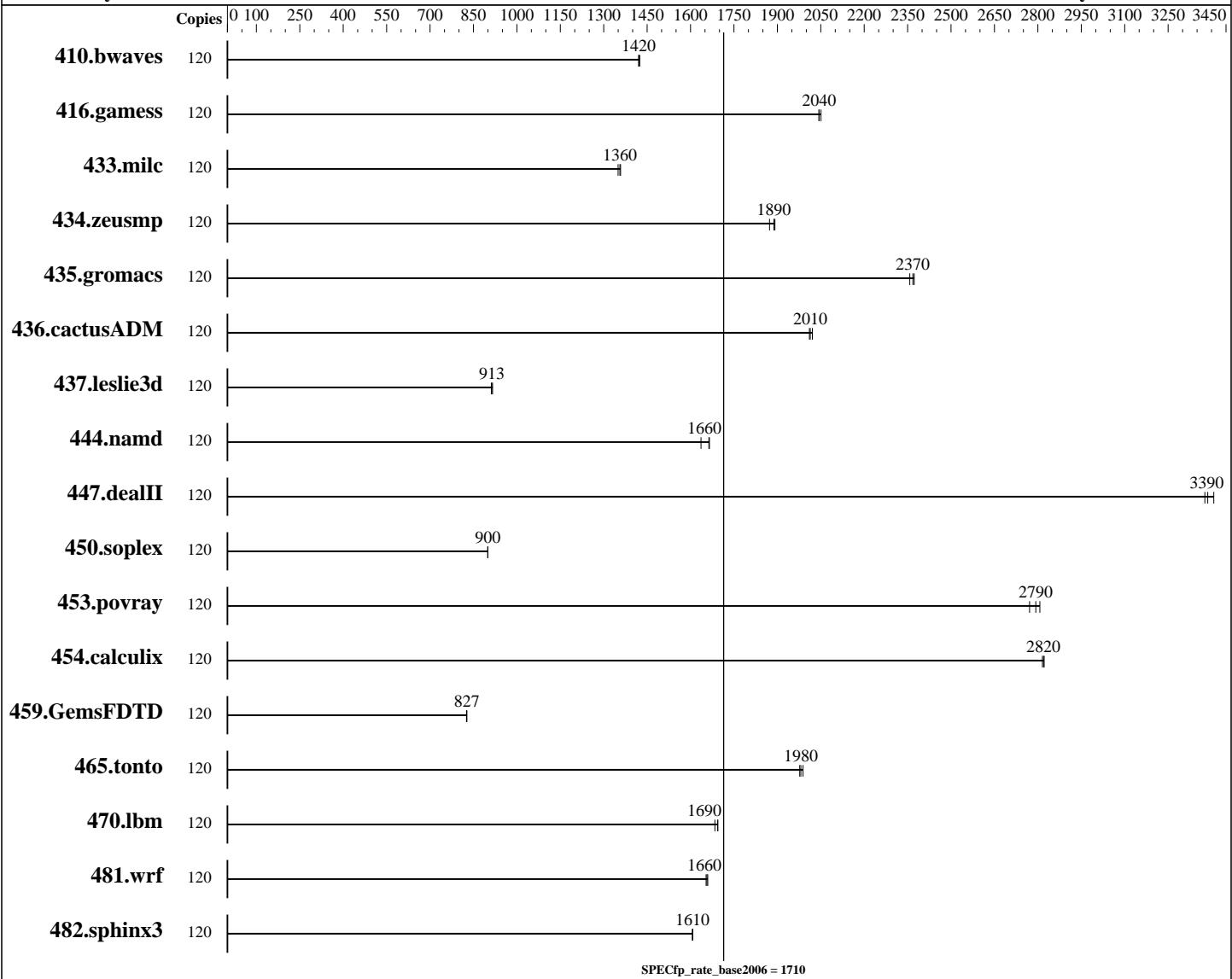
Test date: Feb-2014

Test sponsor: Huawei

Hardware Availability: Feb-2014

Tested by: Huawei

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E7-4890 v2
CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: 2.6.32-431.el6.x86_64
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
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L3 Cache: 37.5 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10 K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 120 | 1147 | 1420 | 1148 | 1420 | 1145 | 1420 | | | | | | | |
| 416.gamess | 120 | 1150 | 2040 | 1150 | 2040 | 1146 | 2050 | | | | | | | |
| 433.milc | 120 | 811 | 1360 | 816 | 1350 | 812 | 1360 | | | | | | | |
| 434.zeusmp | 120 | 577 | 1890 | 578 | 1890 | 583 | 1870 | | | | | | | |
| 435.gromacs | 120 | 363 | 2360 | 361 | 2370 | 362 | 2370 | | | | | | | |
| 436.cactusADM | 120 | 710 | 2020 | 712 | 2010 | 713 | 2010 | | | | | | | |
| 437.leslie3d | 120 | 1231 | 916 | 1237 | 912 | 1235 | 913 | | | | | | | |
| 444.namd | 120 | 588 | 1640 | 578 | 1670 | 579 | 1660 | | | | | | | |
| 447.dealII | 120 | 403 | 3410 | 407 | 3380 | 405 | 3390 | | | | | | | |
| 450.soplex | 120 | 1113 | 900 | 1112 | 900 | 1113 | 899 | | | | | | | |
| 453.povray | 120 | 227 | 2810 | 229 | 2790 | 230 | 2770 | | | | | | | |
| 454.calculix | 120 | 352 | 2820 | 351 | 2820 | 351 | 2820 | | | | | | | |
| 459.GemsFDTD | 120 | 1540 | 827 | 1542 | 826 | 1540 | 827 | | | | | | | |
| 465.tonto | 120 | 594 | 1990 | 596 | 1980 | 597 | 1980 | | | | | | | |
| 470.lbm | 120 | 979 | 1680 | 973 | 1690 | 973 | 1690 | | | | | | | |
| 481.wrf | 120 | 809 | 1660 | 811 | 1650 | 808 | 1660 | | | | | | | |
| 482.sphinx3 | 120 | 1455 | 1610 | 1455 | 1610 | 1456 | 1610 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Performance
 Set Lock_step to disabled

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Platform Notes (Continued)

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on speccpu Tue Feb 18 20:38:43 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-4890 v2 @ 2.80GHz

4 "physical id"s (chips)

120 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 15

siblings : 30

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14

physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14

physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14

physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14

cache size : 38400 KB

From /proc/meminfo

MemTotal: 264349504 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:

Linux speccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 18 20:36

SPEC is set to: /spec

| | | | | | | |
|------------|------|------|------|-------|------|------------|
| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
| /dev/sda1 | ext4 | 241G | 8.9G | 220G | 4% | / |

Additional information from dmidecode:

BIOS American Megatrends Inc. BLISV929 01/19/2014

Memory:

32x 8 GB

64x NO DIMM NO DIMM

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Platform Notes (Continued)

16x Samsung M393B1K70CH0-CH9 8 GB 1333 MHz 2 rank
16x Samsung M393B1K70DH0-CH9 8 GB 1333 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias  
-opt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20121120.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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