



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL380e Gen8
(1.90 GHz, Intel Xeon E5-2440 v2)

SPECfp®2006 = 64.6

SPECfp_base2006 = 62.1

CPU2006 license: 3

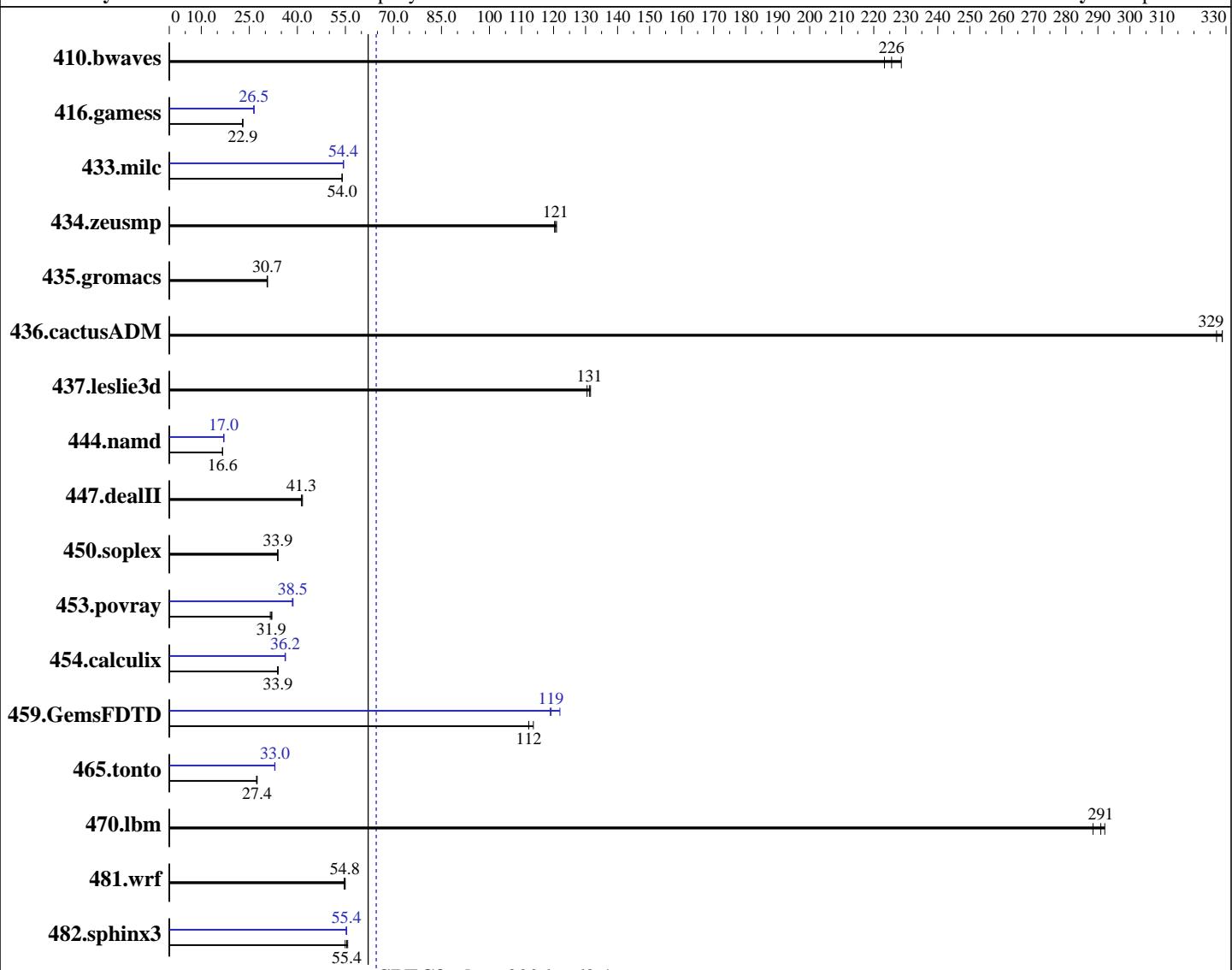
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Jan-2014

Hardware Availability: Jan-2014

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2440 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz: 1900
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3
Compiler: Kernel 3.0.76-0.11-default
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB 15 K SAS, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	60.8	223	59.4	229	60.2	226	60.8	223	59.4	229	60.2	226
416.gamess	853	23.0	855	22.9	854	22.9	740	26.5	742	26.4	740	26.5
433.milc	170	54.0	170	54.0	170	54.0	169	54.4	169	54.4	169	54.4
434.zeusmp	75.2	121	75.6	120	75.4	121	75.2	121	75.6	120	75.4	121
435.gromacs	234	30.5	233	30.7	233	30.7	234	30.5	233	30.7	233	30.7
436.cactusADM	36.3	329	36.5	327	36.3	329	36.3	329	36.5	327	36.3	329
437.leslie3d	71.7	131	72.1	130	71.5	132	71.7	131	72.1	130	71.5	132
444.namd	482	16.7	482	16.6	482	16.6	472	17.0	471	17.0	471	17.0
447.dealII	277	41.3	277	41.3	275	41.6	277	41.3	277	41.3	275	41.6
450.soplex	246	33.9	246	33.9	246	33.9	246	33.9	246	33.9	246	33.9
453.povray	169	31.5	167	31.9	166	32.1	138	38.5	138	38.7	138	38.5
454.calculix	244	33.9	243	34.0	244	33.8	228	36.2	228	36.3	228	36.2
459.GemsFDTD	93.3	114	94.5	112	94.5	112	87.0	122	89.0	119	89.2	119
465.tonto	361	27.2	359	27.4	359	27.4	299	32.9	298	33.0	298	33.0
470.lbm	47.2	291	47.6	288	47.0	292	47.2	291	47.6	288	47.0	292
481.wrf	204	54.6	203	54.9	204	54.8	204	54.6	203	54.9	204	54.8
482.sphinx3	350	55.7	352	55.4	355	54.9	353	55.2	352	55.4	352	55.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

Reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

runspec command invoked through numactl i.e.:

```
numactl --localalloc runspec <etc>
```



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Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled
HP Power Profile set to Maximum Performance
Minimum Processor Idle Power Core State set to C1E state
Minimum Processor Idle Power Package State set to Package C6 (retention) State
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x

```
Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$
running on dl380e-gen8-rf0 Thu Jan 23 01:38:59 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2440 v2 @ 1.90GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 8
        siblings : 8
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      99034532 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 11 (x86_64)
        VERSION = 11
        PATCHLEVEL = 3
```

```
uname -a:
Linux dl380e-gen8-rf0 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 22 17:28 last=S
```

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Platform Notes (Continued)

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        ext3  273G   28G  232G  11%  /
```

Additional information from dmidecode:

BIOS HP P73 11/12/2013

Memory:

12x HP 689911-071 8 GB 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/libs64:/cpu2006/sh"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>

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