



SPEC[®] CFP2006 Result

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NEC Corporation

SPECfp[®]2006 = **87.5**

Express5800/R120e-2E (Intel Xeon E5-2470 v2)

SPECfp_base2006 = **83.8**

CPU2006 license: 9006

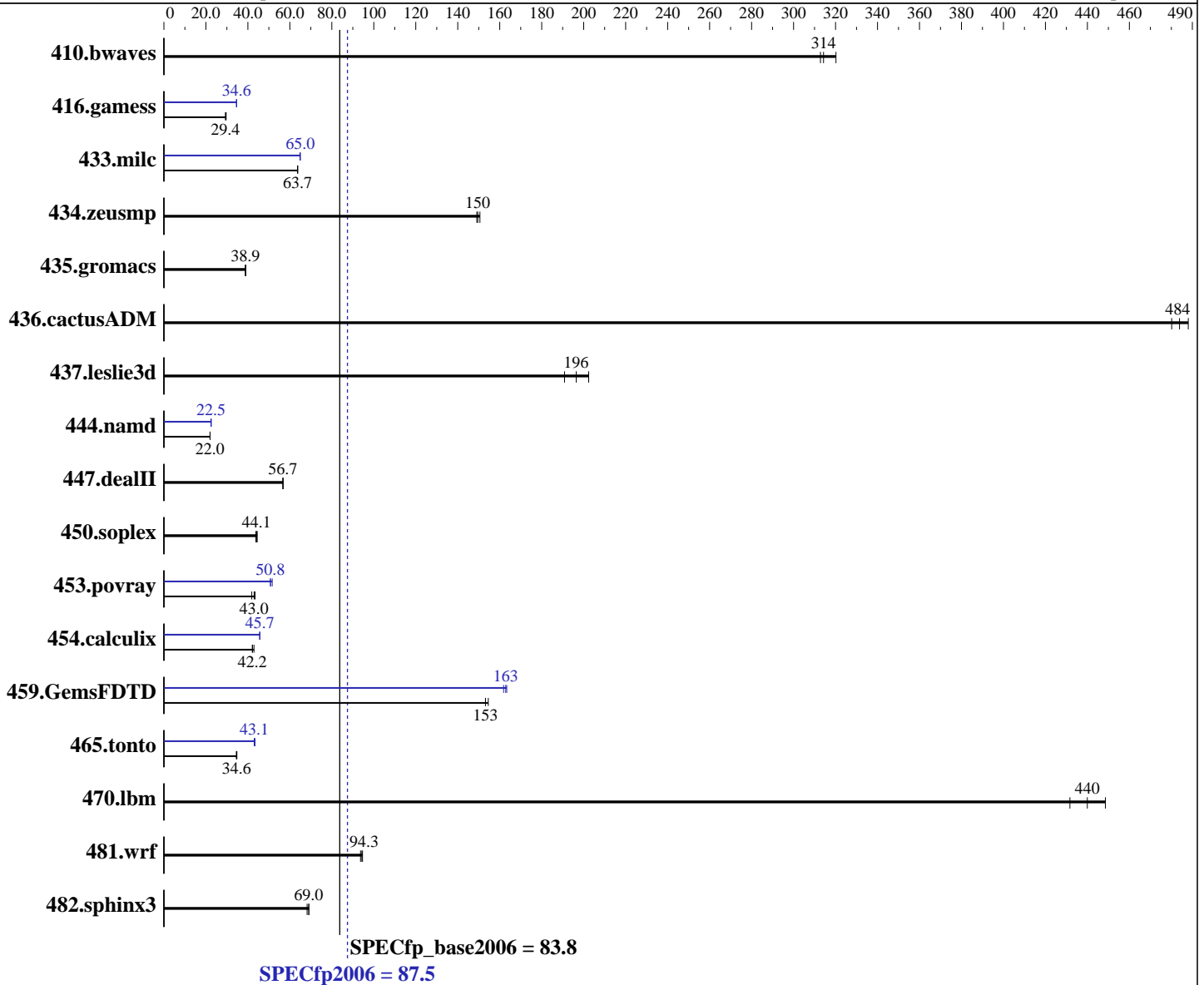
Test date: Dec-2013

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2470 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.18.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	42.4	320	<u>43.2</u>	<u>314</u>	43.4	313	42.4	320	<u>43.2</u>	<u>314</u>	43.4	313
416.gamess	666	29.4	660	29.6	667	29.4	<u>566</u>	<u>34.6</u>	566	34.6	565	34.6
433.milc	144	63.7	144	63.6	144	63.7	142	64.8	141	65.0	141	65.0
434.zeusmp	60.8	150	60.4	151	61.0	149	<u>60.8</u>	<u>150</u>	60.4	151	61.0	149
435.gromacs	184	38.9	183	39.0	184	38.8	<u>184</u>	<u>38.9</u>	183	39.0	184	38.8
436.cactusADM	24.7	484	24.9	480	24.5	488	<u>24.7</u>	<u>484</u>	24.9	480	24.5	488
437.leslie3d	47.8	196	49.2	191	46.4	202	<u>47.8</u>	<u>196</u>	49.2	191	46.4	202
444.namd	365	22.0	365	22.0	365	22.0	357	22.5	357	22.5	357	22.5
447.dealII	202	56.7	202	56.7	202	56.8	202	56.7	202	56.7	202	56.8
450.soplex	189	44.1	188	44.5	190	43.8	<u>189</u>	<u>44.1</u>	188	44.5	190	43.8
453.povray	124	43.0	122	43.5	127	41.9	105	50.7	103	51.6	105	50.8
454.calculix	192	42.9	196	42.1	195	42.2	<u>181</u>	<u>45.7</u>	181	45.7	180	45.7
459.GemsFDTD	68.7	155	69.2	153	69.3	153	<u>65.2</u>	<u>163</u>	65.0	163	65.6	162
465.tonto	283	34.8	284	34.6	285	34.5	227	43.4	229	43.1	228	43.1
470.lbm	31.2	440	31.8	432	30.6	449	<u>31.2</u>	<u>440</u>	31.8	432	30.6	449
481.wrf	118	94.6	118	94.3	119	93.7	118	94.6	118	94.3	119	93.7
482.sphinx3	286	68.2	283	69.0	282	69.1	286	68.2	283	69.0	282	69.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
 Energy Performance: Performance
 Memory Voltage: 1.5 V

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact,1,0"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

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General Notes (Continued)

OMP_NUM_THREADS = "20"

The Express5800/R120e-1E and the Express5800/R120e-2E models are electronically equivalent. The results have been measured on the Express5800/R120e-2E model.

Added glibc-static-2.12-1.107.el6.x86_64.rpm to enable static linking
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>



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For other inquiries, please contact webmaster@spec.org.

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