



# SPEC<sup>®</sup> CFP2006 Result

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## Dell Inc.

PowerEdge M520 (Intel Xeon E5-2470 v2, 2.40 GHz)

**SPECfp<sup>®</sup>2006 = 80.4**

**SPECfp\_base2006 = 76.8**

CPU2006 license: 55

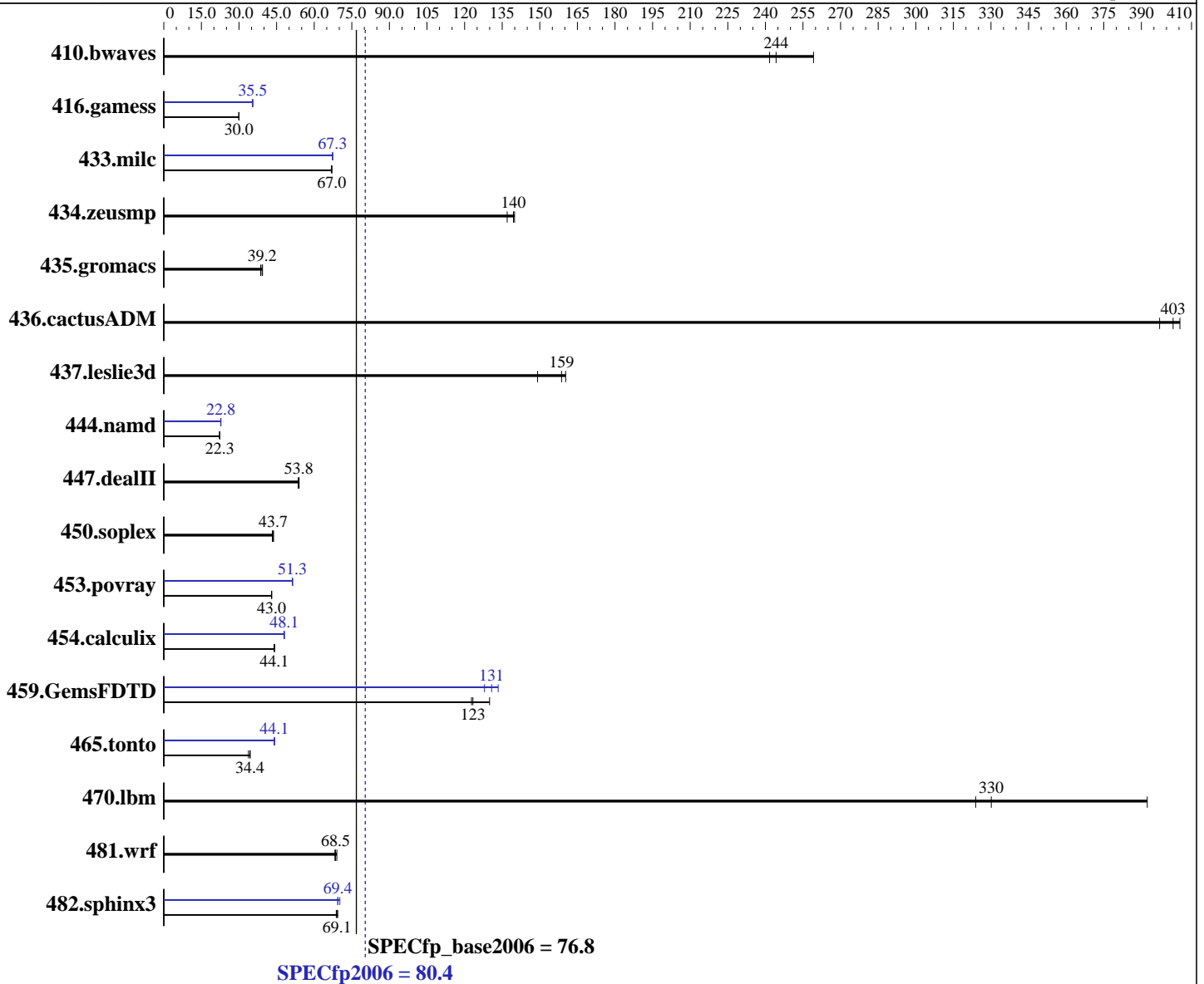
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Nov-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2470 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
Disk Subsystem: 1 x 1 TB 7200 RPM SATA  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	52.4	259	<b>55.6</b>	<b>244</b>	56.2	242	52.4	259	<b>55.6</b>	<b>244</b>	56.2	242
416.gamess	655	29.9	<b>654</b>	<b>30.0</b>	652	30.0	552	35.5	<b>552</b>	<b>35.5</b>	552	35.4
433.milc	<b>137</b>	<b>67.0</b>	137	67.0	137	66.9	136	67.4	136	67.3	<b>136</b>	<b>67.3</b>
434.zeusmp	65.0	140	<b>65.2</b>	<b>140</b>	66.4	137	65.0	140	<b>65.2</b>	<b>140</b>	66.4	137
435.gromacs	185	38.7	181	39.4	<b>182</b>	<b>39.2</b>	185	38.7	181	39.4	<b>182</b>	<b>39.2</b>
436.cactusADM	29.5	405	30.1	397	<b>29.7</b>	<b>403</b>	29.5	405	30.1	397	<b>29.7</b>	<b>403</b>
437.leslie3d	58.6	160	63.0	149	<b>59.2</b>	<b>159</b>	58.6	160	63.0	149	<b>59.2</b>	<b>159</b>
444.namd	360	22.3	<b>360</b>	<b>22.3</b>	359	22.3	353	22.7	352	22.8	<b>352</b>	<b>22.8</b>
447.dealII	213	53.6	212	54.0	<b>213</b>	<b>53.8</b>	213	53.6	212	54.0	<b>213</b>	<b>53.8</b>
450.soplex	193	43.3	<b>191</b>	<b>43.7</b>	191	43.7	193	43.3	<b>191</b>	<b>43.7</b>	191	43.7
453.povray	<b>124</b>	<b>43.0</b>	124	43.0	124	43.0	104	51.3	104	51.4	<b>104</b>	<b>51.3</b>
454.calculix	187	44.2	<b>187</b>	<b>44.1</b>	187	44.1	<b>172</b>	<b>48.1</b>	172	48.1	172	48.0
459.GemsFDTD	81.6	130	<b>86.0</b>	<b>123</b>	86.4	123	<b>81.1</b>	<b>131</b>	82.9	128	79.5	133
465.tonto	<b>286</b>	<b>34.4</b>	286	34.4	292	33.8	223	44.1	223	44.2	<b>223</b>	<b>44.1</b>
470.lbm	35.0	392	42.4	324	<b>41.6</b>	<b>330</b>	35.0	392	42.4	324	<b>41.6</b>	<b>330</b>
481.wrf	<b>163</b>	<b>68.5</b>	164	68.3	162	69.0	<b>163</b>	<b>68.5</b>	164	68.3	162	69.0
482.sphinx3	281	69.4	283	68.8	<b>282</b>	<b>69.1</b>	<b>281</b>	<b>69.4</b>	281	69.4	277	70.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Virtualization Technology disabled  
Execute Disable disabled  
Logical Processor disabled  
System Profile set to Performance  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on linux Fri Nov 1 19:14:19 2013

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
    2 "physical id"s (chips)
    20 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 10
    siblings  : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      198443260 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 1 13:15 last=S

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  910G  9.4G  899G   2% /

```

```

Additional information from dmidecode:
BIOS Dell Inc. 2.0.22 09/23/2013
Memory:
12x 00AD04B300AD HMT42GR7AFR4A-PB 16 GB 1600 MHz

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "20"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

Continued on next page



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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>

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Page 6



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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