



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge M420 (Intel Xeon E5-2407 v2, 2.40 GHz)

SPECfp®2006 = **61.7**

SPECfp_base2006 = **59.9**

CPU2006 license: 55

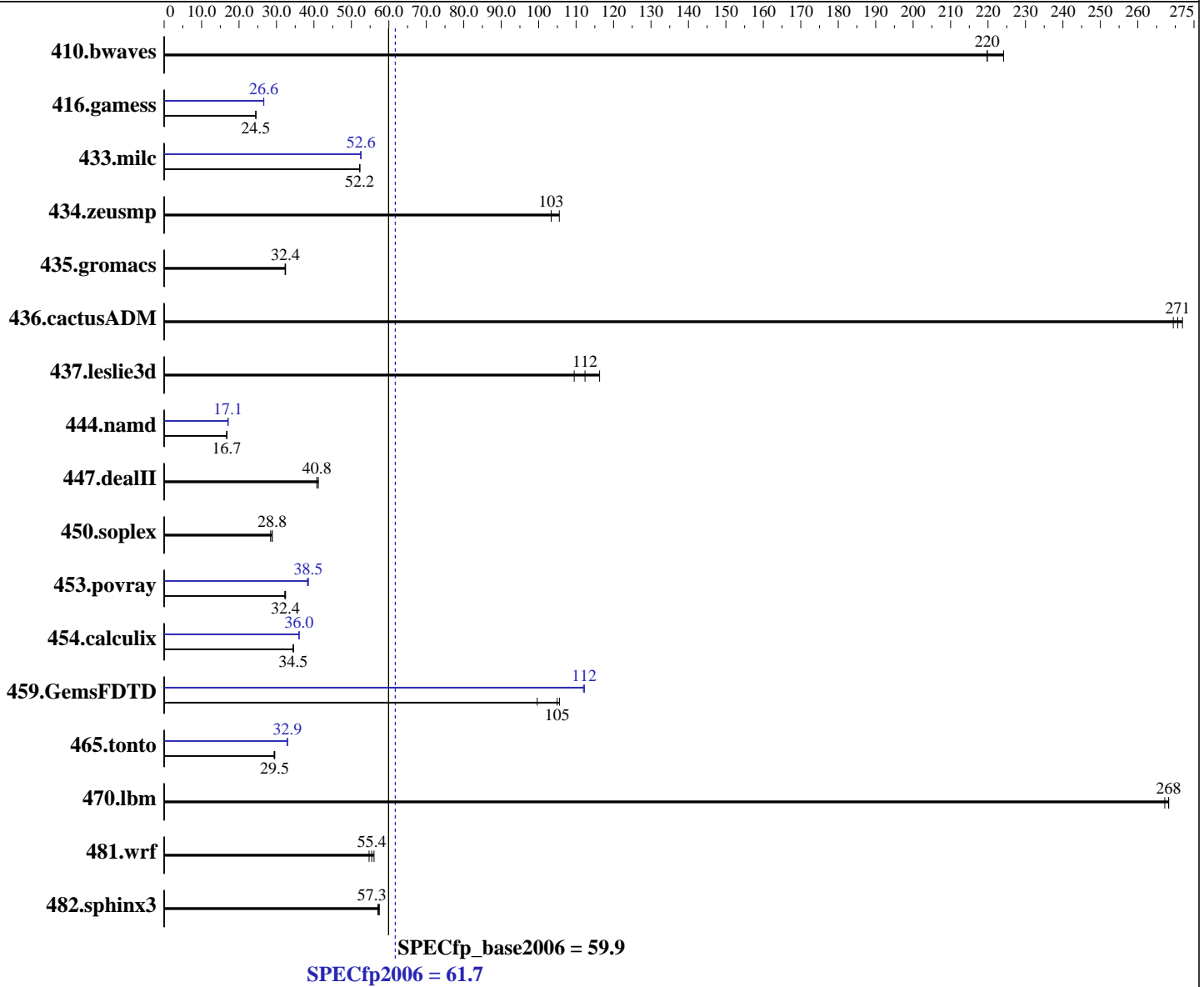
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Nov-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013



| Hardware | |
|----------------------|------------------------------------|
| CPU Name: | Intel Xeon E5-2407 v2 |
| CPU Characteristics: | |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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| Software | |
|-------------------|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) 3.0.70-0.9-default |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext2 |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (6 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem: 2 x 50 GB SATA SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 61.8 | 220 | 61.8 | 220 | 60.6 | 224 | 61.8 | 220 | 61.8 | 220 | 60.6 | 224 |
| 416.gamess | 799 | 24.5 | 800 | 24.5 | 799 | 24.5 | 737 | 26.6 | 735 | 26.6 | 736 | 26.6 |
| 433.milc | 176 | 52.3 | 176 | 52.2 | 176 | 52.2 | 175 | 52.6 | 175 | 52.5 | 175 | 52.6 |
| 434.zeusmp | 88.0 | 103 | 88.0 | 103 | 86.2 | 106 | 88.0 | 103 | 88.0 | 103 | 86.2 | 106 |
| 435.gromacs | 221 | 32.4 | 221 | 32.3 | 220 | 32.4 | 221 | 32.4 | 221 | 32.3 | 220 | 32.4 |
| 436.cactusADM | 43.9 | 272 | 44.3 | 269 | 44.1 | 271 | 43.9 | 272 | 44.3 | 269 | 44.1 | 271 |
| 437.leslie3d | 83.6 | 112 | 85.8 | 109 | 80.8 | 116 | 83.6 | 112 | 85.8 | 109 | 80.8 | 116 |
| 444.namd | 480 | 16.7 | 479 | 16.7 | 480 | 16.7 | 470 | 17.1 | 470 | 17.1 | 470 | 17.1 |
| 447.dealII | 278 | 41.2 | 280 | 40.8 | 280 | 40.8 | 278 | 41.2 | 280 | 40.8 | 280 | 40.8 |
| 450.soplex | 289 | 28.9 | 294 | 28.4 | 289 | 28.8 | 289 | 28.9 | 294 | 28.4 | 289 | 28.8 |
| 453.povray | 164 | 32.4 | 164 | 32.4 | 165 | 32.2 | 139 | 38.3 | 138 | 38.5 | 138 | 38.5 |
| 454.calculix | 239 | 34.5 | 240 | 34.4 | 239 | 34.6 | 229 | 36.0 | 229 | 36.0 | 229 | 36.0 |
| 459.GemsFDTD | 101 | 105 | 107 | 99.6 | 101 | 106 | 94.6 | 112 | 94.6 | 112 | 94.8 | 112 |
| 465.tonto | 333 | 29.6 | 334 | 29.5 | 335 | 29.4 | 299 | 32.9 | 299 | 32.9 | 299 | 32.9 |
| 470.lbm | 51.4 | 267 | 51.2 | 268 | 51.2 | 268 | 51.4 | 267 | 51.2 | 268 | 51.2 | 268 |
| 481.wrf | 201 | 55.4 | 199 | 56.0 | 204 | 54.7 | 201 | 55.4 | 199 | 56.0 | 204 | 54.7 |
| 482.sphinx3 | 339 | 57.5 | 342 | 57.1 | 340 | 57.3 | 339 | 57.5 | 342 | 57.1 | 340 | 57.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
Execute Disable disabled
Logical Processor disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on linux Mon Nov 25 20:21:29 2013

This section contains SUT (System Under Test) info as seen by
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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2407 v2 @ 2.40GHz

2 "physical id"s (chips)

8 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

physical 0: cores 0 1 2 3

physical 1: cores 0 1 2 3

cache size : 10240 KB

From /proc/meminfo

MemTotal: 99123324 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 11 (x86_64)

VERSION = 11

PATCHLEVEL = 3

uname -a:

Linux linux 3.0.70-0.9-default #1 SMP Tue Apr 2 13:01:38 UTC 2013 (25ae5d2)

x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 25 12:11 last=S

SPEC is set to: /root/cpu2006-1.2

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda2 | ext2 | 83G | 9.4G | 73G | 12% | / |

Additional information from dmidecode:

BIOS Dell Inc. 2.0.22 09/23/2013

Memory:

6x 00CE04B300CE M393B2G70BH0-YK0 16 GB 1333 MHz

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "8"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 20:54:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 January 2014.