



# SPEC® CFP2006 Result

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Dell Inc.

**SPECfp®2006 = 44.6**

PowerEdge R320 (Intel Xeon E5-2403 v2, 1.80 GHz)

**SPECfp\_base2006 = 43.7**

CPU2006 license: 55

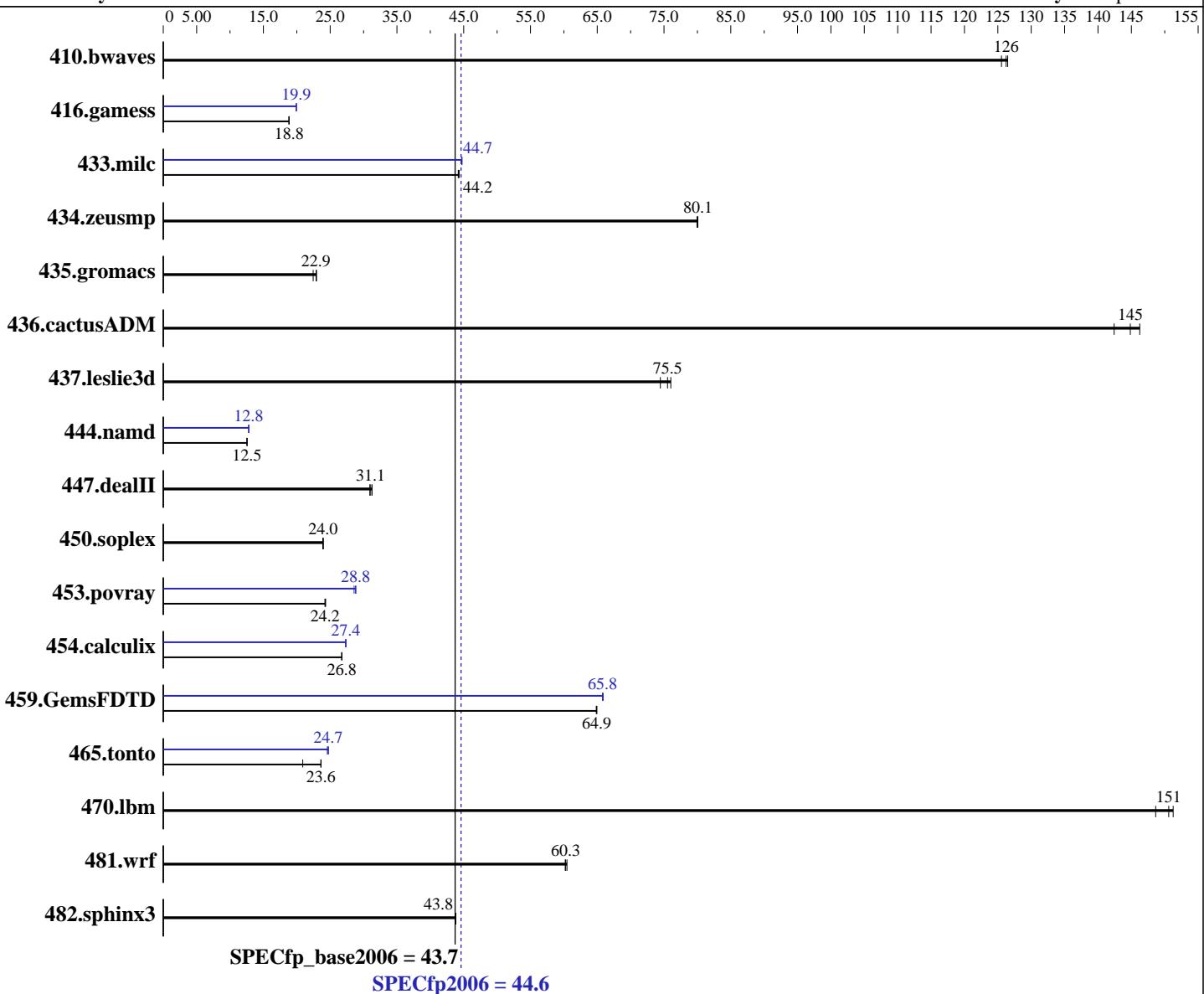
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Oct-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013



## Hardware

CPU Name: Intel Xeon E5-2403 v2  
 CPU Characteristics:  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE  
 for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran  
 Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (6 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio								
410.bwaves	108	126	<b>108</b>	<b>126</b>	107	126	108	126	<b>108</b>	<b>126</b>	107	126
416.gamess	1040	18.8	1039	18.8	<b>1039</b>	<b>18.8</b>	<b>982</b>	<b>19.9</b>	982	19.9	983	19.9
433.milc	207	44.3	<b>208</b>	<b>44.2</b>	208	44.2	<b>205</b>	<b>44.7</b>	205	44.7	205	44.7
434.zeusmp	114	80.1	114	79.9	<b>114</b>	<b>80.1</b>	114	80.1	114	79.9	<b>114</b>	<b>80.1</b>
435.gromacs	318	22.5	<b>312</b>	<b>22.9</b>	311	23.0	318	22.5	<b>312</b>	<b>22.9</b>	311	23.0
436.cactusADM	<b>82.5</b>	<b>145</b>	81.7	146	83.9	142	<b>82.5</b>	<b>145</b>	81.7	146	83.9	142
437.leslie3d	<b>124</b>	<b>75.5</b>	124	76.0	126	74.5	<b>124</b>	<b>75.5</b>	124	76.0	126	74.5
444.namd	639	12.5	<b>639</b>	<b>12.5</b>	639	12.5	<b>627</b>	<b>12.8</b>	626	12.8	627	12.8
447.dealII	370	30.9	366	31.3	<b>368</b>	<b>31.1</b>	370	30.9	366	31.3	<b>368</b>	<b>31.1</b>
450.soplex	348	24.0	<b>348</b>	<b>24.0</b>	349	23.9	348	24.0	<b>348</b>	<b>24.0</b>	349	23.9
453.povray	220	24.2	219	24.3	<b>219</b>	<b>24.2</b>	186	28.6	184	28.9	<b>185</b>	<b>28.8</b>
454.calculix	308	26.8	<b>308</b>	<b>26.8</b>	309	26.7	302	27.4	302	27.3	<b>302</b>	<b>27.4</b>
459.GemsFDTD	164	64.8	<b>163</b>	<b>64.9</b>	163	64.9	<b>161</b>	<b>65.8</b>	161	65.9	161	65.8
465.tonto	<b>417</b>	<b>23.6</b>	416	23.6	471	20.9	<b>398</b>	<b>24.7</b>	400	24.6	398	24.7
470.lbm	92.4	149	<b>91.2</b>	<b>151</b>	90.8	151	92.4	149	<b>91.2</b>	<b>151</b>	90.8	151
481.wrf	186	60.2	<b>185</b>	<b>60.3</b>	185	60.5	186	60.2	<b>185</b>	<b>60.3</b>	185	60.5
482.sphinx3	445	43.8	<b>445</b>	<b>43.8</b>	445	43.8	445	43.8	<b>445</b>	<b>43.8</b>	445	43.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:

Virtualization Technology disabled

Execute Disable disabled

Logical Processor disabled

System Profile set to Performance

Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on linux Thu Oct 31 21:15:01 2013

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2403 v2 @ 1.80GHz
        1 "physical id"s (chips)
        4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 4
    physical 0: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      99123704 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 31 11:04 last=S
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2  267G  9.7G  256G  4%  /
```

```
Additional information from dmidecode:
```

```
BIOS Dell Inc. 2.0.21 09/23/2013
Memory:
6x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1333 MHz
```

```
(End of data from sysinfo program)
```



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "4"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
           -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
           -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
           -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
           -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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